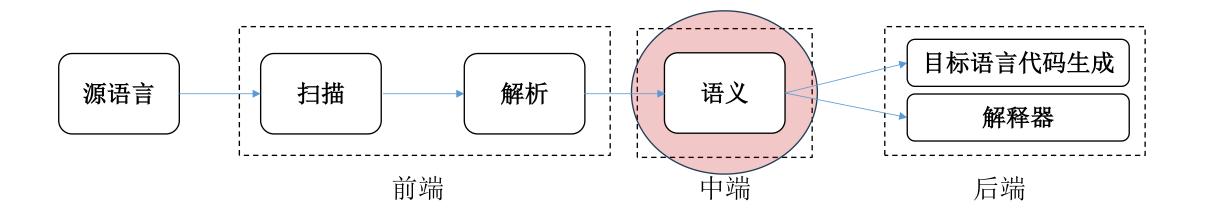


# 中间语言IR

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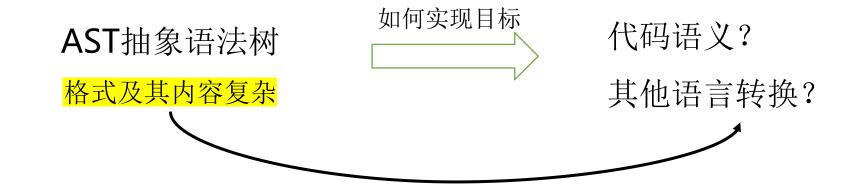
# 中端

- 抽象语法树
- 基本类型检查
- ・中间语言
- · SSA静态单一赋值
- 控制流
- 数据流
- 优化









中间语言: Intermediate Representation/ IR





- 什么是中间语言?
  - 由指令和地址做成,变量名、常量、编译器生成的临时变量或存储单元
  - 对代码的中间表示
  - 起到一个承上启下的作用:上-源代码AST,下-
- 中间语言规范代码内容
  - 三地址
    - 只有三个操作数 left = right1 < op> right2

Opcode	Destination	Source 1	Source 2



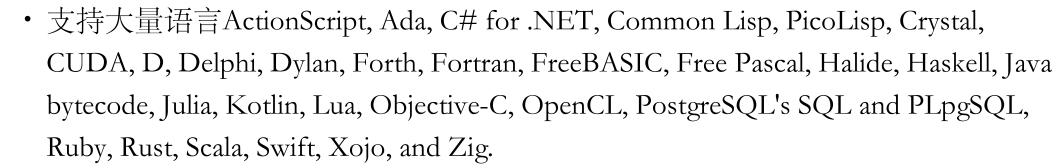
- 知名中间表示
  - <mark>llvm</mark>
  - Web assembly
  - Dalvik bytecode
  - MLIR
- · 自研语言GLang





#### 中间语言LLVM

- 诞生于UIUC 2000-2003年
- 事实标准中间语言





Vikram Adve



**LLVM** 

Chris Lattner







#### 面向底层逻辑的中间语言典范LLVM





**LLVM** 

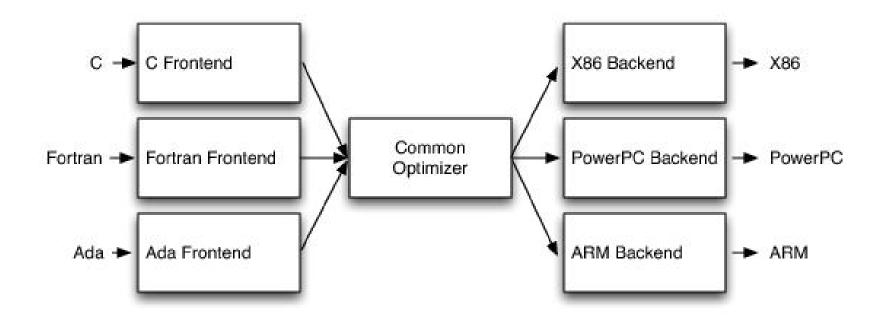
LLVM is a set of compiler and toolchain technologies that can be used to develop a frontend for any programming language and a backend for any instruction set architecture.

- wikipedia



• LLVM架构







```
unsigned add1(unsigned a, unsigned b) {
  return a+b;
}

// Perhaps not the most efficient way to add two numbers.
unsigned add2(unsigned a, unsigned b) {
  if (a == 0) return b;
  return add2(a-1, b+1);
}
```



```
define i32 @add1(i32 %a, i32 %b) {
entry:
  %tmp1 = add i32 %a, %b
  ret i32 %tmp1
define i32 @add2(i32 %a, i32 %b) {
entry:
  %tmp1 = icmp eq i32 %a, 0
  br i1 %tmp1, label %done, label %recurse
recurse:
  %tmp2 = sub i32 %a, 1
  %tmp3 = add i32 %b, 1
  %tmp4 = call i32 @add2(i32 %tmp2, i32 %tmp3)
  ret i32 %tmp4
done:
  ret i32 %b
```



#### 符号系统:

- % 临时符号 如%1, %2
- @全局符号 如@add1



#### IR定义:标识符、基础类型、和数据存取

- 全局变量/函数名称: @name
- · 局部变量/临时变量: %x、%0(不可重复定义, 纯数字编号需连续)
- 类型: void、i32、i32\*、i8、i8\*、i1
- · 栈空间分配: alloca
- 数据存取: load/store

```
→ 声明全局变量g, 类型*i32, 初始值10
ag = global i32 10
                                  → 声明函数fib,参数名为%0,类型i32
define i32 (a) fib(i32 %0) {
 %x = alloca i32
                                   → 声明局部变量%x,类型为i32*
 store i32 %0, %x
 \frac{\%g0}{} = load i32, i32* @g
                                   → 声明临时变量%g0,类型为i32
 ret i32 %g0
define i32 (a)main() {
 %r0 = call i32 @fib(i32 1)
 ret i32 %r0;
```





#### IR定义:函数

• 声明: define

• 调用: call

• 返回: ret

```
@g = global i32 10
                                      ▶声明函数fib,参数类型i32
define i32 @fib(i32 %0) {
 %x = alloca i32
 store i32 %0, %x
 %g0 = load i32, i32* @g
 ret i32 %g0
define i32 @main() {
                                      ▶ 声明函数main
%r0 = call i32 @fib(i32 1)
                                      → 调用函数fib
ret i32 %r0;
```





### IR定义:复合类型数据定义和存取



#### IR定义: 算数运算

```
%2 = alloca i32

%3 = add i32 %0, 1

%4 = sub i32 %3, 2

%5 = mul i32 %3, 3

%6 = sdiv i32 %4, 4

store i32 %6, i32* %2

%7 = load i32, i32* %2

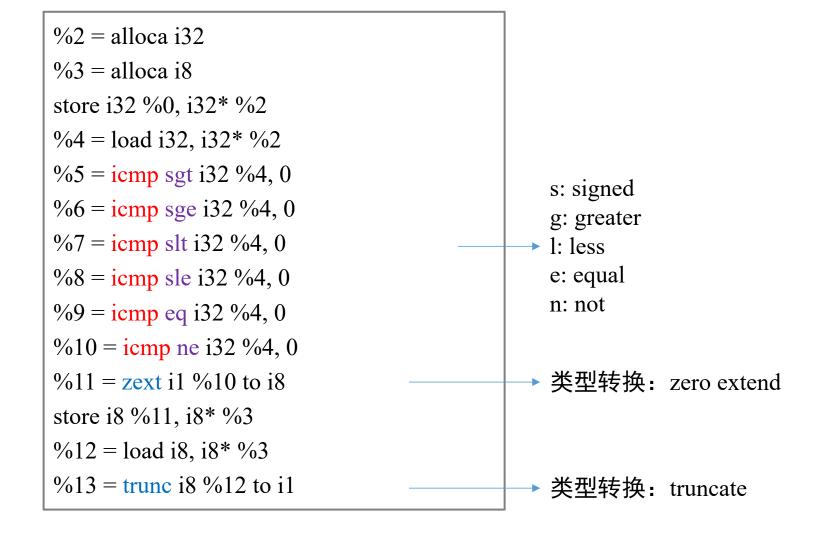
ret i32 %6
```

浮点数运算用 fadd/fsub/fmul/fdiv





#### IR定义:比较运算和类型转换







# IR定义: 跳转语句

· 基于br实现if-else和while等控制流功能



```
%2 = alloca i32
 store i32 0, i32* %2
%3 = load i32, i32* %2
%4 = icmp sgt i32 %3, 0
 br i1 %4, label %bb1, label %bb2
                                                      ▶ 条件跳转
bb1:
store i32 1, i32* %2
                                                      ▶ 直接跳转
 br label %bb3
bb2:
store i32 0, i32* %2
 br label %bb3
bb3:
                                                     如前序代码块为%bb1,则%8=0,
%r0 = phi i32 [ 0, %bb1 ], [ %3, %bb2 ]
                                                     如前序代码块为%bb2,则
ret i32 %r0
                                                     %8=%3
                                                                           CODE SECURITY
                                                                           RESEARCH
```

### IR定义:逻辑运算

• 无需定义专门的逻辑"与"和"非"指令



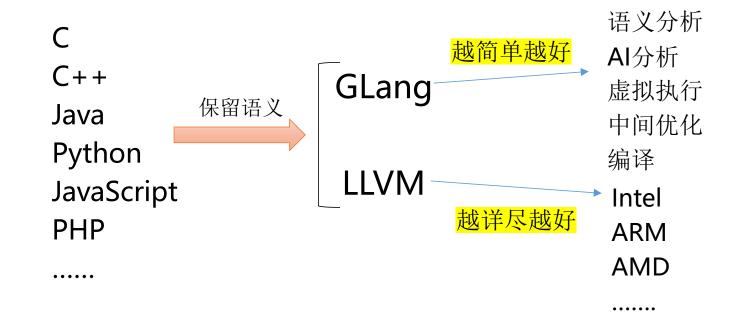


# 面向语义逻辑的中间语言General Language / GLang



• GLang IR

• 理念:保留最少的符号信息





### 例子: python



```
def condition():
    a = "hello"
    b = a + " world"
    if len(a) > 0:
        x = b
    elif 3 != 4:
        x = 5
    else:
        x = 7
    return x
```

```
[{'method decl': {'name': 'condition',
            'body': [
                  {'variable decl': {'name': 'a'}},
                   {'assign stmt': {'target': 'a', 'operand': "'hello"'}},
                   {'assign stmt': {'target': '\%v0', 'operator': '+', 'operand': 'a', 'operand2': "' world"'}},
                   {'variable decl': {'name': 'b'}},
                   {'assign stmt': {'target': 'b', 'operand': '%v0'}},
                   {'call stmt': {'target': '%v1', 'name': 'len', 'args': ['a']}},
                   {'assign stmt': {'target': '%v2', 'operator': '>', 'operand': '%v1', 'operand2': '0'}},
                   {'if stmt': {'condition': '%v2',
                           'then body': [
                                      {'variable decl': {'name': 'x'}},
                                      {'assign stmt': {'target': 'x', 'operand': 'b'}}],
                           'else body': [
                                     {'assign stmt': {'target': '%v3', 'operator': '!=', 'operand': '3', 'operand2': '4'}},
                                     {'if stmt': {'condition': '%v3',
                                              'then body': [{'variable decl': {'name': 'x'}},
                                                        {'assign stmt': {'target': 'x', 'operand': '5'}}],
                                              'else body': [{'variable decl': {'name': 'x'}},
                                                        {'assign stmt': {'target': 'x', 'operand': '7'}}]}}]}},
                   {'return stmt': {'target': 'x'}}]}}]
```



#### 例子



operation	parent_stmt_id	stmt_id	name	attrs	unit_id	parameters	body	receiver_object	field	source	target	data_type	args	operand
variable_decl	0	10	aa	['global']	4									
variable_decl	0	11	dd	['global']	4									
method_decl	0	12	hh		4	13.0	15.0							
block_start	12	13			4									
parameter_decl	13	14	name		4									
block_end	12	13			4									
block_start	12	15			4									
field_write	15	16			4			%this	name	name				
block_end	12	15			4									
method_decl	0	17	bb		4	18.0	20.0							
block_start	17	18			4									
parameter_decl	18	19	n		4									
block_end	17	18			4									
block_start	17	20			4									
field_write	20	21			4			%this	name	n				
block_end	17	20			4									
variable_decl	0	29	cc	['const']	4									
method_decl	0	31	%unit_init		4		32.0							
block_start	31	32			4									
field_read	32	22			4			hh	prototype		%vv1			
field_write	32	23			4			%vv1	sayhello	bb				
field_read	32	24			4			hh	prototype		aa			
field_read	32	26			4			hh	sayhello		dd			
new_object	32	28			4						%vv4	hh		
assign_stmt	32	30			4						cc			%vv4
block_end	31	32			4									



# GLang

控制流语句

return_stmt	target					TUNIVES
if_stmt	condition	then_body	else_body			S S S
dowhile_stmt	condition	body				
while_stmt	condition	body	else_body			
for_stmt	init_body	condition	condition_prebody	update_body	body	
forin_stmt	attr	data_type	name	target	body	— (90°)
switch_stmt	condition	body				
case_stmt	condition	body				
default_stmt	body					
break_stmt	target					
continue_stmt	target					
goto_stmt	target					
yield_stmt	target					
sync_stmt						
throw_stmt	target					
try_stmt	body	catch_body	else_body	final_body		
catch_stmt	body					
final_stmt	body					
label_stmt	name					
assert_stmt	condition					
del_stmt	target					
raise_stmt	target					
pass_stmt						
break_stmt	target					
global_stmt	target					
nonlocal_stmt	target					
type_alias_stmt	target	source				
with_stmt	attr	with_init	body			
block_start	stmt_id	parent_stmt_id				_
block_end	stmt_id	parent_stmt_id				ECURITY
new_array	target	attr	data_type			RCH

#### 符号

- · 临时变量%+vv数字
- attrs:
  - · 一条语句中的修饰符,例如private\public







如何把AST翻译为Glang?



```
AST转IR: 自顶向下翻译
                                                                                         int foo(int n){
                                                                                            while (n>0) {
                                                                                                      int x;
                                                                                               n = n-1;
                                  FnDef
                  FnDecl
                                                  codeBlockStmtList
                                                                                            return x;
                 paramDecl
        id
                                type
       foo
                 varDeclList
                varDeclScalar
                                                     whileStmt
                                                                                                        returnStmt
                          type
                           int
                                                  codeBlockStmtList
                                                                                                         rightVal
                                                                                                         arithExpr
              boolExpr
                                       varDeclStmt
                                                                    assignStmt
                                                                                                         exprUnit
              boolUnit
                                        varDecl
                                                                              rightVal
                                                                                                            id
                                                           leftVal
                                                                                                            X
                                      varDeclScalar
              comExpr
                                                                              arithExpr
                                    id
                                                              id
         exprUnit
                    exprUnit
                                                 type
                                                                            arithBiOpExpr
                                                 int
                                    X
           id
                      num
                                                                     arithExpr
                                                                                     arithExpr
                                                                     exprUnit
                                                                                     exprUnit
```

id

X

num





#### AST转IR: 自顶向下翻译

- 详见例子common\_parser和java\_parser
  - Common\_parser负责分发
  - · Java\_parser负责处理每一条具体语句





# 难点

- Target, error = f()?
- a, b = 1?
- Lambda x: x+1?
- O.f.f.f = 10
- Def f(): return a, b, c, d



