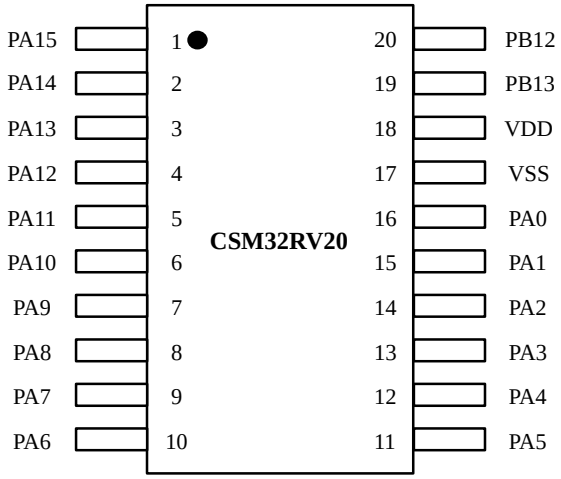
CSM32RV20 TSSOP20 pinout



Pin	Port	I/O	Alternate functions	Extra features
1	PA15	I/O	TIM2_CH4N / TX4 / EXTI[15]	RF detection
2	PA14	I/O	ADC_TRI / TIM2_CH4 / RX4 / EXTI[14]	-
3	PA13	I/O	TIM2_CH3N / EXTI[13]	REFP output voltage
4	PA12	I/O	TIM2_CH3 / EXTI[12]	PGA input
5	PA11	I/O	TIM2_BKIN / TIM2_CH2N / TX3 / EXTI[11]	REFN output voltage
6	PA10	I/O	TIM1_BKIN / TIM2_CH2 / RX3 / EXTI[10]	ADC_IN9
7	PA9	I/O	TIM1_CH1 / TX / TIM2_CH1N / EXTI[9]	ADC_IN8
8	PA8	I/O	SDA / RX / TIM2_CH1 / EXTI[8]	ADC_IN7
9	PA7	I/O	SCL / MOSI / TIM1_CH4N / EXTI[7]	ADC_IN6
10	PA6	I/O	TX1 / MISO / TIM1_CH4 / EXTI[6]	ADC_IN5
11	PA5	I/O	RX1 / SCK / TIM1_CH3N / EXTI[5]	ADC_IN4
12	PA4	I/O	MOSI / TIM1_CH1IN / TIM1_CH3 / TX2 / EXTI[4]	ADC_IN3
13	PA3	I/O	MISO / TIM1_CH1N / TIM1_CH2N / RX2 / EXTI[3]	ADC_IN2
14	PA2	I/O	SCK / TIM1_CH1 / TIM1_CH2 / EXTI[2]	-
15	PA1	I/O	TMSC / SDA / TIM1_CH1N / EXTI[1]	-
16	PA0	I/O	TCKC / SCL / TIM1_CH1 / EXTI[0]	-
17	VSS	S	Ground	-
18	VDD	S	Power supply	-
19	PB13	O	-	OSC_OUT
20	PB12	I	-	OSC_IN

S: power supply pin; I: input; O: output; I/O: input/output;

GPIO	TIM1	TIM2	UART	I2C	SPI	ADC
PA15		TIM2_CH4N	TX4			
PA14		TIM2_CH4	RX4			
PA13		TIM2_CH3N				
PA12		TIM2_CH3				PGA_IN
PA11		TIM2_BKIN TIM2_CH2N	TX3			
PA10	TIM1_BKIN	TIM2_CH2	RX3			ADC_IN9
PA9	TIM1_CH1	TIM2_CH1N	TX			ADC_IN8
PA8		TIM2_CH1	RX	SDA		ADC_IN7
PA7	TIM1_CH4N			SCL	MOSI	ADC_IN6
PA6	TIM1_CH4		TX1		MISO	ADC_IN5
PA5	TIM1_CH3N		RX1		SCK	ADC_IN4
PA4	TIM1_CH1IN TIM1_CH3		TX2		MOSI	ADC_IN3
PA3	TIM1_CH1N TIM1_CH2N		RX2		MISO	ADC_IN2
PA2	TIM1_CH1 TIM1_CH2				SCK	
PA1 *	TIM1_CH1N			SDA		
PA0 *	TIM1_CH1			SCL		

*: keep in mind PA1 is also used for TMSC and PA0 for TCKC.