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# Autotuning Parallel Application in Heterogeneous Systems

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# Abstract

Nowadays computational platforms have been evolving to the high computational power direction, however it requires a lot of energy to achieve such high performance with single but powerful processing unit. To manage this energy cost and keep with high performance, computers are built under the assumption of heterogeneous systems, in other words, computers that have different kind of processing units with different functions, such as CPU, GPU, Xeon Phi and FPGA. So, developers should take advantage of parallel activity and scheduling tasks by using the various parts of the heterogeneous systems.

Now the problem is how to efficiently achieve the highest performance possible when running software applications by taking the most advantage of such heterogeneous systems without jeopardizing the application performance and its results. Overall, the problem consists in the co-existence work of multicore specs, its parallelism and its shared cache problems; CPU parallelism and scheduling tasks; performance.

For this problem's solution is expected to find patterns and variables that helps tuning application with the best performance main goal in mind. Detecting these patterns and variables that improve applications, by making them parallelized, helps in the progression of how to make automatic paralleled code, since now-a-days making parallel code requires a lot of effort and time.

This kind of solution requires some validation process and metrics to make sure that it is doing its work and with proper results. To do so, the idea of the process' validation is going to be about comparing the behaviour of three different codes: a version of a serialized code; a version of the same code but with an expert manually paralleling it; and a version of the serialized code but «automatically» parallelized by a tool called Kremlin. The metric that was used to compare these three code versions is execution time in different experience environment.

The application of this work will help developing better automatic tools to make parallel code, which means in a long term, developers will have less burdened about creating parallel code, consequently, saving them time.

applications will achieve its highest performance possible in an automatic way and developers will have less burdened about creating parallel code, consequently, saving them time.



# Resumo

Atualmente as plataformas computacionais têm vindo a evoluir na direção do elevado poder computacional, no entanto, estas requerem uma quantidade enorme de energia para atingir elevado desempenho individualmente. De modo a gerir este custo energético e manter a elevada performance, os computadores são construídos sobre a assunção de sistemas heterogéneos, isto é, computadores compostos por diferentes tipos de unidades de processamentos com diferentes funcionalidades, como por exemplo, CPU, GPU, Xeon Phi e FPGA. É neste sentido que os programadores devem tirar proveito de atividade paralela e escalonamento de tarefas recorrendo às várias partes que compõem o sistema heterogéneo.

O problema incide sobre como atingir de forma eficiente o maior desempenho possível quando se corre uma aplicação de software, tirando o maior proveito dos sistemas heterogéneos sem prejudicar o resultado e o desempenho da aplicação.

Para solucionar este problema é esperado encontrar/padrões e variáveis que ajudem a afinar aplicações com o principal objetivo de atingir a melhor performance em mente. Detetar estes padrões e variáveis que melhoram aplicações, colocando-as paralelas, ajuda no avanço de como fazer código paralelelo automaticamente, uma vez que nos dias de hoje fazer código paralelo requer muito tempo e esforço.

Este tipo de solução requer um processo de validação e métricas para assegurar que se está a fazer o trabalho corretamente e com resultados aceitáveis. Para tal, a ideia da validação do processo consiste em comparar o comportamento de três diferentes códigos: uma versão sequencial de um código; a versão deste mesmo código mas paralelizada por um perito; e a versão do código sequencial mas paralelizado «automaticamente» por uma ferramenta denominada de Kremlin. A métrica que foi utilizada para comprar estas três versões de código é o tempo de execução em diferentes ambientes experimentais

A aplicação deste trabalho irá ajudar a desenvolver melhores ferramentas para fazer código paralelo automático, significando que, a longo prazo, programadores estarão menos sobrecarregados a criarem código paralelo o que, consequentemente, poupar-lhes-á tempo.





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# Abbreviations

CPU	Central Processing Unit
GPU	Graphic Processing Unit
FPGA	Field-Programmable Gate Array
OpenCL	Open Computing Language
CHC	Cooperative Heterogeneous Computing
OpenMP	Open Multi-Processing
WWW	<i>World Wide Web</i>





# Chapter 1

## 2 Introduction

### 4 1.1 Context

Previously, computer systems were built to maximize their processing power in compactness and  
6 individually because programs were developed with a sequential approach. With the advance  
in microchips' technology, computers increased their processing capacity per volume, however  
8 some issues arose, such as high energy cost, high temperature and low equipment durability. To  
solve these issues some measures needed to take place in order to make computers systems more  
10 reliable, durable, efficient, and powerful.

Recently, the computing industry has moved away from exponential scaling of clock frequency  
12 toward chip multiprocessors in order to better manage trade-offs among performance, energy effi-  
ciency, and reliability [?]

14 Combining different computer processing components, such as CPU, GPU Xeon Phi and  
FPGA, in a single computer system removed some heavy burden in the main processing core,  
16 making the computer system with better performance and reliable. However some concerns arose:  
how to properly use these components without jeopardizing the computer system and application  
18 performance. Some processing components can handle specif jobs better then others and com-  
bined the computer can achieve a whole new performance level; for instance, the use of a GPU  
20 together with a CPU to accelerate deep learning algorithm, analytics, and engineering applica-  
tions [?], however this kind of utility is not yet well optimized and its utility is only recently  
22 emerging.

- mencionar multiplicação de matrizes, -dois algoritmos ligeiramente diferentes para a multi-  
24 plicação

## 1.2 Motivation and Goal

My motivation for this thesis is to advance a little further on the field of the automatic code parallelization and replace the manual parallelization labor because it requires a lot of time and effort to achieve significant performance. 2 4

## 1.3 Structure of the Report

This report is divided in three more chapters. The next one is called *Achieving the Highest Processing Power*, and it is related to the state of the art of my thesis' scope. In this chapter there are three sections. The first section is related to the context of the state of the art in the field. The other two sections are two different but complementary approaches which help and describe the state of the art. 6 8 10

The third chapter addresses the problem involved in my thesis and how I propose to solve it, including the approach, the methodology and solution's validation. The last chapter includes final consideration related to the work developed so far in *Preparação da Dissertação* course, expected results with my proposed solution and work plan to develop the solution. In the end of this report there are the references used to develop this report. 12 14

## Chapter 2

## 2 Methodology

### 2.1 Introduction

According to the state of the art presented in chapter two, there are many means to, in some kind of automatic way, improve an applications performance. During my research, my focus was to find ways to automatically enhance the execution time in applications and programs. For this propose, Kremlin had a crucial impact in other to understand the viability of automatically parallelize code.

To study the utility and impact of automatic tools, the matrix multiplication algorithm will be used as a reference to make the performance comparison between original algorithm, an expert manually parallelizing the original algorithm and using the Kremlin's indications to parallelize the original algorithm.

To increase the credibility of this experiment, two similar algorithms for the matrix multiplication were used. As mentioned and explained in the chapter two, there is the traditional way of multiplying square matrices, naming as a quick reference *Mult* algorithm, see in the appendix's list 5.1 this algorithm implementation, written in C++ programming language; and the optimized algorithm that multiplies each element from the first matrix with the correspondent line of this matrix element but for the second matrix, naming this algorithm as *MultLine*, see in the appendix's list 5.2 this algorithm implementation, written in C++ programming language. These algorithms differs from one another in the variables preparation and the order of the loops, which differs how the memory accessed. The *MultLine* algorithm is an optimized version for matrix multiplication because it takes advantages of what is preloaded in cache and starts pre-calculating the intermediate values that will lead to the final and correct result of the multiplication, which means that won't be needed to load unnecessary values to cache memory and/or will need afterwards.

Several experiments were conducted to understand the influence of Kremlin's indications versus code being manually parallelized by an expert. The data's length, in this case, the matrix size; the number of threads used and if the code was parallelized were the used metrics to evaluate the results, based on a comparison of the execution time.

In this chapter it is explained the methodology and the steps followed to report in the Results and Discussion chapter the results and conclusions obtained from the performed experiences. This chapter also includes detailed information of the acquired data from the conducted experiences, as in, how it is obtained and its meaning; also includes the methods that were used to analyse the obtained data and the reason behind those methods; and, in the end, how the data was validated in order to verify its correctness, accuracy and reliability.

## 2.2 Research Method

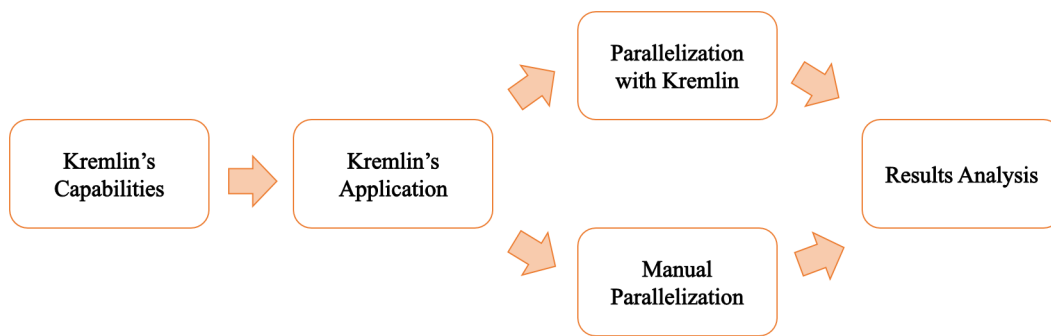


Figure 2.1: Followed up methodology

In Figure 2.1 is outlined the steps that were followed to study the impact of the code being automatically parallelized. This methodology has five states. Firstly and using simple applications, an evaluation was made for Kremlin's tool in order to understand how to use this software tool and evaluate the results that it can achieve, for instance, if it has similar results comparing with an expert parallelizing manually the same code. After this, Kremlin will be applied to a set of codes with specific characteristics.

Before applying Kremlin, I manually parallelized the same sample of code in order to evaluate the results and, afterwards, compare with the Kremlin's output. Since these states (the experiences with Kremlin and the manual code parallelization) required several attempts there were transitions between these states.

Finally, in the last state, after several attempts and tuning exercises applied to both code cases (Manual and Kremlin), data was collected from this experience to evaluate and validate its correctness in order to conclude how helpful can automatic parallelization can be.

To sum up, this methodology as three main stages: learn and evaluate Kremlin's uses and results; finding the tuning parameter through several attempts using Kremlin's outputs and manually parallelize the application's code; and, in the end, compare and analyse the results in every attempt to take conclusions;

### 2.2.1 Deep learn on Kremlin's usage

2 Firstly, and according to all tools/frameworks mentioned presented in the second chapter, *Achieving the Highest Processing Power*, in the *Using Code Parallelization* section ??, Kremlin was  
 4 chosen because it presented the best results, easy usage and accessibility comparing to the others ??.

6 Kremlin is a tool that indicates, for a sequential program, which block can be parallelised and some metrics theoretical calculated, such as, overall speedup; self parallelism for each block; the  
 8 ideal time reduced for each block, in percentage; the actual time reduced for each block, in percentage; and the block coverage, in percentage. The way this tool was used is as it follows: first,  
 10 an object file, \*.o extension, is required from the compilation of a sequential code. Afterwards, it is time to use the Kremlin's compiler with the generated object file so that it can profile the appli-  
 12 cation. In order to do so, Kremlin's compiler runs the program as it is supposed to work. Now that the profiling is done, Kremlin generates the indications that should be followed to parallelize de  
 14 provided sequential code. It also includes the blocks that can be parallelized and the impact of this theoretical parallelization with the calculations done during the profiling. Since this parallelization  
 16 report is done, the developer has to interpret it, confront with the code an apply.

The Kremlin's usage seems easy, linear and fast forward, however it has some limitations  
 18 that I experienced during the learning of Kremlin's capabilities: Kremlin's requires a specific environment mentioned in the Kremlin's repository [Kre]. It requires several software, libraries,  
 20 compilers installations and a modern Unix operative system as its bases, such as MAC OS, RHEL 7 or other Linux distribution compatible with the software specification required. Additionally,  
 22 when installing the Kremlin's tool, some minor fixes are required in order to successfully install.

From the experiences that I have been through, Kremlin has another limitation: it can not  
 24 compile and profile all kind of programs: it can only profile programs that use C/C++ as its programming language; programs that take advantage of data structures from the *Standard Library*,  
 26 such as, stack, list, priority queue, queue, list, hash table, map, multimap, heap, and rest, since it doesn't recognize these structures; another Kremlin's limitations is its capability of compiling  
 28 programs that have a deep function call level greater that seven. By deep function call level I mean the depth a function has starting from the *main()* function until it is called, like a functions  
 30 call tree. For instance: in a program there is the *main()* function, a first level, that calls a *foo1()* function, and this function calls a *foo2()*, that this calls a *foo3()* function, and so on. In this case,  
 32 the depth of *foo3()* function is four. Another small issue that Kremlin's tool has is the definition of the iterator variable used in the *for*'s loops must be defined outside of the loop, as it is in C  
 34 programming language.

### 2.2.2 Kremlin's application in specific code samples

36 After all the experiences made in the previous state and as mentioned in the introduction of this chapter, the matrix multiplication algorithm was used to see the potentialities of Kremlin's com-  
 38 piler to profile and identify the regions that can be parallelized. So, Kremlin was used in two

similar, relatively similar in code structure, matrix multiplication codes. The reason behind the choice was because these two versions of the algorithm are really close to one another, which means that the testing environment is similar to one another, consequently, the results should be similar.

### 2.2.3 Code parallelization with Kremlin's data

Kremlin's tool just points the regions/blocks where the program can be parallelized. In both code samples there are various numbers of inner *for* loops, for the *Mult* code there are three inner *for* loops, which one of them has a degree of three and the rest a degree of two; and for the *MultLine* code there are four inner *for* loops, which one of them has a degree of three and the rest a degree of two as well. At this time, after reading the report provided by Kremlin's tool, the developer must locate the loops, apply which loop should be parallelized, if it should be, and in case of inner *for* loops, what loop should be parallelized using the OpenMP *pragma* directives.

In my case, I followed all the instructions provided by Kremlin, located all the *for* loops blocks indicated by kremlin's tool and applied the OpenMP *pragma* directives.

Following the two reports, 5.8 5.9, and looking at the code's structure for both codes, it can be divided in 2 bigger parts: the *for* loops used for matrices initialization and the *for* loop for the matrix multiplication. With this information, understating the code and using an expert knowledge, the code parallelization was done.

### 2.2.4 Manually Code parallelization

In order to not be manipulated by the Kremlin's indications, the both codes were previously manually parallelized, this way it was guaranteed that the expert parallelization wasn't bias nor influenced.

For this parallelization, as mentioned before, it requires knowledge in, firstly, matrix multiplication algorithm; code understanding; best practice in what can and can not be parallelized, taking into account the overhead that could occur; and understand the thread behaviour in order to make it do the proper job without jeopardizing the programs outputs and/or performance.

Analysing the code, only the *for* loop for the matrix multiplication was parallelized and applied the OpenMP *pragma* directives to the innermost *for* loop. In this case, each code as a slight difference because for *Mult* code each value of the result matrix must be calculated individually, so each thread is responsible for it and must treat that value as a private variable that isn't shared by the other threads. In the opposite, and since the *MultLine* code calculates the values by adding the multiplication to the respective matrix's cell, each thread does not need to have their own private variable.

The bigger part of the code responsible for the matrices initialization wasn't parallelised, unlike in kremlin's case, because the gain would be noticeable on a large matrix size or it even could cause thread trampling, which could lead an overhead increase.

### 2.2.5 Results analysis

To obtain the final execution time of each implementation (Original Matrix Multiplication 5.1, Original Matrix Multiplication by line 5.2, Manual Matrix Multiplication by an expert 5.3, Manual Matrix Multiplication by line by an expert 5.4, Kremlin Matrix Multiplication ?? and Kremlin Matrix Multiplication by line 5.6), these six implementations suffered many modifications and tweaks since this process is a try-error until it is found the believed best parallelization. It is hardly possible to parallelize a whole program at the first try.

After compiling all these implementations and registering all the execution time for different matrix sizes and number of threads (not applied to the Original codes), this data was organized so it could be used to compare results and conclude about the performed experiences.

## 2.3 Data collection from executed experiences

From all the developed work, the obtained data can be divided in two moments: Kremlin's indications reports and the execution times for the six code variations.

### 2.3.1 Kremlin's indications reports

For the *Mult* 5.8 and *MultLine* 5.9 codes were generated a report done by Kremlin. This report displays for each parallelizable block:

**Time reduced** percentage of time reduced if parallelization is implemented;

**Ideal Time reduced** percentage of ideal time reduced if parallelization is implemented;

**Coverage** percentage of sequential execution time in a block;

**Self Parallelism** amount of parallelism in a block;

**Parallelism type** classification of the parallelizable block;

**Loop location block** lines range of the parallelizable block;

**Function location** function name of the parallelizable block, mentioning the line where the function is defined;

**File location** file name of the parallelizable block, mentioning the line where the function is called;

The guidelines given by the report must be followed by the order it is suggested because the first detected block has the biggest impact in programs performance and should be parallelized first. The Coverage and Self Parallelism are metrics that indicates the speedup of the block, which, consequently, interferes with the time reduced. This block speedup must be equal or less then the overall program speedup, based on Amdahl's law, and can be calculated as it follows [GJ]:

$$speedup \leq \frac{1}{(1 - Coverage) - \frac{Coverage}{SelfParallelism}} \quad (2.1)$$

### 2.3.2 Execution times for the code variations

After running the six implementations, the results can be divided in three major groups and each group has got the respective implementations of the *Mult* and *MultLine* algorithms. Each group has its own experimental environment, with their own variables and their own meaning according to the given context.

#### 2.3.2.1 Original code

The Original code only variable is the matrix size. This group is a reference group to compare the results of the others groups and to quantify the impact of the others groups. The results of this group are the execution times running both matrix multiplications algorithm versions.

#### 2.3.2.2 Manual code parallelization

The Manual code parallelization by an expert has as variables the matrix size and number of threads used for each run. This group's results are the execution times for both matrix multiplication algorithm versions. These results were used to confront with the following group in order to evaluate the improvement that a guided parallelizations, using Kremlin, can for an application.

#### 2.3.2.3 Kremlin's code parallelization

Like the previous group, Kremlin's code parallelization indications use the same variables and provide the same type of the results. However, this group is responsible to define if it is advantageous to use software tools to help with code parallelization, in an automatic way.

## 2.4 Data analysis method

Analysing data is a very important stage because it is necessary to have correct conclusions. From the experiments made a lot of data as been generated and without a proper organization it is hard to understand the meaning, therefore, hard to take good conclusions from its analysis. There are three groups of code and in each group two different algorithms implementation. In order to make a correct analysis from the execution times for each situation and comparing with the others cases, the collected results were stored in tables along with the experimental related variables. Additionally, calculations were required, such as, difference between executed times between groups and algorithm implementation; ratio between these executed times; the percentage of the increase/decrease for these executed times; and the impact in the execution time that the others two groups have comparing with the Original code group. After these data manipulation, the best way to analyse all this generated and calculated data is by a dispersion plot. Using a dispersion plot,



it transforms data into information visually understandable and easier to conclude because these plots display the variation of results according to the experimental environment variables.

## 2.5 Data validation

A considerable amount of data was generated and, more importantly, it is important that this data is scientific correct, or at least there is an explanation. In order to keep its fidelity, it is crucial to validate each and every piece of data. The first measure is to have a critical position every time by questioning if the obtained values makes sense when compared with theoretical, or expected or referenced value. In this current case, for instance, the Original group implementation is the reference, which means if the other groups have a lower execution time, or the data has some defect caused by some hardware component, or the implementation isn't good enough, or any other reason that can justify the data invalidation.

Criticism can not be the only measure because it could be luck and the gathered data happened to be correct. It is important consistency. To do so, the tests must be performed several times under the same circumstances and with a plausible and considerable amount of values to find patterns. For this particular case it is used a matrix size large enough, [1000,2000,3000,4000], and a wide range for the number of threads, [1,2,3,4,5,6,7,8]. For instance, if the matrix size was small, such as one hundred, the execution time would be so low and with so much error accumulated since the CPU executed fast enough that it couldn't count the time with precision.

Finally, to make reasonable comparisons and analogies between results, the experience environments must have some connection in its variables or environment. Without a connection, the data as no meaning, therefore, it turns impossible to take conclusions. In the performed experiences, it was used the same algorithm, Matrix Multiplication, with small modifications in the implementations but with the same structure. Additionally, the environment variables were the same: number of threads and matrix size.

## Methodology

## Chapter 3

# Results and Discussion

### 3.1 Introduction

Generally, the first objective a developer has when building software is making it work. After some experience and good practices, the development becomes faster, elegant and with concerns about its performance. When dealing if performance issues, there is a lot of measures to pay attention from the lowest hardware level to the highest software level. Now-a-days performance is as much important as the creating software, because it makes the work faster, with less cost, and, in the end, more revenue. However it is really hard for a single person masters performance as a whole because there are to many variables, conditions, aspects, and realities making humanly impossible mastering everything.

The approach to achieve high performance level is to have handful of expertises in each concrete area. Even so, mastering specific fields in the performance level, it is hard, takes time, lots of effort and most of the times impossible to achieve the perfect performance. Since achieving high level of performance is so important and requires a lot of effort to try to achieve it, then, first of all, is it possible to achieve high performance level in applications in an automatic way? If so, how can it be achievable? Can it totally replace an expert?

Focusing these question to the field of code parallelization, more will rise, not necessarily related to this specific filed: since exists tools, like Kremlin, which help to, automatically, parallelize code, how acceptable are their results? How this specific tool can help in getting one step closer to automatic code parallelization? In which way can Kremlin be better than an expert?

In order to answer all previous questions, this chapter is divided in two main sections: the first section is related to the Kremlin's activity and how is it helpful. The second section is the confrontation of all the gathered data to verify what is better and how can it contribute to the future of automatic code parallelization.

### 3.2 Kremlin's reports

When Kremlin compiles and profiles a sequential code, it provides a report with locations of the blocks that can be parallelized. Additionally it gives some values that indicates the theoretical gain if the parallelization is implemented.

For the *Mult* and *MultLine* algorithm, Kremlin gave these reports 5.8 5.9, respectively. Taking into account that the manual code parallelization was done in the first place, the risk of being bias is null and, additionally, helps to understand if Kremlin is reporting things correctly.

In this case, Kremlin detected the block code with the most impact on application performance for both implementations (*Mult* and *MultLine*). Additionally, Kremlin's report pointed the locations of more blocks to be parallelized, however, the impact of these blocks being parallelised might have a low impact on applications' performance, also for both implementations. The impact of the parallelization made in the others block is analysed in the next section because a verdict can be made after comparing the execution times of the Manual's group against Kremlin's group.

The justification behind these analysis is based on the *time reduced*, *ideal time reduced*, *coverage* and *self parallelism* values and the block location, provided by the report, comparing with the expected result and manual code parallelization by an expert, in both implementation.

Getting a close look in these reports, at the left side is *Mult* report values, 5.8, and on the right side is *MultLine* report values, 5.9:

Table 3.1: Kremlin's report values for the matrix multiplication block

<b>Time reduced</b>	66.38%	<b>Time reduced</b>	63.01%
<b>Ideal time reduced</b>	70.96%	<b>Ideal time reduced</b>	63.20%
<b>Coverage</b>	88.51%	<b>Coverage</b>	84.02%
<b>Self parallelism</b>	5.05	<b>Self parallelism</b>	4.03

In both reports, the high percentage of the reduced time and time reduced means that parallelizing these blocks the execution time of this block is, theoretically, reduced in between those two values.

Taking a close look in the others blocks, their locations refers to the matrices initialization and the values of timed reduced and ideal timed reduced are really low, around 3% in both implementations, which means that the improved performance is insignificant and might cause delay in during de applications executions. However, this situations is confirmed in the next section.

### 3.3 Comparison between Original, Manual and Kremlin

The previous section has an important role because the reports credibility and correctness influences the the results in this chapter, consequently, could lead to misguided and wrong conclusions. Since the report gave correct feedback and it is well justified, the following values are valid.

To get a satisfying answer for the initial questions, it is necessary to, in first place, understand context of each experience and respective results; following the evolution and the comparison is

made between data. So, in a first instance, each group (Original, Manual, Kremlin) is going to be analysed individually to establish the context and basis knowledge. Then, the second subsection will focus in measuring the impact of Manual and Kremlin group have to the Original group to prove that these measures, in practical terms, have a huge impact improving applications performance. After this knowledge also as been established, the results will prove if the Kremlin's guidelines make the code with better performance comparing to the expert's results parallelizing the code manually, and respond to the question if automatically parallelizing code is a reliable and good practice.

### 3.3.1 The Three groups individually analysed

As mentioned in the previous chapter, each group has its purpose based on the variables used and results obtained. To understand the overall impact of these implementations, it is important to firstly understand the experiences that were made in each group separately and analyse their results, step by step.

#### 3.3.1.1 Original

This group has the sequential code version of the *Mult* and *MultLine* algorithms. As mentioned earlier, this group establish the base reference for the execution time. From now on, all experiences should have better performance, unless there is an explanation for the Original Group has better results, in some particular cases.

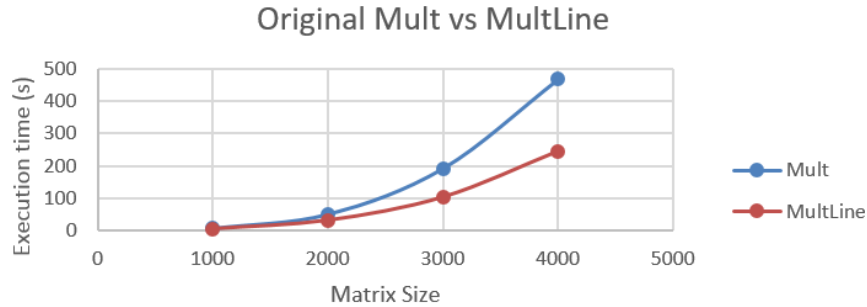


Figure 3.1: Dispersion plot that represents the evolution of execution time with the matrix size increase in both *Mult* and *MultLine* sequential implementations

According to Figure 3.1, the *MultLine* algorithm has better results the higher the matrix size is, as expected, since this algorithm takes advantage of the values preloaded in memory cache.

Another aspect noteworthy is the increase of the function inclination variation, in both implementations, as the matrix size increases. Specially for the *Mult* implementation. This is related to the memory cache size. The smaller the size the higher will be the variation of the function inclination.

### 3.3.1.2 Manual

Manually parallelizing a code requires a lot of effort, time and know-how since the way it is done requires de expert to understand the code, have practice in detecting potential parallelized blocks of code, identify the best way to parallelize those blocks and test the work until it gives a reasonable result. So, this group corresponds to this situation and, in theoretical perspective, has the bests results.

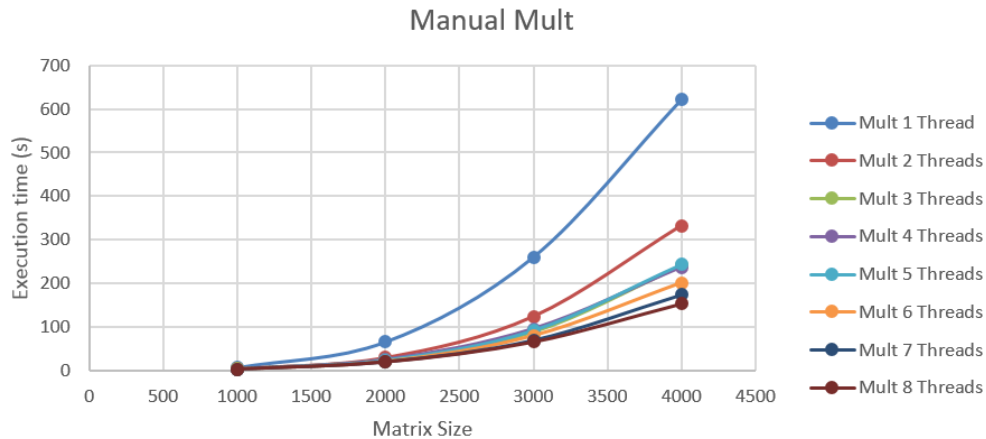


Figure 3.2: Dispersion plot that represents the evolution of execution time with the matrix size increase in function of the number of threads. Version of the manual code parallelization for *Mult* algorithm

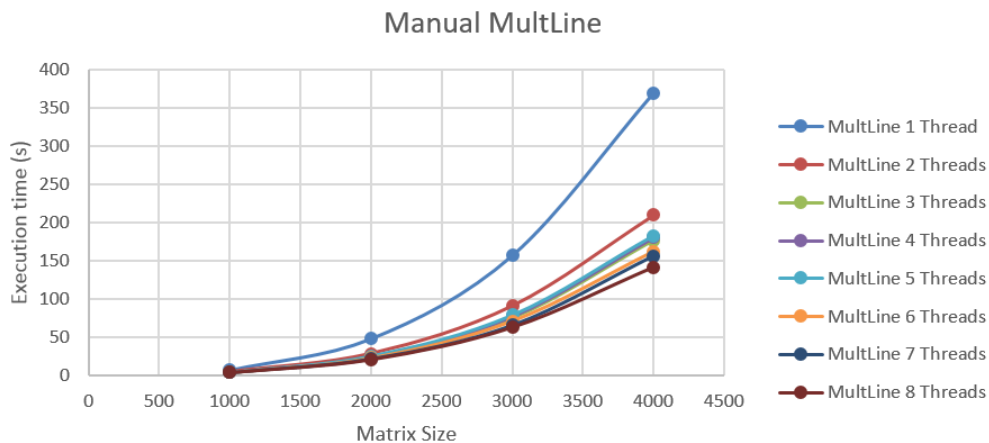


Figure 3.3: Dispersion plot that represents the evolution of execution time with the matrix size increase in function of the number of threads. Version of the manual code parallelization for *MultLine* algorithm

Figure 3.2 and Figure 3.3 have similar behaviours including the fact that using eight threads makes the application with the best performance because the executed time is inferior as long as

the matrix size increases.

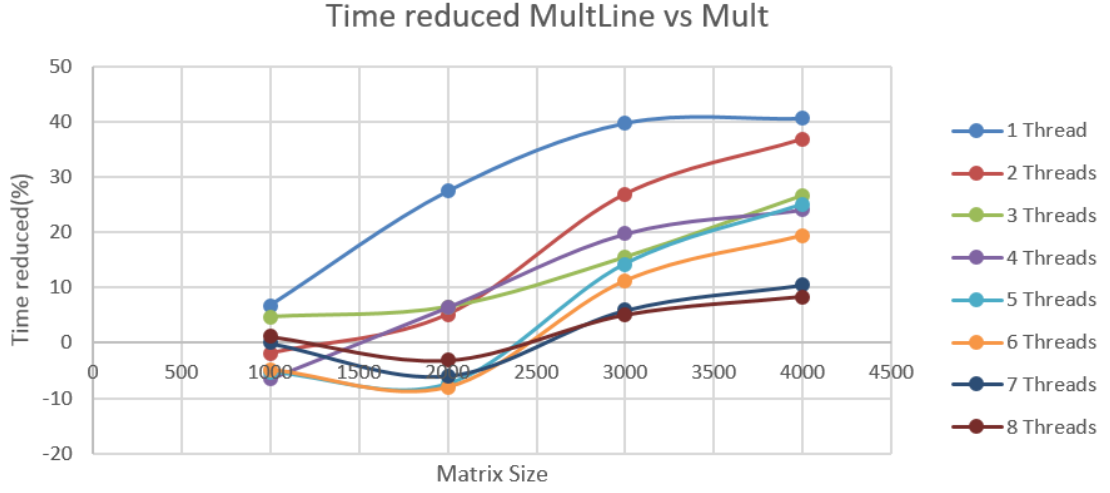


Figure 3.4: Dispersion plot that represents the evolution of time reduced with the matrix size in function of the number of threads. Versions of *Mult* and *MultLine* manual implementations.

2 However, there is a difference between these two plots is the value of the execution time. This  
 difference can be analysed in Figure 3.4. Time reduced is a percentage of how much the *MultLine*  
 4 implementation reduces comparing to the *Mult* implementation.

It is a fact, observed in Figure 3.4, that increasing the matrix size, the time reduced tends to  
 6 a certain value which is independent of the number of threads. This value is the ceiling where  
*MultLine* algorithm can not be better than *Mult* algorithm for the same hardware components;  
 8 meaning the existence of a hardware limitation (processor power, memory ram and cache size)  
 since the increase of the matrix size will, proportionally, increase the number of loads, writes and  
 10 cache missed for both algorithms.

Another fact is that the higher the thread number, lesser will be de timed reduced, and lesser  
 12 will be the executed time, also related to the maximum capacity of threads a multicore processor  
 can have and have working at the same time.

14 Another noteworthy fact is , for low values of matrix size, 1000 and 2000, and the higher  
 the number of threads being used, the time reduced is negative, meaning executed time for *Mult*  
 16 implementation is lower than *MultLine* implementations, concluding that *Mult* implementation is  
 better suited for low size data in case a high number of threads are being used. This is due to many  
 18 threads are being used simultaneously and they are trampling each other in order to complete their  
 task, which increases the overall overhead, and so the reason behind ne time reduced negative  
 20 value.

### 3.3.1.3 Kremlin

This group is constituted by the result of the implementations indicated by Kremlin's report. The experiences conducted in this group and the respective obtained results will demonstrate if Kremlin can actually bring acceptable results.

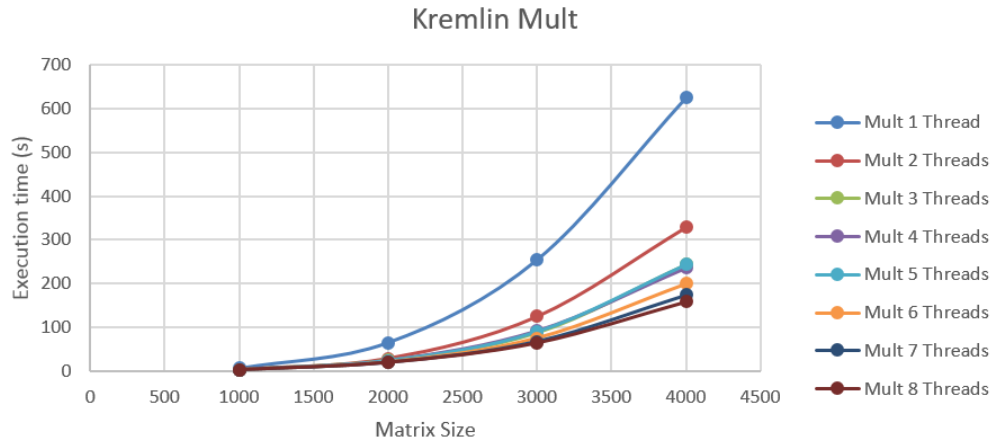


Figure 3.5: Dispersion plot that represents the evolution of execution time with the matrix size increase in function of the number of threads. Version of the Kremlin code parallelization for *Mult* algorithm

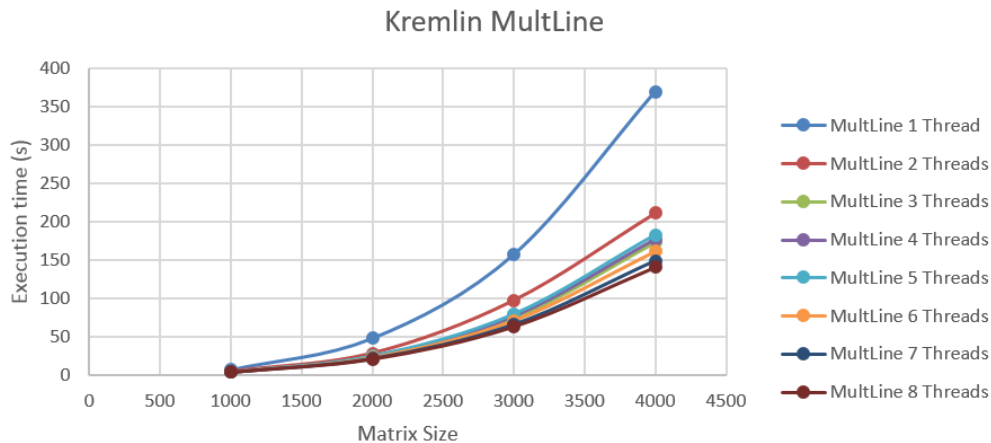


Figure 3.6: Dispersion plot that represents the evolution of execution time with the matrix size increase in function of the number of threads. Version of the Kremlin parallelization code for *Multline* algorithm

Figure 3.5 and Figure 3.6, as expected, have similar behaviour compared to manual group, since the code samples from both groups have the same structure and the experimental environment is the same: same experimental variables(matrix size, number of threads, parallelized code) and same result type (execution time).



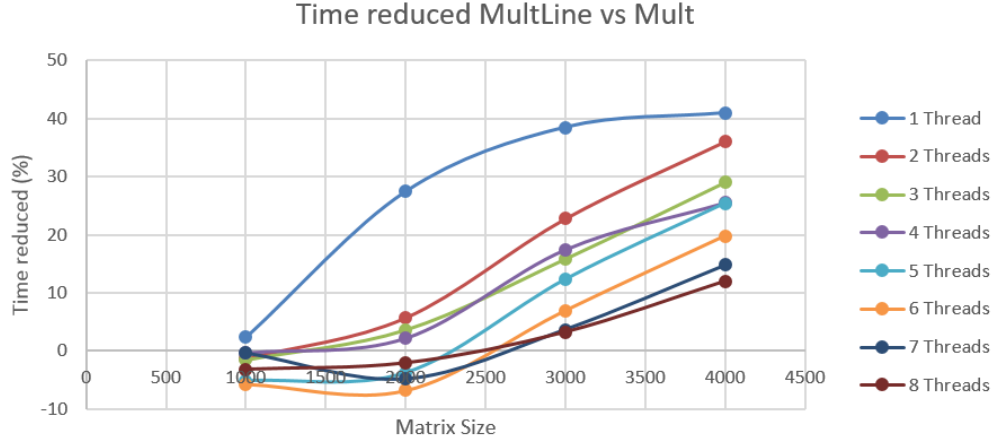


Figure 3.7: Dispersion plot that represents the evolution of time reduced with the matrix size in function of the number of threads. Versions of *Mult* and *MultLine* Kremlin's implementations.

Additionally, and for the same reason, Figure 3.7 also has the same behaviour as the manual group and, therefore, the analysis is the same.

### 3.3.1.4 Overall review

Through the analysis of these three tests groups, separately, and since these groups were tested under the same circumstances, it is possible to conclude that using these versions of the matrix multiplication algorithm do not jeopardize the results, since they have similar behaviours, moreover, they increase assurance and credibility for the following up analysis and conclusions. It is because of the similarity of behaviours that this comparison is valid and correct, even so the performance is different, which was expected, as explained before.

### 3.3.2 Measuring the performance's impact using code parallelization

The conducted analysis presented in previous section explains the characteristics of each group and the plausible connection with each other. To quantify how beneficial can code parallelization be, the next sub sub sections will prove its impact.

The first sub sub section compares how much better was the improvement for Manual group, using the Original group as base. The second sub sub sections compares the same ways as the previous sub sub sections but, instead using the Manual group, it will be the Kremlin group. Finally, overall conclusions will be presented about both sub sub sections.

For this analysis, either Manual or Kremlin groups, the number of threads that will be used is eight since, and for both groups, using eight threads gave the best performance results. This does not mean that for the other number of threads the results were worst than the Original group, far from that. The goal is achieving the highest performance, so the best results were picked.

### 3.3.2.1 Comparison between Manual and Original groups

In Figure 3.8 is presented a dispersion plot demonstrating that Manual group has an overwhelming better performance and that performance increases with the matrix size to a certain point, explained in 3.3.1.2. This includes both implementations. 2

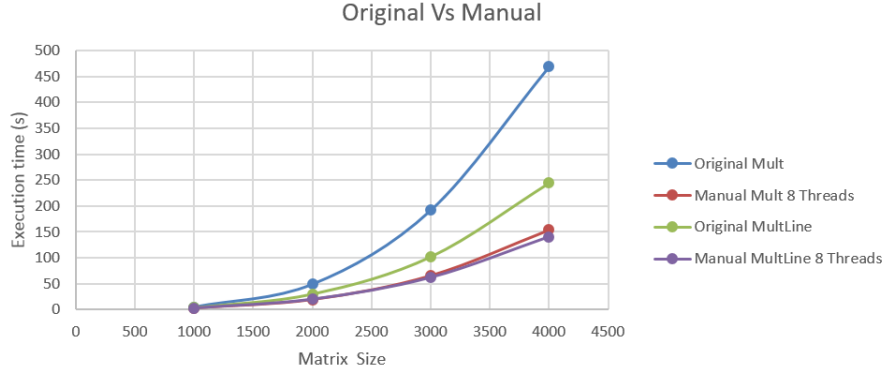


Figure 3.8: Dispersion plot that represents the evolution of execution time with the matrix size in function of Manual and Original.

With this plot, it is proved that using parallelization techniques in programs and applications can achieve higher performances. In this particular case, it can be more than twice better for the *Mult* case and almost twice for *MultLine*. These values are presented in 3.3.2.3. 4  
6

### 3.3.2.2 Comparison between Kremlin and Original groups

Regarding the Kremlin experimental results, in Figure 3.9 is shown a dispersion plot revealing, as expected, and like in the previous sub sub section, that Kremlin's group surpassed the Original group in terms of execution time, in both implementations. 8  
10

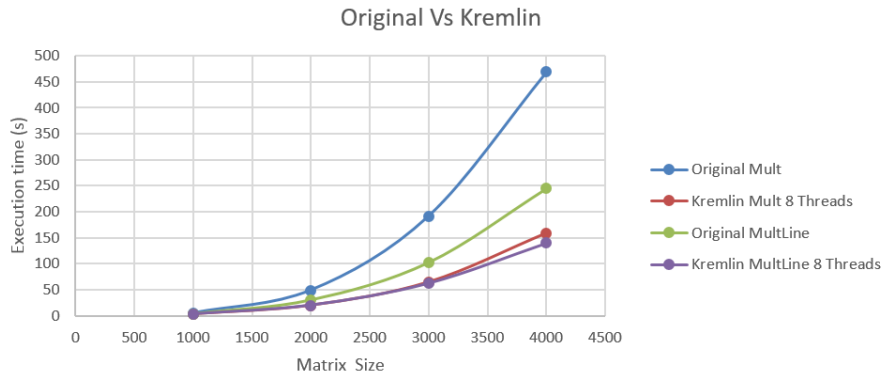


Figure 3.9: Dispersion plot that represents the evolution of execution time with the matrix size in function of Kremlin and Original groups implementations.

Like in previous case, this plot proves that using Kremlin as an automatic tool to help identifying parallelizable code blocks can enhance applications performance and is a good asset because it can save time by indicating where it is possible to make a block parallel, instead of the developer looking for them.

### 3.3.2.3 Overall comparison

If code parallelization would not bring any beneficial impact in programs and applications, obviously, it would make no sense using it. Additional, and for this concrete case, if Kremlin's performance was worst, this tool would be useless to help in making paralleled code automatically, and the test would end here.

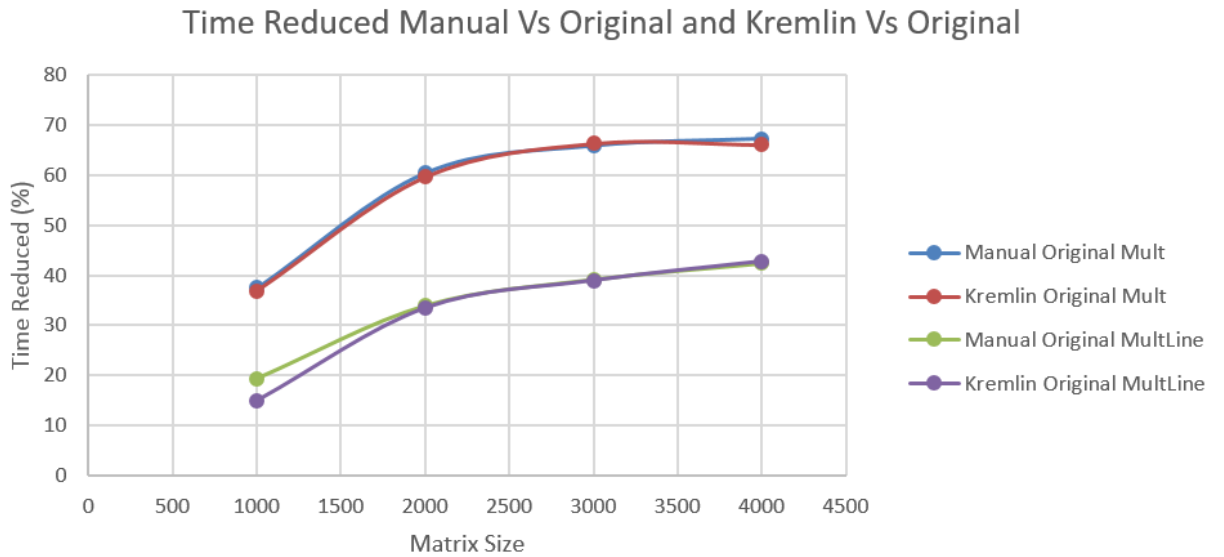


Figure 3.10: Dispersion plot that represents the evolution of time reduced with the matrix size in function of Manual and Kremlin groups implementations.

In Figure 3.10 is compared the time reduced for Manual and Kremlin groups comparatively to Original Group. From this plot it is drawn that *Mult* algorithm takes greater advantages of code parallelization, however *MultLine* has better execution times.

Quantifying such increase, for the *Mult* algorithm the time reduced can be around 67%, meaning that the *Mult* implementation, for both groups, can be 67% faster, more than twice, compared to the Original group, for the same algorithm. Regarding the *MultLine* algorithm, the time reduced can be around 42%. The 25% difference of both implementations represents the impact of the difference on matrix multiplication algorithm versions. Meaning that *MultLine* algorithm makes a huge difference on performance levels.

### 3.3.3 Comparison between Manual and Kremlin groups

Before starting the comparison between results obtained from running the parallelized code written by an expert and running the parallelized code with Kremlin's indication, the difference between them is that in the Kremlin group, more specifically, in the matrix initializations blocks, they are parallelized as well. These blocks were parallelized because Kremlin detected them, however the theoretical impact for these parallelized blocks could be small, positively or negatively, or even with no effect. Additional, in the expert perspective, they were not taken in consideration.

Now that the three groups were characterized and the comparison between Manual and Kremlin with Original group to establish viability in both solutions, taking a close look at all this information and analysis and looking at Figure 3.10, the lines in the plot overlap or are really closed to one another in both algorithms. This observation confirms what is expected, which is manually parallelizing a code by an expert or following Kremlin's instructions gives the same results, approximately. However, they are not exactly the same because a small modification was done to Kremlin's group code.

In order to evaluate if there is really a difference in results, the following plots present the execution time ratio between Manual and Kremlin's group, for both implementations.

To correctly analyse Figures 3.11 and 3.12, it is important to visualize the range of the values:

Table 3.2: Interval of values for *Mult* (left table) and *MultLine* (right table)

<b>Lower bound</b>	0,966	<b>Lower bound</b>	0,936
<b>Upper bound</b>	1,049	<b>Upper bound</b>	1,073
<b>Interval size</b>	0,083	<b>Interval size</b>	0,137

Looking at the tabulated values, there are cases where Kremlin group had better performance than the Manual group, since there are values less than 1. However, looking at the interval size, for both cases, they are really small, meaning that the execution time for both groups is similar.

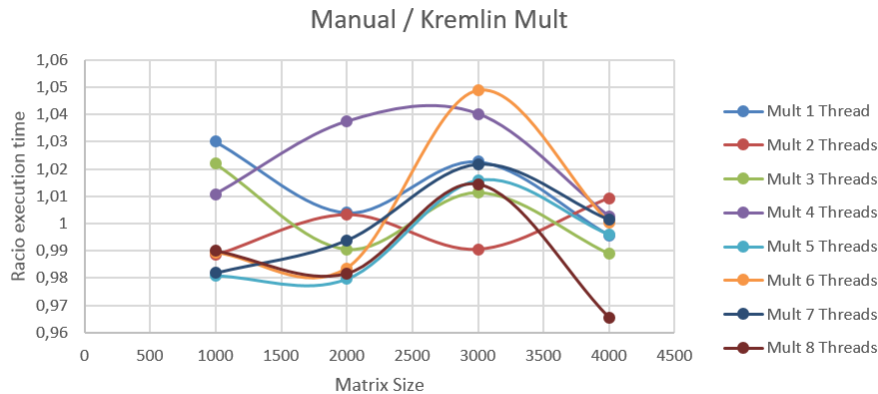


Figure 3.11: Dispersion plot that represents the evolution of execution time ratio with the matrix size in function of the number of threads, for the *Mult* algorithm .

## Results and Discussion

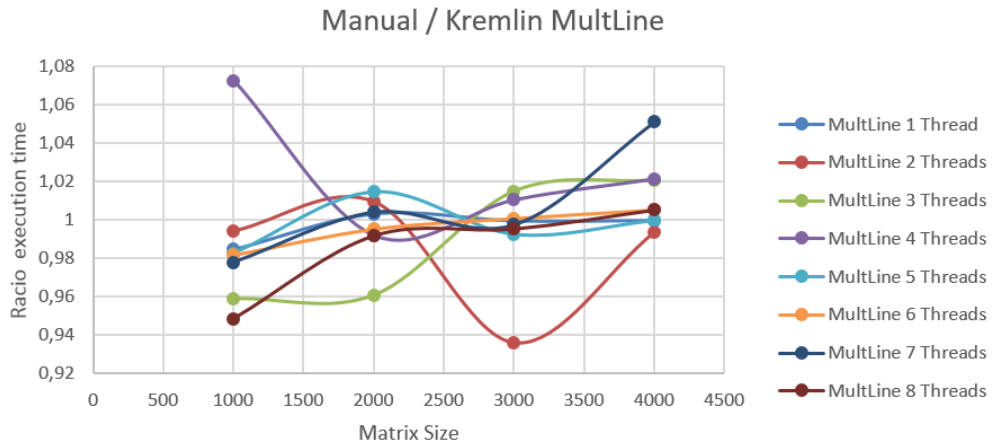


Figure 3.12: Dispersion plot that represents the evolution of execution time ratio with the matrix size in function of the number of threads, for the *MultLine* algorithm.

Confronting Figures 3.11 and 3.12, they seemed confusing, messed up, random, and that is partial true because the execution time values from both groups are that close form one another and a slight alteration makes, apparently, an huge impact. It is also partial false because there are patterns in these plots: for each matrix size there is a concentration of lines, meaning that it is not so random and the explanation is that both parallelizations have similar performances. However, and again, the interval value is small.

Trying to se the data in a different angle and perspective to understand if there is any correlation, pattern or relation, these Figures, 3.13 and 3.14, are an unsuccessful attempt of it. The point of these two figures is to understand the variation of execution time ratio with number of threads in function of matrix size, however there is no relation for both cases. So, the number of threads has no direct relation with the execution time ratio in function of the matrix size.

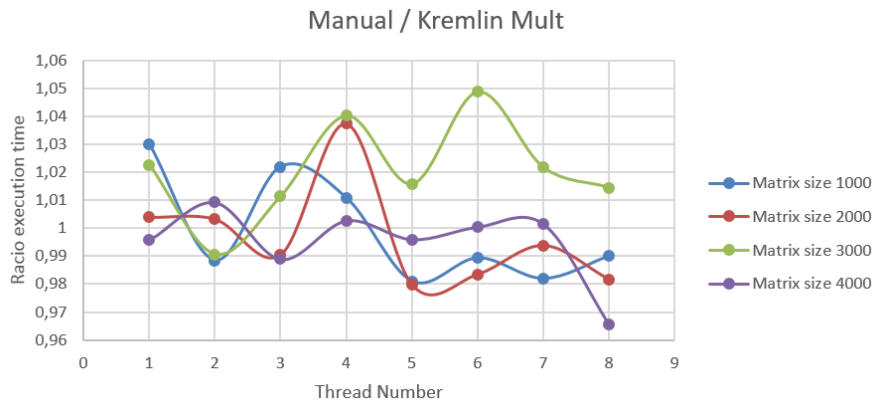


Figure 3.13: Dispersion plot that represents the evolution of execution time ratio with the number of threads in function of matrix size, for the *Mult* algorithm.

## Results and Discussion

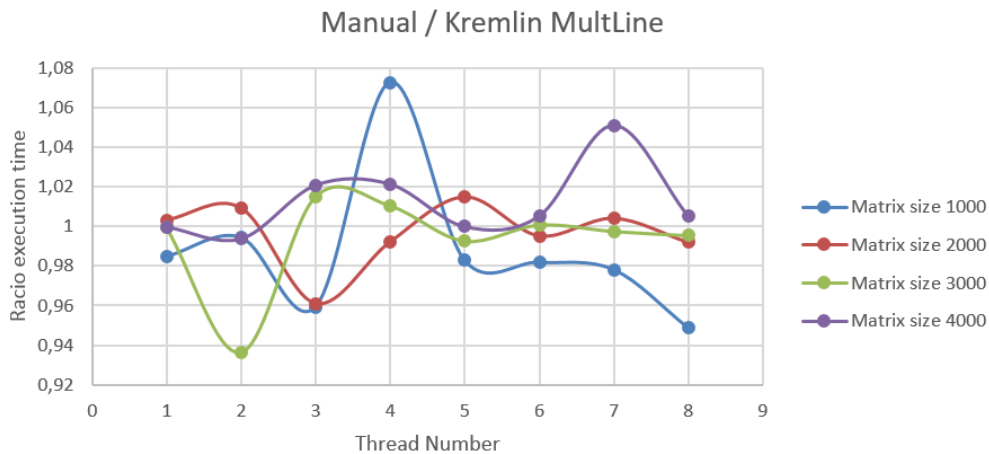


Figure 3.14: Dispersion plot that represents the evolution of execution time ratio with the number of threads in function of matrix size, for the *MultLine* algorithm.

So far, in this sub sub section, the analysis done is in a general plane. this Figure 3.15 is about execution time ratio in function of matrix size using eight threads. This specific case was chosen because for both groups, using 8 threads had the best results, as previously mentioned and verified. In the green line is established as a reference: the values under this line mean Kremlin performed better than Manual, and the values above this line mean the opposite. Looking at those values, there is more values under the line than above, meaning that Kremlin group performed better, for both implementations.

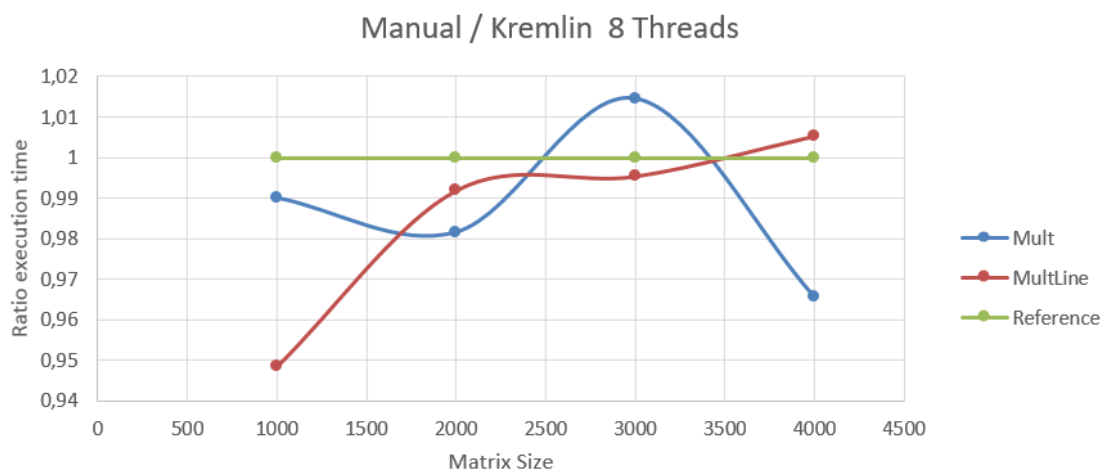


Figure 3.15: Dispersion plot that represents the evolution of execution time ratio with the matrix size in for 8 threads being used, using *Mult* and *MultLine* algorithms.

## **Chapter 4**

## **2 Conclusion**

## Conclusion



# References

- <sup>2</sup> [GJ] Saturnino Garcia Jr. *A Practical Oracle for Sequential Code Parallelization*. Cited on page [7](#).
- [Kre] kremlin: like gprof but for parallelization. <https://bitbucket.org/elsaturnino/kremlin>. Accessed: 2017-04-17. Cited on page [5](#).
- <sup>4</sup>

## REFERENCES

# Chapter 5

## 2 Appendices

### 5.1 Developed code

#### 4 5.1.1 Original Matrix Multiplication (Mult)

Listing 5.1: Matrix Multiplication original algorithm, written in C++

```
6 double OnMult(int m_ar, int m_br)
2 {
3     double Time1, Time2;
4     double temp;
10    int i, j, k;
6     double *pha, *phb, *phc;
12
8     //Matrixes Memory allocation
14    pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
10    phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
16    phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
12
18    //Starting counting time
14    Time1 = omp_get_wtime();
20
16    //Loading matrix values
22    for(i=0; i<m_ar; i++)
18        for(j=0; j<m_ar; j++)
24            pha[i*m_ar + j] = (double)1.0;
20
26    for(i=0; i<m_br; i++)
22        for(j=0; j<m_br; j++)
28            phb[i*m_br + j] = (double)(i+1);
24
26    //Matrix Multiplication
32    for(i=0; i<m_ar; i++)
28    {    for( j=0; j<m_br; j++)
34        {    temp = 0;
30            for( k=0; k<m_ar; k++)
36                {
32                    temp += pha[i*m_ar+k] * phb[k*m_br+j];
```

## Appendices

```
33         }
34         phc[i*m_ar+j]=temp;
35     }
36 }
37
38 //Stopping time
39 Time2 = omp_get_wtime();
40
41 //Freeing memory used for matrixes
42 free(pha);
43 free(phb);
44 free(phc);
45
46 return Time2 - Time1;
47 }
```

### 5.1.2 Original Matrix Multiplication By line (MutLine)

Listing 5.2: Matrix Multiplication by line original algorithm, written in C++

```
1 double OnMultLine(int m_ar, int m_br)
2 {
3     double Time1, Time2;
4     double temp;
5     int i, j, k;
6     double *pha, *phb, *phc;
7
8     //Matrixes Memory allocation
9     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
10    phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
11    phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
12
13    //Starting counting time
14    Time1 = omp_get_wtime();
15
16    //Loading matrix values
17    for(i=0; i<m_ar; i++)
18        for(j=0; j<m_ar; j++)
19            pha[i*m_ar + j] = (double)1.0;
20
21    for(i=0; i<m_br; i++)
22        for(j=0; j<m_br; j++)
23            phb[i*m_br + j] = (double)(i+1);
24
25    for(i=0; i<m_ar; i++)
26        for(j=0; j<m_ar; j++)
27            phc[i*m_ar + j] = (double)0.0;
28
29
30    //Matrix Multiplication
31    for(i=0; i<m_ar; i++)
32    {
33        for( k=0; k<m_ar; k++)
34        {
35            for( j=0; j<m_br; j++)
```

## Appendices

```
35         {
36             phc[i*m_ar+j] += pha[i*m_ar+k] * phb[k*m_br+j];
37         }
38     }
39 }
40
41
42 //Stopping time
43 Time2 = omp_get_wtime();
44
45 //Freeing memory used for matrixes
46 free(pha);
47 free(phb);
48 free(phc);
49
50 return Time2 - Time1;;
51 }
```

### 5.1.3 Manual Matrix Multiplication (Mult)

Listing 5.3: Matrix Multiplication manually parallelised using OpenMP library, written in C++

```
20
21 double OnMultThreading(int m_ar, int m_br, int x)
22 {
23     double Time1, Time2;
24     double temp;
25     int i, j, k;
26     double *pha, *phb, *phc;
27
28     //Matrixes Memory allocation
29     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
30     phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
31     phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
32
33     //Starting counting time
34     Time1 = omp_get_wtime();
35
36     //Loading matrix values
37     for(i=0; i<m_ar; i++)
38         for(j=0; j<m_ar; j++)
39             pha[i*m_ar + j] = (double)1,0;
40
41     for(i=0; i<m_br; i++)
42         for(j=0; j<m_br; j++)
43             phb[i*m_br + j] = (double)(i+1);
44
45     //Matrix Multiplication
46     for(i=0; i<m_ar; i++)
47     {
48         for( j=0; j<m_br; j++)
49         {
50             temp = 0;
51             #pragma omp parallel for reduction(+:temp) num_threads (x)
52             for( k=0; k<m_ar; k++)
53             {
```

## Appendices

```

33         temp += pha[i*m_ar+k] * phb[k*m_br+j];
34     }
35     phc[i*m_ar+j]=temp;
36 }
37 }
38
39 //Stopping time
40 Time2 = omp_get_wtime();
41
42 //Freeing memory used for matrixes
43 free(pha);
44 free(phb);
45 free(phc);
46
47 return Time2 - Time1;
48 }

```

### 5.1.4 Manual Matrix Multiplication By line (MultLine)

Listing 5.4: Matrix Multiplication by line manually parallelised using OpenMP library, written in C++

```

1  double OnMultLineThreading(int m_ar, int m_br, int x)
2  {
3      double Time1, Time2;
4      int i, j, k;
5      double *pha, *phb, *phc;
6
7      //Matrixes Memory allocation
8      pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
9      phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
10     phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
11
12     //Starting counting time
13     Time1 = omp_get_wtime();
14
15     //Loading matrix values
16     for(i=0; i<m_ar; i++)
17         for(j=0; j<m_ar; j++)
18             pha[i*m_ar + j] = (double)1,0;
19
20     for(i=0; i<m_br; i++)
21         for(j=0; j<m_br; j++)
22             phb[i*m_br + j] = (double)(i+1);
23
24     for(i=0; i<m_ar; i++)
25         for(j=0; j<m_ar; j++)
26             phc[i*m_ar + j] = (double)0,0;
27
28
29     //Matrix Multiplication
30     for(i=0; i<m_ar; i++)
31     {
32         for(k=0; k<m_ar; k++)

```

## Appendices

```
32     {
33         #pragma omp parallel for num_threads (x)
34         for( j=0; j<m_br; j++)
35         {
36             phc[i*m_ar+j] += pha[i*m_ar+k] * phb[k*m_br+j];
37         }
38     }
39 }
40
41 //Stopping time
42 Time2 = omp_get_wtime();
43
44 //Freeing memory used for matrixes
45 free(pha);
46 free(phb);
47 free(phc);
48
49 return Time2 - Time1;
50 }
```

### 22 5.1.5 Kremlin Matrix Multiplication (Mult)

Listing 5.5: Matrix Multiplication with Kremlin's indications for parallelization, written in C++

```
24 double OnMultKremlin(int m_ar, int m_br, int x)
25 {
26     double Time1, Time2;
27     double temp;
28     int i, j, k;
29     double *pha, *phb, *phc;
30
31     //Matrixes Memory allocation
32     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
33     phb = (double *)malloc((m_ar * m_br) * sizeof(double));
34     phc = (double *)malloc((m_ar * m_br) * sizeof(double));
35
36     //Starting counting time
37     Time1 = omp_get_wtime();
38
39     //Loading matrix values
40     for(i=0; i<m_ar; i++)
41     {
42         #pragma omp parallel for num_threads (x)
43         for(j=0; j<m_br; j++)
44             pha[i*m_ar + j] = (double)1,0;
45     }
46
47     for(i=0; i<m_br; i++)
48     {
49         #pragma omp parallel for num_threads (x)
50         for(j=0; j<m_br; j++)
51             phb[i*m_br + j] = (double)(i+1);
52     }
53
54     //Matrix Multiplication
55     for(i=0; i<m_ar; i++)
```

## Appendices

```
30 {   for( j=0; j<m_br; j++)
31     {   temp = 0;
32         #pragma omp parallel for reduction(+:temp) num_threads (x)
33         for( k=0; k<m_ar; k++)
34             {
35                 temp += pha[i*m_ar+k] * phb[k*m_br+j];
36             }
37         phc[i*m_ar+j]=temp;
38     }
39 }
40
41 //Stopping time
42 Time2 = omp_get_wtime();
43
44 //Freeing memory used for matrixes
45 free(pha);
46 free(phb);
47 free(phc);
48
49 return Time2 - Time1;
50 }
```

### 5.1.6 Kremlin Matrix Multiplication By line (MultLine)

Listing 5.6: Matrix Multiplication by line with Kremlin's indications for parallelization, written in C++

```
1 double OnMultLineKremlin(int m_ar, int m_br, int x)
2 {
3     double Time1, Time2;
4     int i, j, k;
5     double *pha, *phb, *phc;
6
7     //Matrixes Memory allocation
8     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
9     phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
10    phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
11
12    //Starting counting time
13    Time1 = omp_get_wtime();
14
15    //Loading matrix values
16    for(i=0; i<m_ar; i++)
17        #pragma omp parallel for num_threads (x)
18        for(j=0; j<m_ar; j++)
19            pha[i*m_ar + j] = (double)1,0;
20
21    for(i=0; i<m_br; i++)
22        #pragma omp parallel for num_threads (x)
23        for(j=0; j<m_br; j++)
24            phb[i*m_br + j] = (double)(i+1);
25
26    for(i=0; i<m_ar; i++)
```



## Appendices

```
27     #pragma omp parallel for num_threads (x)
28     for(j=0; j<m_ar; j++)
29         phc[i*m_ar + j] = (double)0.0;
30
31     //Matrix Multiplication
32     for(i=0; i<m_ar; i++)
33     {     for( k=0; k<m_ar; k++)
34         {
35             #pragma omp parallel for num_threads (x)
36             for( j=0; j<m_br; j++)
37             {
38                 phc[i*m_ar+j] += pha[i*m_ar+k] * phb[k*m_br+j];
39             }
40         }
41     }
42 }
43
44 //Stoping time
45 Time2 = omp_get_wtime();
46
47 //Freeing memory used for matrixes
48 free(pha);
49 free(phb);
50 free(phc);
51
52 return Time2 - Time1;
53 }
```

### 5.1.7 Sequential program compiled and profiled by Kremlin

Listing 5.7: Program compiled and profiled by Kremlin with both Mult and MultLine algorithms, written in C++

```
30 1 //#include <omp.h>
31 2 #include <stdio.h>
32 3 #include <iostream>
33 4 #include <iomanip>
34 5 #include <time.h>
35 6 #include <stdlib.h>
36 7 //#include <papi.h>
37 8 #include <fstream>
38 9 #include <chrono>
39
40
41 11 using namespace std;
42
43 13 #define SYSTEMTIME clock_t
44 14 /*
45 15 double OnMultLineThreading(int m_ar, int m_br,int x)
46 16 {
47 17     double Time1, Time2;
48
49 19     char st[100];
50 20     double temp;
```

## Appendices

```

21  int i, j, k;
22
23  double *pha, *phb, *phc;
24
25
26
27  pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
28  phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
29  phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
30
31  for(i=0; i<m_ar; i++)
32      for(j=0; j<m_ar; j++)
33          pha[i*m_ar + j] = (double)1.0;
34
35
36
37  for(i=0; i<m_br; i++)
38      for(j=0; j<m_br; j++)
39          phb[i*m_br + j] = (double)(i+1);
40
41  for(i=0; i<m_ar; i++)
42      for(j=0; j<m_ar; j++)
43          phc[i*m_ar + j] = (double)0.0;
44
45
46
47  Time1 = omp_get_wtime();
48
49  for(i=0; i<m_ar; i++)
50  {   for( k=0; k<m_ar; k++)
51      {
52          #pragma omp parallel for num_threads (x)
53          for( j=0; j<m_br; j++)
54          {
55              phc[i*m_ar+j] += pha[i*m_ar+k] * phb[k*m_br+j];
56          }
57      }
58  }
59
60
61
62  Time2 = omp_get_wtime();
63  // sprintf(st, "Time: %3.3f seconds\n", (double)(Time2 - Time1));
64  // cout << st;
65
66  /*cout << "Result matrix: " << endl;
67  for(i=0; i<min(10,m_ar); i++)
68  {   for(j=0; j<min(10,m_br); j++)
69      cout << phc[j] << " ";
70  }
71  cout << endl;*/
72  /*
73  free(pha);
74  free(phb);
75  free(phc);
76  return (double)(Time2 - Time1);
77

```

## Appendices

```

78  */
79  /*
80  double OnMultThreading(int m_ar, int m_br,int x)
81  {
82
83      double Time1, Time2;
84
85      char st[100];
86      double temp;
87      int i, j, k;
88
89      double *pha, *phb, *phc;
90
91
92
93      pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
94      phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
95      phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
96
97      for(i=0; i<m_ar; i++)
98          for(j=0; j<m_ar; j++)
99              pha[i*m_ar + j] = (double)1.0;
100
101
102
103      for(i=0; i<m_br; i++)
104          for(j=0; j<m_br; j++)
105              phb[i*m_br + j] = (double)(i+1);
106
107
108
109      Time1 = omp_get_wtime();
110
111      for(i=0; i<m_ar; i++)
112      {
113          for( j=0; j<m_br; j++)
114          {
115              temp = 0;
116              #pragma omp parallel for reduction(+:temp) num_threads (x)
117              for( k=0; k<m_ar; k++)
118              {
119                  temp += pha[i*m_ar+k] * phb[k*m_br+j];
120              }
121              phc[i*m_ar+j]=temp;
122          }
123      }
124
125      Time2 = omp_get_wtime();
126      // sprintf(st, "Time: %3.3f seconds\n", (double)(Time2 - Time1));
127      // cout << st;
128
129      /*cout << "Result matrix: " << endl;
130      for(i=0; i<1; i++)
131      {
132          for(j=0; j<min(10,m_br); j++)
133              cout << phc[j] << " ";
134      }
135      cout << endl;*/
136  }
137  /*

```

## Appendices

```

135     free(pha);
136     free(phb);
137     free(phc);
138     return (double)(Time2 - Time1);
139
140 }*/
141
142 double OnMult(int m_ar, int m_br)
143 {
144
145     //double Time1, Time2;
146
147     char st[100];
148     double temp;
149     int i, j, k;
150
151     double *pha, *phb, *phc;
152
153
154
155     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
156     phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
157     phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
158
159     for(i=0; i<m_ar; i++)
160         for(j=0; j<m_ar; j++)
161             pha[i*m_ar + j] = (double)1.0;
162
163
164
165     for(i=0; i<m_br; i++)
166         for(j=0; j<m_br; j++)
167             phb[i*m_br + j] = (double)(i+1);
168
169
170
171     //Time1 = omp_get_wtime();
172     auto Time1 = std::chrono::high_resolution_clock::now();
173
174     for(i=0; i<m_ar; i++)
175     {
176         for(j=0; j<m_br; j++)
177         {
178             temp = 0;
179             for(k=0; k<m_ar; k++)
180             {
181                 temp += pha[i*m_ar+k] * phb[k*m_br+j];
182             }
183             phc[i*m_ar+j]=temp;
184         }
185     }
186
187     //Time2 = omp_get_wtime();
188     auto Time2 = std::chrono::high_resolution_clock::now();
189     // sprintf(st, "Time: %3.3f seconds\n", (double)(Time2 - Time1));
190     // cout << st;
191
192     /*cout << "Result matrix: " << endl;

```

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```

192     for(i=0; i<1; i++)
193     {   for(j=0; j<min(10,m_br); j++)
194         cout << phc[j] << " ";
195     }
196     cout << endl;*/
197
198     free(pha);
199     free(phb);
200     free(phc);
201     auto time = Time2-Time1;
202     return (double) std::chrono::duration_cast<std::chrono::milliseconds>(time).count();
203 }
204
205
206 double OnMultLine(int m_ar, int m_br)
207 {
208     //double Time1, Time2;
209
210     char st[100];
211     double temp;
212     int i, j, k;
213
214     double *pha, *phb, *phc;
215
216
217
218     pha = (double *)malloc((m_ar * m_ar) * sizeof(double));
219     phb = (double *)malloc((m_ar * m_ar) * sizeof(double));
220     phc = (double *)malloc((m_ar * m_ar) * sizeof(double));
221
222
223     for(i=0; i<m_ar; i++)
224         for(j=0; j<m_ar; j++)
225             pha[i*m_ar + j] = (double)1.0;
226
227
228
229     for(i=0; i<m_br; i++)
230         for(j=0; j<m_br; j++)
231             phb[i*m_br + j] = (double)(i+1);
232
233     for(i=0; i<m_ar; i++)
234         for(j=0; j<m_ar; j++)
235             phc[i*m_ar + j] = (double)0.0;
236
237
238
239     //Time1 = omp_get_wtime();
240     auto Time1 = std::chrono::high_resolution_clock::now();
241
242     for(i=0; i<m_ar; i++)
243     {   for( k=0; k<m_ar; k++)
244         {
245             for( j=0; j<m_br; j++)
246             {
247                 phc[i*m_ar+j] += pha[i*m_ar+k] * phb[k*m_br+j];
248             }

```

## Appendices

```

249     }
250 }
251 }
252
253
254 //Time2 = omp_get_wtime();
255 auto Time2 = std::chrono::high_resolution_clock::now();
256 // sprintf(st, "Time: %3.3f seconds\n", (double)(Time2 - Time1));
257 //cout << st;
258
259 /*cout << "Result matrix: " << endl;
260 for(i=0; i<min(10,m_ar); i++)
261 {   for(j=0; j<min(10,m_br); j++)
262     cout << phc[j] << " ";
263 }
264 cout << endl;*/
265
266 free(pha);
267 free(phb);
268 free(phc);
269 auto time = Time2-Time1;
270 return (double) std::chrono::duration_cast<std::chrono::milliseconds>(time).count();
271 }
272 }
273
274 void OutputToFile(int lin, int col, int inc, int limit/*, char* filename*/)
275 {
276     int i;
277     double temp;
278     ofstream myfile;
279     myfile.open (/*filename*/"matrixMultResult.csv");
280     myfile << "i,Algoritmo a,Algoritmo b\n";
281     for(i=lin;i <= limit; i=i+inc)
282     {
283         temp=OnMult(i,i);
284         myfile << i << "," << temp << ",";
285         temp=OnMultLine(i,i);
286         myfile << temp << "\n";
287     }
288     myfile.close();
289 }
290
291
292
293
294 float produtoInterno(float *v1, float *v2, int col)
295 {
296     int i;
297     float soma=0.0;
298
299     for(i=0; i<col; i++)
300         soma += v1[i]*v2[i];
301
302     return(soma);
303 }
304
305 /*)

```

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```
306 void handle_error (int retval)
307 {
308     printf("PAPI error %d: %s\n", retval, PAPI_strerror(retval));
309     exit(1);
310 }
311
312 void init_papi() {
313     int retval = PAPI_library_init(PAPI_VER_CURRENT);
314     if (retval != PAPI_VER_CURRENT && retval < 0) {
315         printf("PAPI library version mismatch!\n");
316         exit(1);
317     }
318     if (retval < 0) handle_error(retval);
319
320     std::cout << "PAPI Version Number: MAJOR: " << PAPI_VERSION_MAJOR(retval)
321               << " MINOR: " << PAPI_VERSION_MINOR(retval)
322               << " REVISION: " << PAPI_VERSION_REVISION(retval) << "\n";
323 }*/
324
325
326 int main (int argc, char *argv[])
327 {
328
329     char c;
330     int lin, col, nt=1, inc, limit,x;
331     int op;
332     char* filename;
333     //int EventSet = PAPI_NULL;
334     long long values[2];
335     int ret;
336     /*
337     cout << "Numero de processadores: " << omp_get_num_procs() << endl;
338     ret = PAPI_library_init( PAPI_VER_CURRENT );
339     if ( ret != PAPI_VER_CURRENT )
340         std::cout << "FAIL" << endl;
341
342
343     ret = PAPI_create_eventset(&EventSet);
344     if (ret != PAPI_OK) cout << "ERRO: create eventset" << endl;
345
346
347     ret = PAPI_add_event(EventSet,PAPI_L1_DCM);
348     if (ret != PAPI_OK) cout << "ERRO: PAPI_L1_DCM" << endl;
349
350
351     ret = PAPI_add_event(EventSet,PAPI_L2_DCM);
352     if (ret != PAPI_OK) cout << "ERRO: PAPI_L2_DCM" << endl;
353
354     */
355
356
357     op=1;
358     do {
359         cout << endl;
360         cout << "1. Multiplication" << endl;
361         cout << "2. Line Multiplication" << endl;
362         cout << "3. outputToFile" << endl;
```

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```

363     cout << "4. multithreading on Multiplication" << endl;
364     cout << "5. multithreading on LineMultiplication" << endl;
365     cout << "Selection?: ";
366
367     cin >> op;
368     if (op == 0)
369         break;
370
371     printf("Dimensions: lins cols ? ");
372     cin >> lin >> col;
373
374
375     if(op == 3)
376     {
377         printf("Dimensional increment: inc ? ");
378         cin >> inc;
379         printf("Limit: limit ? ");
380         cin >> limit;
381         /*printf("outputFile: filename.csv (must be an existing file) ? ");
382         cin >> filename;*/
383     }
384     /*if(op == 4 || op == 5)
385     {
386         printf("Number of threads: x");
387         cin >> x;
388     }*/
389
390
391     // Start counting
392     /*ret = PAPI_start(EventSet);
393     if (ret != PAPI_OK) cout << "ERRO: Start PAPI" << endl;
394 */
395     switch (op){
396     case 1:
397         cout << OnMult(lin, col)<< endl;
398         break;
399     case 2:
400         cout << OnMultLine(lin, col)<<endl;
401         break;
402     case 3:
403         OutputToFile(lin, col, inc, limit/*, filename*/);
404         break;
405     /*case 4:
406         cout << OnMultThreading(lin , col,x)<< endl;
407         break;
408     case 5:
409         cout << OnMultLineThreading(lin , col,x)<< endl;
410         break;*/
411     }
412
413     /*ret = PAPI_stop(EventSet, values);
414     if (ret != PAPI_OK) cout << "ERRO: Stop PAPI" << endl;
415     printf("L1 DCM: %lld \n",values[0]);
416     printf("L2 DCM: %lld \n",values[1]);
417     ret = PAPI_reset( EventSet );
418     if ( ret != PAPI_OK )
419         std::cout << "FAIL reset" << endl; */

```



```

420
421
422
423     } while (op != 0);
424     /*
425         ret = PAPI_remove_event( EventSet, PAPI_L1_DCM );
426         if ( ret != PAPI_OK )
427             std::cout << "FAIL remove event" << endl;
428
429         ret = PAPI_remove_event( EventSet, PAPI_L2_DCM );
430
431
432         ret = PAPI_destroy_eventset( &EventSet );
433         if ( ret != PAPI_OK )
434             std::cout << "FAIL destroy" << endl;
435     */
436
437 }

```

## 20 5.2 Kremlin's Reports

### 5.2.1 Kremlin report for Matrix Multiplication, Mult version

Listing 5.8: Kremlin's indication of the blocks that should be parallelised and theoretical variables that where calculated for Mult algorithm version

```

22
1  [ 0] TimeRed(4)=66.38%, TimeRed(Ideal)=70.96%, Cov=88.51%, SelfP=5.05, DOALL
24  LOOP matrixmul.cpp [ 148 - 181]:      OnMult
3  FUNC matrixmul.cpp [ 142 - 142]:      OnMult called at file matrixmul.cpp, line 397
26
5  [ 1] TimeRed(4)=3.10%, TimeRed(Ideal)=3.37%, Cov=4.13%, SelfP=5.44, DOALL
26  LOOP matrixmul.cpp [ 149 - 167]:      OnMult
7  FUNC matrixmul.cpp [ 142 - 142]:      OnMult called at file matrixmul.cpp, line 397
36
9  [ 2] TimeRed(4)=3.10%, TimeRed(Ideal)=3.37%, Cov=4.13%, SelfP=5.44, DOALL
10  LOOP matrixmul.cpp [ 149 - 161]:      OnMult
11  FUNC matrixmul.cpp [ 142 - 142]:      OnMult called at file matrixmul.cpp, line 397
34

```

### 5.2.2 Kremlin report for Matrix Multiplication, MultLine version

Listing 5.9: Kremlin's indication of the blocks that should be parallelised and theoretical variables that where calculated for MultLine algorithm version

```

36
1  .....
38  [ 0] TimeRed(4)=63.01%, TimeRed(Ideal)=63.20%, Cov=84.02%, SelfP=4.03, DOALL
3  LOOP matrixmul.cpp [ 213 - 247]:      OnMultLine
40  FUNC matrixmul.cpp [ 207 - 207]:      OnMultLine called at file matrixmul.cpp, line 400
5
40  [ 1] TimeRed(4)=2.86%, TimeRed(Ideal)=2.96%, Cov=3.81%, SelfP=4.45, DOALL

```

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7	LOOP matrixmul.cpp [ 213 - 235]: OnMultLine	
8	FUNC matrixmul.cpp [ 207 - 207]: OnMultLine called at file matrixmul.cpp, line 400	2
9		
10	[ 2] TimeRed(4)=2.86%, TimeRed(Ideal)=2.96%, Cov=3.81%, SelfP=4.45, DOALL	4
11	LOOP matrixmul.cpp [ 213 - 231]: OnMultLine	
12	FUNC matrixmul.cpp [ 207 - 207]: OnMultLine called at file matrixmul.cpp, line 400	6
13		
14	[ 3] TimeRed(4)=2.86%, TimeRed(Ideal)=2.96%, Cov=3.81%, SelfP=4.45, DOALL	8
15	LOOP matrixmul.cpp [ 213 - 225]: OnMultLine	
16	FUNC matrixmul.cpp [ 207 - 207]: OnMultLine called at file matrixmul.cpp, line 400	10