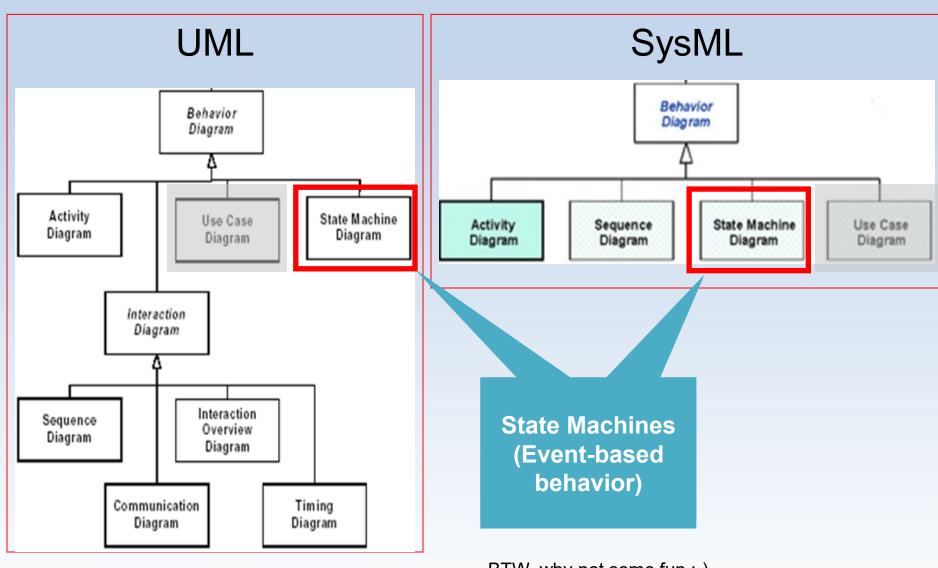


Behavioural Modelling (with UML and SysML)

- Event Based Behavior => State Machines
- Black-blox view => Use Cases
- ...also
 - Flow-Based Behaviour
 - Message Based Behaviour

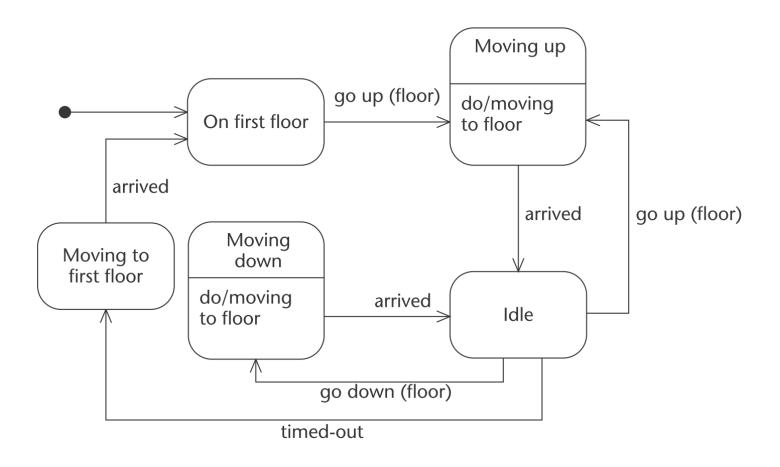
Behaviour



BTW, why not some fun ;-) https://www.youtube.com/watch?v=ABA3TGQVhTg

State Machine Diagrams (UML & SysML)

- A state machine diagram models the lifecycle of an entity.
- It specifies how state changes as a response to events.



States

- A state represents a situation where an invariant condition holds.
- Invariant conditions are the properties that the system satisfies in every reachable state.

- An invariant condition can be:
 - static (e.g. waiting for an event)

or

dynamic (e.g. performing a set of activities)

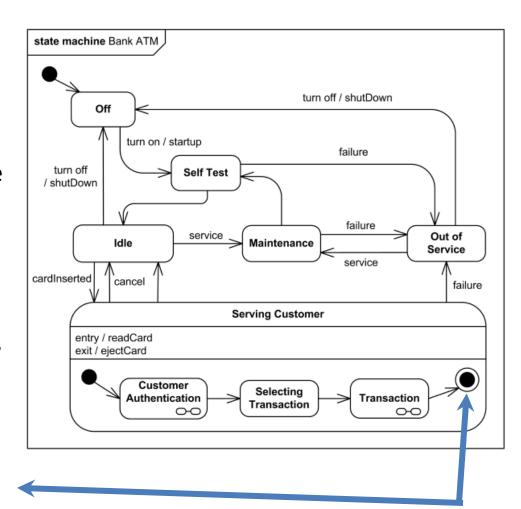
Entity Life Cycle

Transition diagrams define two kinds of constraints on entity life cycles:

- The set of allowed states. This is a static constraint.
- The set of legal sequences of these states. This is a transition constraint because it involves two or more states.

The **life cycle** of an entity **e** at time **t** is the sequence of states activated by **e** since its creation until **t**.

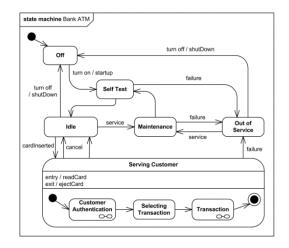
A life cycle is said to be **complete** if its last state is always a final state.

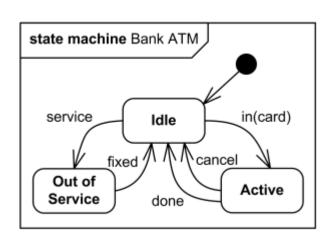


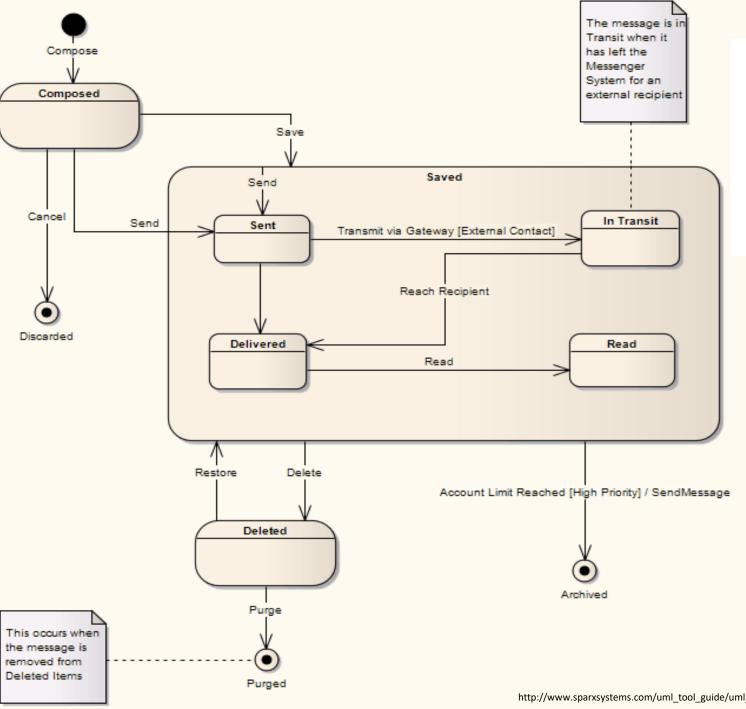
UML/SysML state machines

- Entity Behavior can be modeled as state machines.
- In principle, not all entities need to be modeled as state machines.
- Transition diagrams are an effective mechanism for defining the behavior of the instances of some entity types.
- However, they are not intended to be the best mechanism in all cases. This is one reason why some conceptual modeling languages offer several ways to model behavior.

Conceptual Modeling of Information Systems (chapter 13, page 301)







An example of a State **Machine Diagram**

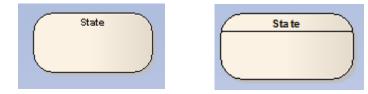
States...

- The State Machine concept can be used for modeling discrete behavior through finite state transition systems.
- The state machine represents behavior as the state history of an object in terms of its transitions and states.
- The activities that are invoked during the transition, entry, and exit of the states, are specified along with the associated event and guard conditions.
- A composite state has nested states that can be sequential or concurrent

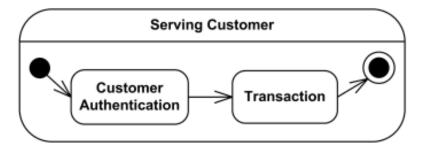
Simples States and Composite States

There are two types of states

Simple States

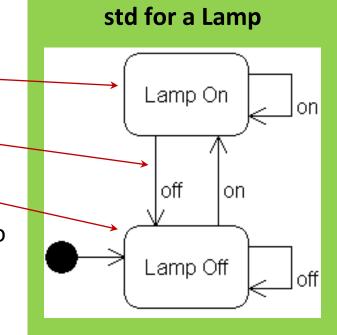


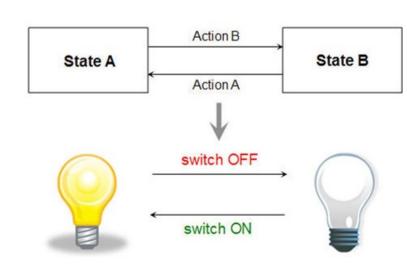
Composite States: a state that contains other states (substates), allowing the construction of hierarchical (or composite) state machines



State Transitions

- A transition is based on:
 - a source state s,
 - an event **d**,
 - a target state *t*.
- When the state machine is in state s and it receives event d for which there is a transition to state t, then the transition is said to be enabled.
 - The event d is called the trigger of the transition.
 - One says that d fires the transition.
- If the machine receives an event which is not the trigger of any transition then the machine is unaffected by the event.





State Transitions

An example of a transition of a rental car is:

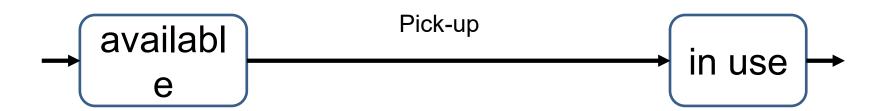
Source state: available

Trigger: domain event <u>Pick-Up</u>

– Target state: in use

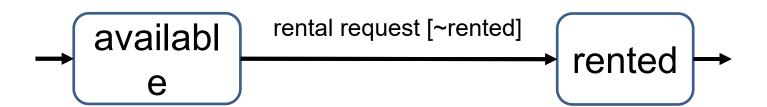
The meaning is that:

- When a car is in the state available ...
 - and the domain event <u>Pick-Up</u> of that car occurs,
 - Then, the new state of the car is in use.



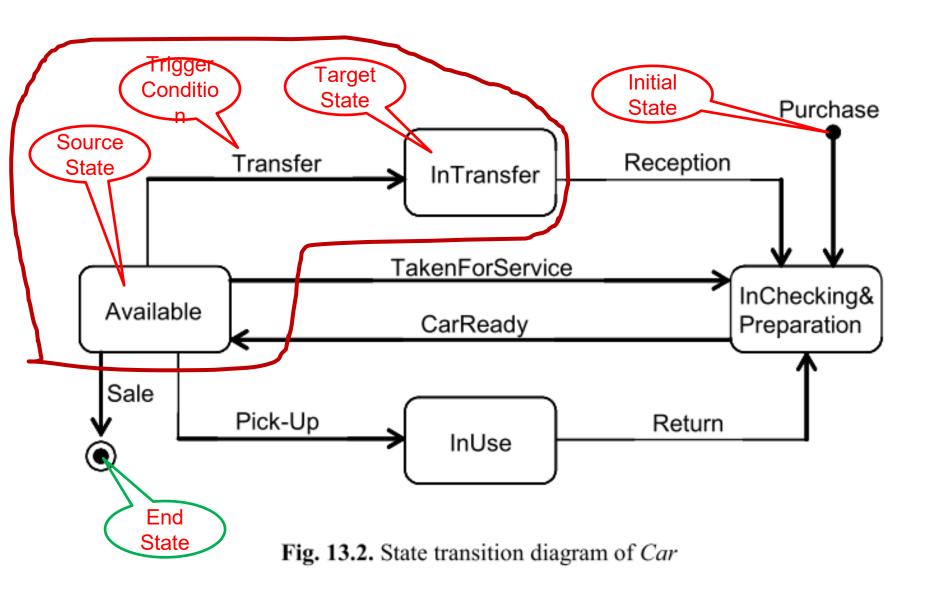
State Transitions and Guards

- Example of a transition of a rent car with a guard is:
 - Source state: available
 - Guard: the car is not booked for maintenance
 - Trigger: domain event Rental Request
 - Target state: rented
- The meaning is that:
 - when a car is in the state Available
 - and the domain event <u>Rental Request</u> occurs,
 - if the car is not booked for maintenance
 - then the new state of the car is rented.



```
transition ::= [ triggers ] [ guard ] [ '/' behavior-expression ]
triggers ::= trigger [ ',' trigger ]*
guard ::= '[' constraint ']'
```

State Transitions (UML / SysML)



Example: Telephone (partial)

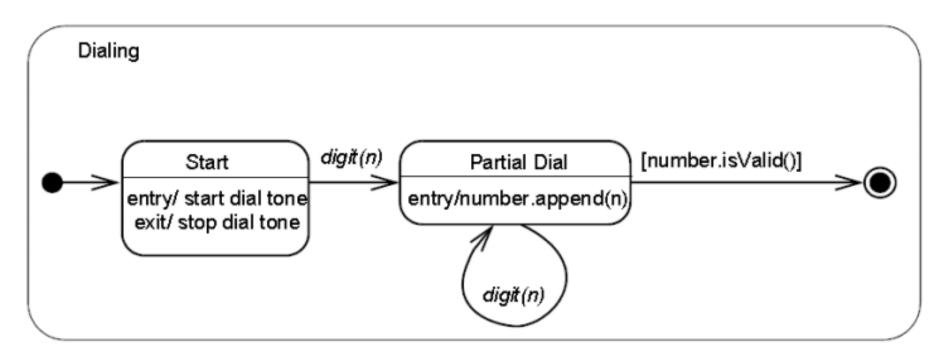


Figure 14.7 Composite State with two States

Example: Telephone

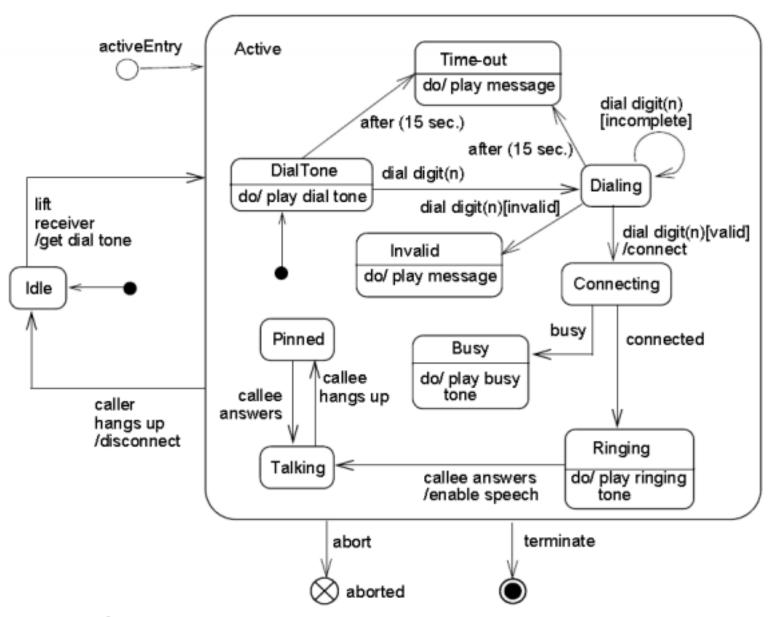


Figure 14.36 StateMachine diagram representing a telephone

Remembering Moore and Mealy Machines

A Moore machine

associates an output to a state.

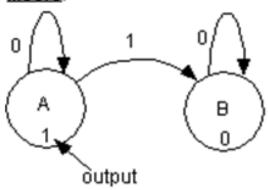
Each state has exactly one output. Every time the machine reaches a target state it produces the output associated with that state.

A Mealy machine

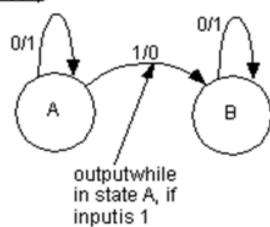
associates an output to a transition.

Every time the machine makes a transition to a target state it produces the output associated with that transition.

<u>Moore</u>:



Mealy:



It can be shown that for each Moore machine there exists at least one Mealy machine that produces the same outputs for all possible inputs, an vice-versa. Hence, Moore and Mealy machines are said to be equivalent.

UML/SysML state machines

 UML/SysML state machines have the characteristics of both Mealy machines and Moore machines.

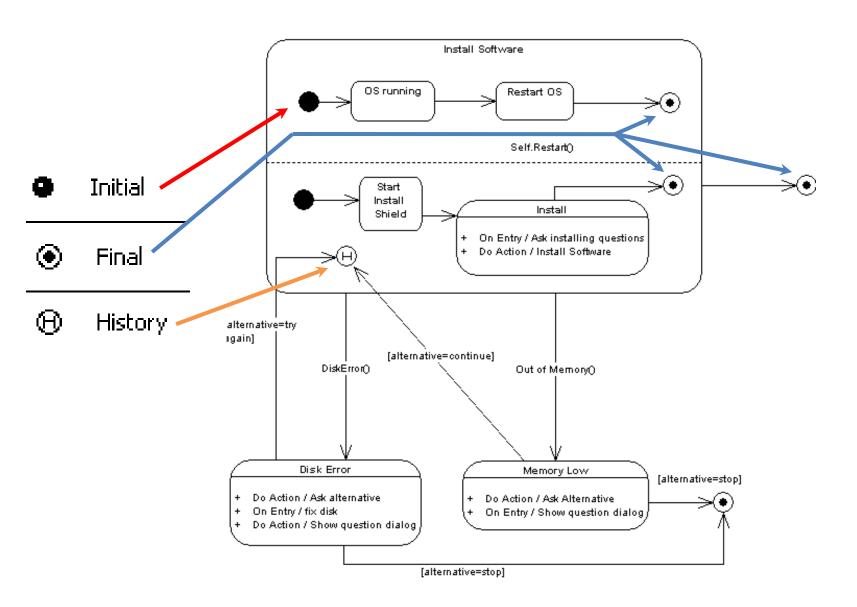
- They support entry and exit actions, which are associated with states rather than transitions, as in Moore machines.
- They support actions that depend on both the state of the system and the triggering event, as in Mealy machines.

- A pseudostate is an abstract state used to represent the start, end or internally to connect other stated of the diagram.
- The following pseudostates are defined in UML/SysML.

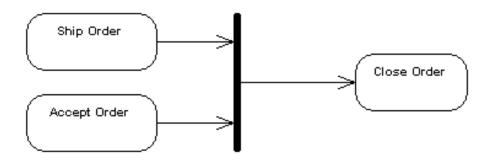
•	Initial	— Fork√Join	\Diamond	Choice
•	Final	 Fork/Join	•	Junction
Θ	History			

- Initial
- Final
- (H) History

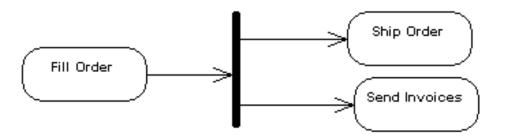
- Initial state Represents the first state that occurs when entering a region. An initial state must be present in each region.
- Terminal/Final state Represents the final state of a region and ends the execution of all states within that region.
- History Specifies that, when the statechart leaves and then returns to a region, the statechart enters the substate that was active when the statechart left the region.
 - Cf. **shallow** and **deep** history on the book.



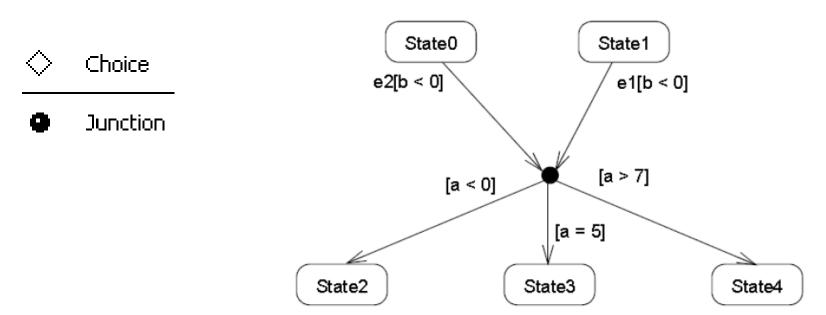
- Fork√Join
 - Fork/Join
- Join serve to merge several transitions, with no guards, emanating from one or more source states.



 Fork - serve to split an incoming transition, with no guard, into two or more transitions.



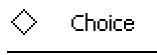
Pseudostates - Junction



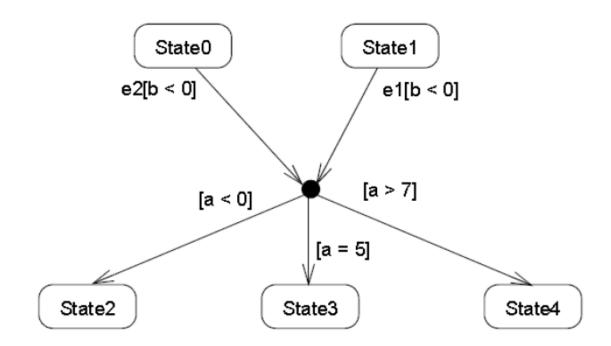
Junction

- A pseudostate with at least one incoming and one outgoing transition. In general, the
 incoming transitions have a trigger, and both the incoming and the outgoing transitions have a
 guard.
- A junction provides a means to simplify two or more transitions by factoring out their common parts. For example, a junction can be used to converge multiple incoming transitions into a single outgoing transition representing a shared transition path.
- Conversely, a junction can be used to split an incoming transition into multiple outgoing transition segments with different guard conditions.

Pseudostates - Junction



Junction

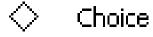


Example of a junction to simplify four transitions:

- The transition from State0 to State3 triggered by e2 and guarded by the condition ["b<0" and "a=5"]
- The transition from StateO to State4 triggered by e2 and guarded by the condition ["b<O" and "a>7"]
- The transition from State1 to State2 triggered by e1 and guarded by the condition ["b<0" and "a<0"]

• ..

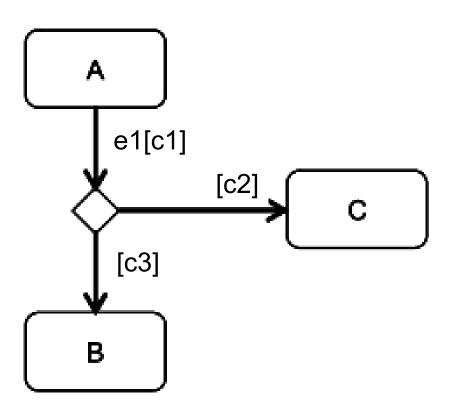
Pseudostates - Choice



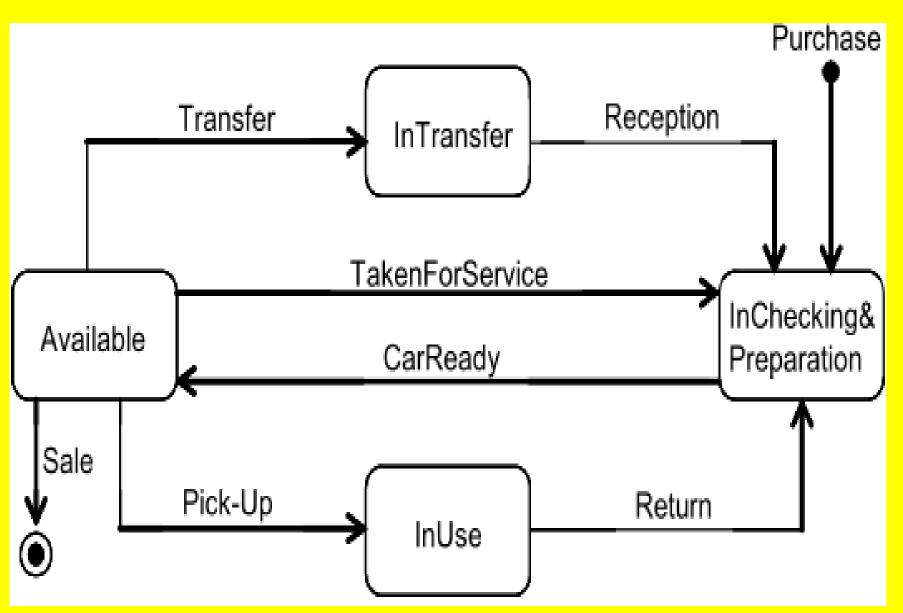
Junction

Choice

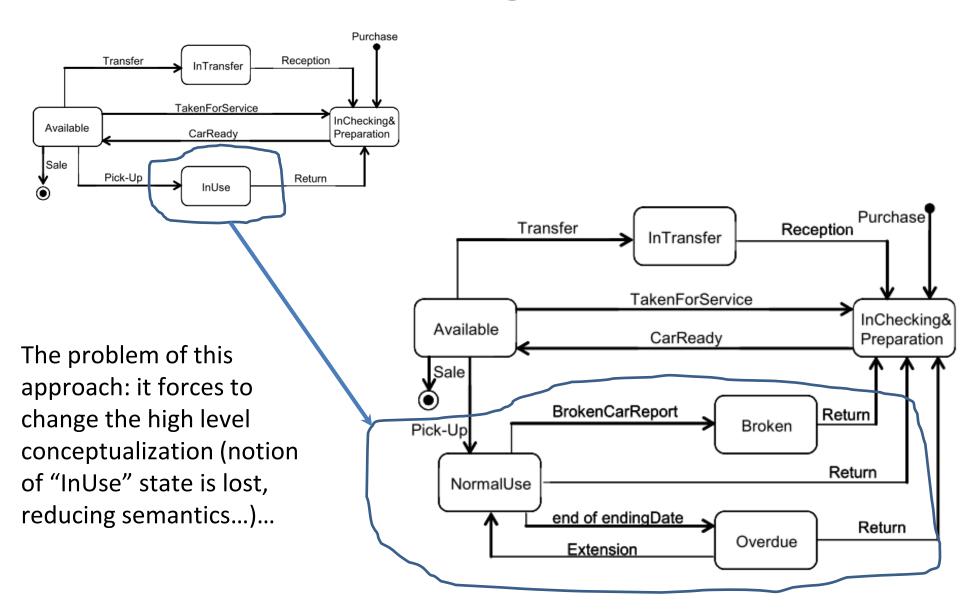
- Similar to a junction, but the guards of the outgoing transitions are evaluated once the incoming transitions have produced their effect.
- Choices are considered as dynamic conditional branches because the target state is not known until the operations associated with the incoming transitions have been completed.



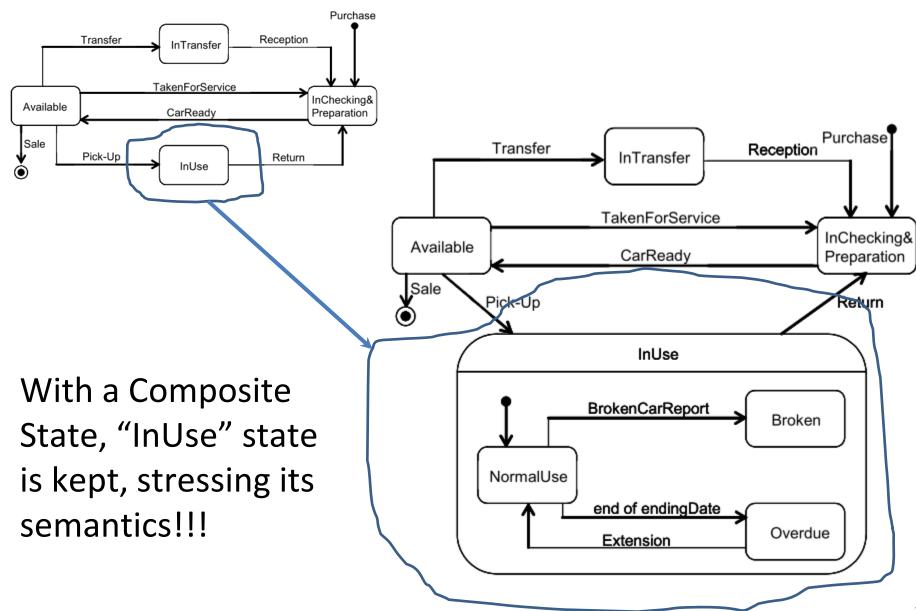
Example: Car rental



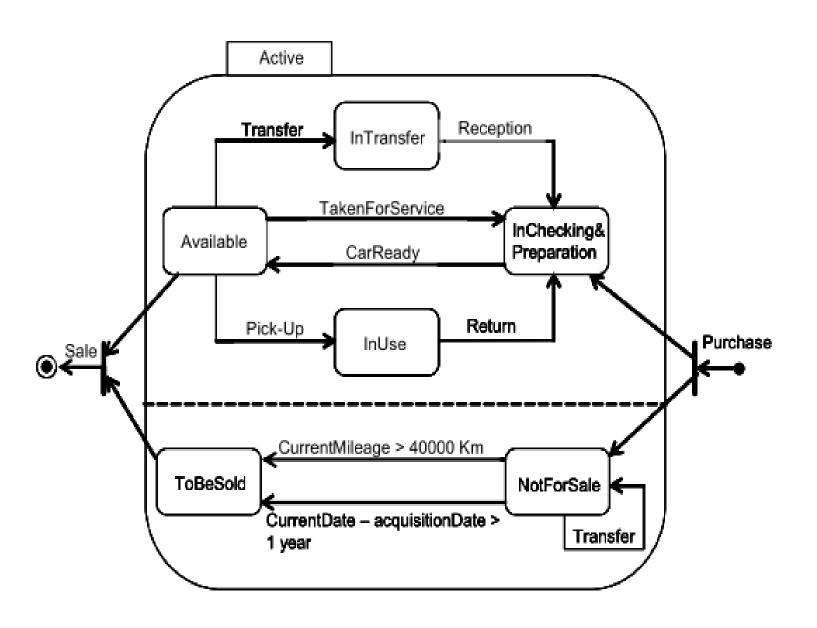
Detailing states



Detailing states



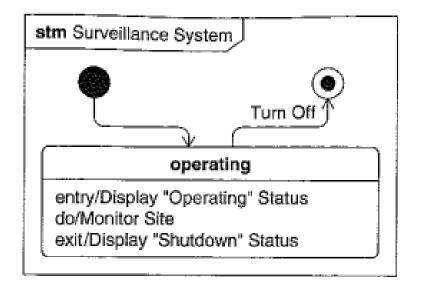
Parallelism



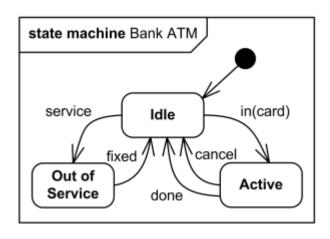
States and Behaviours

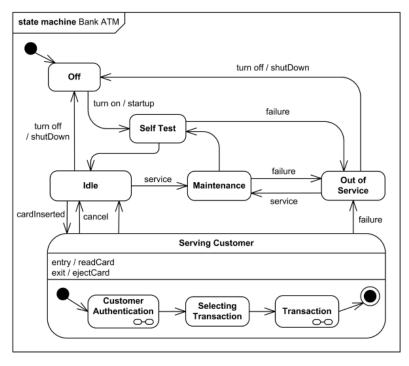
- Behaviours
 - entry/...action...
 - do/...action...
 - exit/...action....

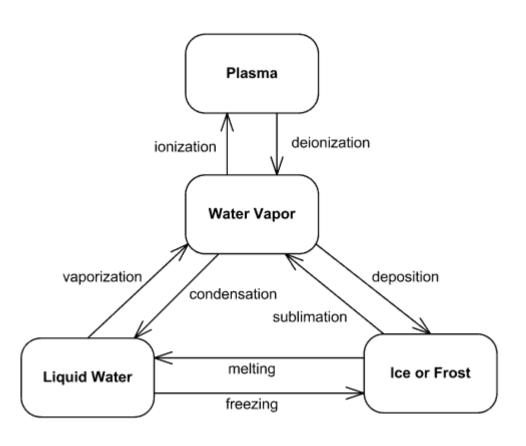




State Diagrams as Event-based Behaviour

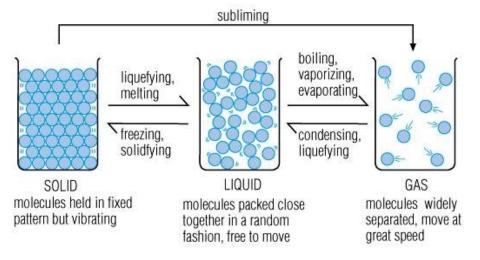


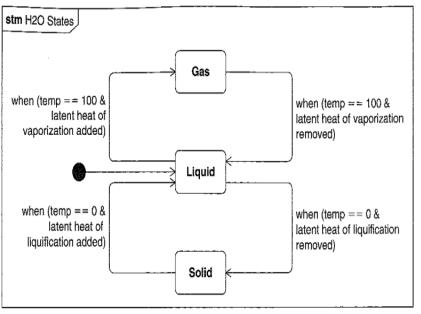


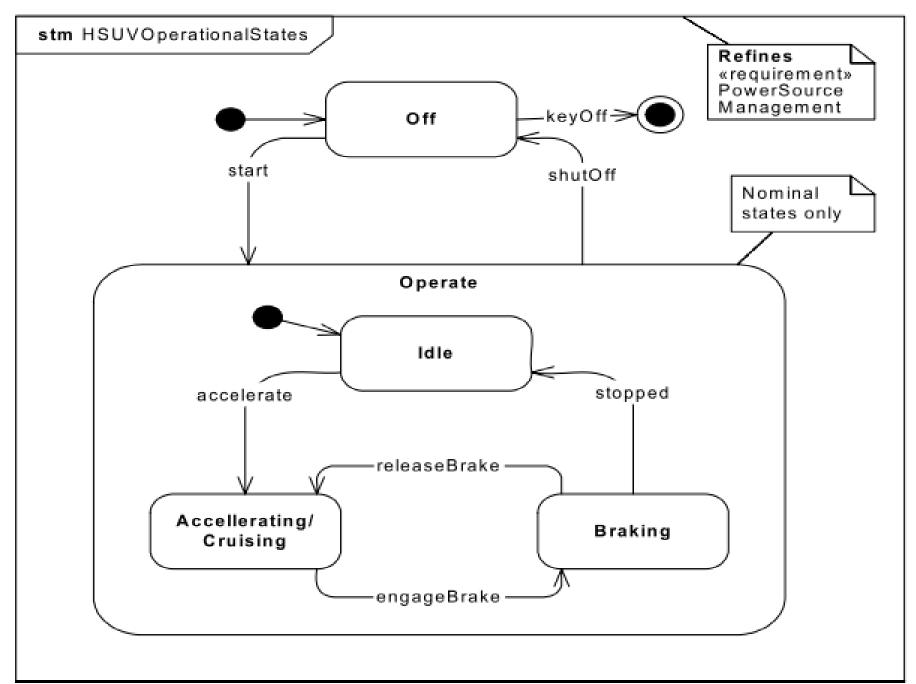


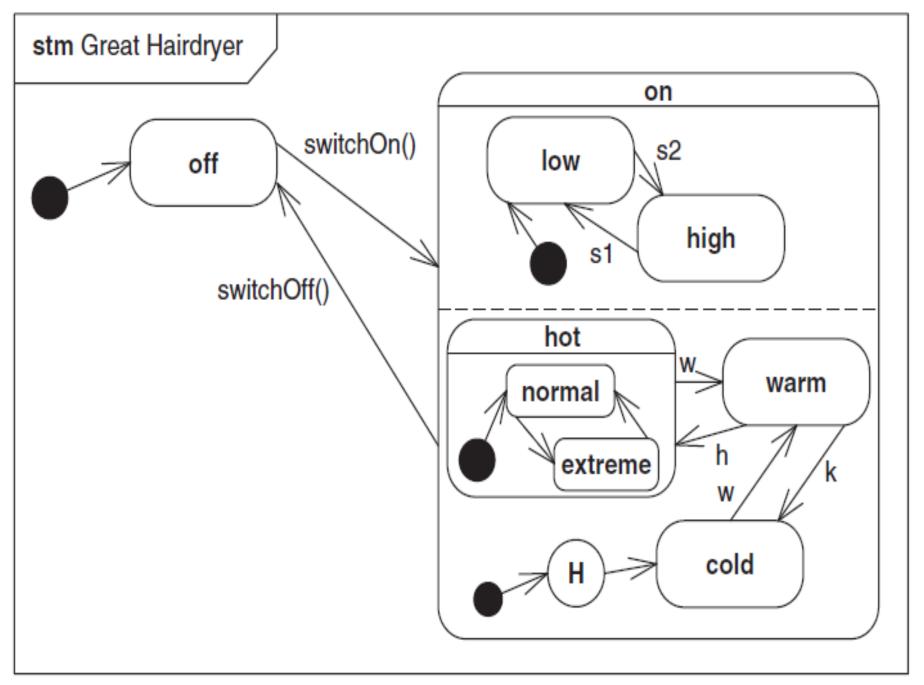
H_2O

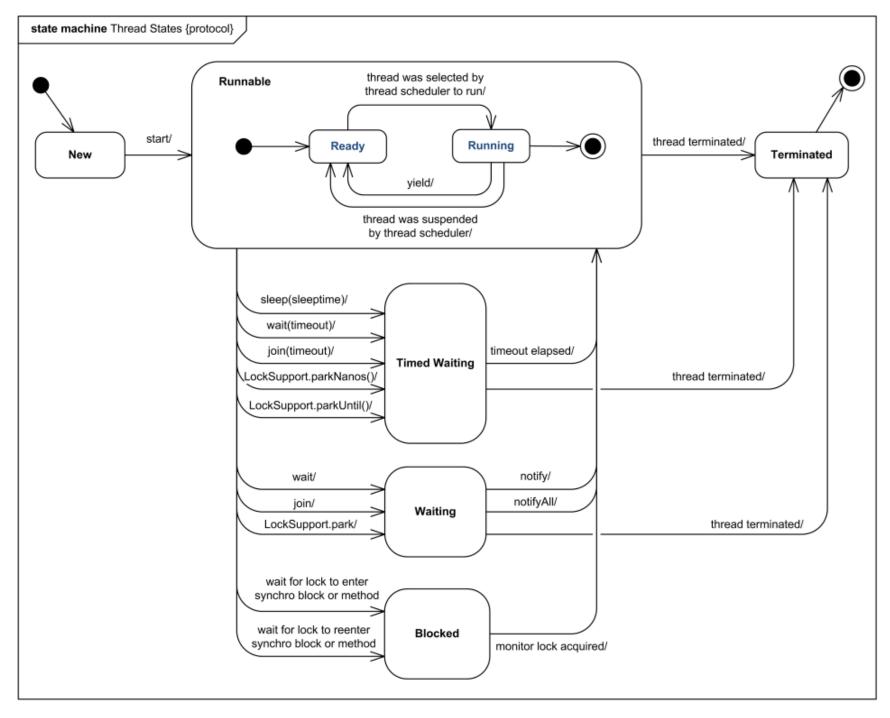
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State Machine Diagrama Overview

