L1T DQM Status Update

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L1TBPTX test

- Two attempt to test L1TBPTX module were made at P5 to test L1TBPTX module last week

    - Test done between RAMP and ADJUST

    - Test consisted on disabling ENABLE_TT_LVDS_X (x=0,1,4) at PSB_SLOT_9

    - Expected BPTX trigger to be disabled which the new tools should see.

- What actually happen:

    - GT ouput rates went to 600hz (just the randoms)

    - L1 BPTX AND and preBPTX veto trigger rate did not change and single BPTX triggers actually increased rate. (seen from WbM)

    - Since trigger where still present L1TBPTX did not see any problem, and also almost no events actually go to DQM (only randoms firing).

L1TRCT problem

- L1TRCT code had a issue for several months.

  - The emulator code would pick up the the wrong FED for the first run after a change to the included FED list.

- I got together experts from DAQ and DD to figure out the problem.

  - A bug was identified which causes this problem.

  - Suggestions where made to how to correct this problem and to improve the code to.

  - Alexander Savin is now working making the fix for this.

- More details at: https://savannah.cern.ch/bugs/?98366

Request for storage space at P5

- A request was made to mount 500Gb for DQM testing at P5 machine dqm-c2d07-18.cms

  - This is an official Central DQM maintained machine for development purposes

  - This will allow having some local files for testing in the online environment before playback testing.

  - The current online code differs from the current release by a significant amount of tags so each install occupies a large amount of space

    - This area can be use for that

New DQM render pluggin

- New render plugin responsible for the "L1T Summary Map" was deployed

  - Deployed on Wednesday

  - Code render changes to L1T Summary map introduced by Vasile some months ago.

  - Quality tests now need to be tunned for this information to be valid

    - Subsystem collaboration will be needed here.

  - Shifter instructions need to be updated accordingly

New CMSSW tags I

- Current L1T code was successfully ported to CMSSW_6_1_X
  - Code was queue for release CMSSW_6_1_0_pre5 or CMSSW_6_1_0_pre6
    - Timescale of deployment to officially release 1-2 weeks
  - This likely will be the release used for the full data RERECO during 2013.
- All new online code ported to offline where possible
  - Included recent changes to L1TRPC and L1TLTC modules

New CMSSW tags II

- Tags queue for deployment
    - DQM/L1TMonitor V06-02-20
    - DQM/L1TMonitorClient V07-00-03
    - DQMOffline/L1Trigger V00-00-08
- New code modules development underway to included on pre6/pre7
    - Rates, Synchronization, Efficiency and Turn on Curves.

To Do List

- Improve Layouts
  - Shift layout will be updated today/tomorrow
  - Others will follow
- Fix some bugs on the online code
- Port and develop more tools for offline DQM code.