

# MC VBF+MET QCD Samples Status

J. Pela

Imperial College London

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For the GEN-SIM step:

## Step I - Status

Sample	Jobs	Done
VBFAQCD_Pt_80to120_MET40_step1_part1_v1	5000	4166
VBFAQCD_Pt_80to120_MET40_step1_part2_v1	5000	3216
VBFAQCD_Pt_120to170_MET40_step1_v1	3500	1255
VBFAQCD_Pt_170to300_MET40_step1_v1	2750	2040
VBFAQCD_Pt_300to470_MET40_step1_v1	400	330
VBFAQCD_Pt_470to600_MET40_step1_v1	250	250

For the REDIGI adding PU step (partial sample):

## Step II - Status

Sample	Jobs	Done
VBFAQCD_Pt_80to120_MET40_step2_v1	336	121
VBFAQCD_Pt_120to170_MET40_step2_v1	9	5
VBFAQCD_Pt_170to300_MET40_step2_v1	49	27
VBFAQCD_Pt_300to470_MET40_step2_v1	67	28
VBFAQCD_Pt_470to600_MET40_step2_v1	5	4

## Status and next steps

- Had to resubmit step 1... forgot to include SIM on in (removed for my initial tests)
- Step 2 now includes samples indicated by Phat for PU and first test jobs over step one successfully finished
- Already tested steps 1 to 3 offline and all works.
- Now using white list for step 1 which greatly reduced errors