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Parallel Computing - Computer Science INE/UFSC, Florianópolis

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## Presentation Outline

### Introduction

### Virtualization

Post-Copy Migration

### Metrics

LW Processors

- Introduction
- Virtualization
- 3 Migration
  - Pre-Copy Migration
  - Post-Copy Migration
- Metrics
- LW Processors
  - Kalray MPPA-256
- Motivation and Justification



#### Introduction

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### Introduction

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Motivation and Justification

### Transferring a process between machines

- Proposed for:
  - Load balancing
  - Fault tolerance
  - System administration
  - Data access locality
- Arising of distributed systems
- Solutions established:
  - MOSIX (1985)
  - V (1988)
  - OSF/1 AD TNC (1995)



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- Transferring a process between machines
- Proposed for:
  - Load balancing
  - Fault tolerance
  - Improved system administration
  - Data access locality
- Arising of distributed systems
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  - MOSIX (1985)
  - V (1988)
  - OSF/1 AD TNC (1995)



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**IW Processors** 

- Despite the research efforts, migration has not achieved widespread use
- Treating residual dependency is a difficult task:
  - Open files stored in the source node
  - Open communicators with other processes
  - Shared resources
  - Internal kernel state.
- Solution: Virtualization



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Motivation and Justification

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## Virtualization

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### Create a Virtual Version of Computing Resources

- CPU
- Memory
- Storage device
- Network device

### Features

- Improved previous features
- Power management
- Security

### Two types

- Virtual Machines (VM)
- Containers



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## Virtualization

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- Memory
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## Two types

- Virtual Machines (VM)
- Containers



# Virtual Machines (VM)

### Introduction

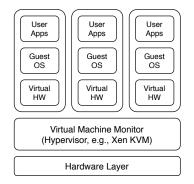
### Virtualization

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- Encapsulates a whole operating system
- Depends on a Hypervisor to provide the hardware resources
  - Full virtualization
  - Paravirtualization





# Migration of VMs

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### Memory content

- Hypervisor allocated memory
- Guest OS allocated memory
- Application requested memory
- Disk content
  - Hypervisor allocated blocks
  - Guest OS used blocks
  - Application used blocks



## Migration of VMs

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### Memory content

- Hypervisor allocated memory
- Guest OS allocated memory
- Application requested memory

### Disk content

- Hypervisor allocated blocks
- Guest OS used blocks
- Application used blocks



### Containers

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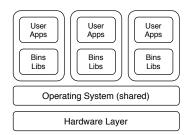
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### OS-level virtualization

- Multiple isolated user space instances (Shared OS)
- Lightweight compared to VMs
  - Linux Containers
  - Docker





# Migration of Containers

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- Transfer the file system of the container
- Save the state of the container into a file (all processes and their resources)
- Transfer the container file
- Restart the container



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# Migration



## Migration Techniques

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### Generally, there are three stages of memory transfer:

- Push Copy stage (iteration copy)
- Stop-and-Copy stage
- Pull Copy stage (on-demand copy)
- Migration techniques
  - Stop-and-Copy
  - Pre-Copy
  - Post-Copy
  - Hybrid



# Migration Techniques

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### ■ Generally, there are three stages of memory transfer:

- Push Copy stage (iteration copy)
- Stop-and-Copy stage
- Pull Copy stage (on-demand copy)

### Migration techniques:

- Stop-and-Copy
- Pre-Copy
- Post-Copy
- Hybrid



### Introduction

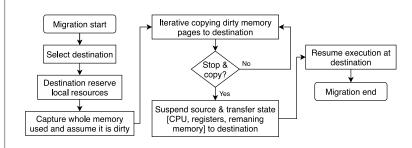
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**Pre-Copy** Scenario



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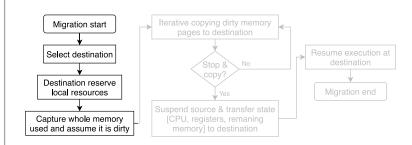
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Resource reservation and transfer preparation



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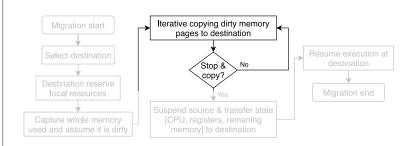
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**Sending the memory pages** before the execution context

### **Push Copy Stage**



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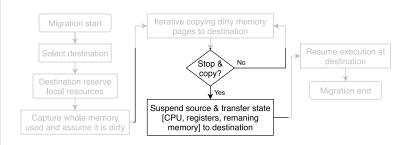
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When enough memory has been transferred, send the execution context Stop-and-Copy Stage



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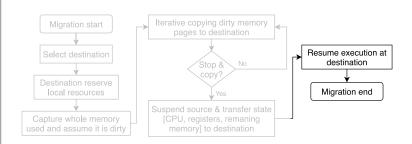
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**Resume stopped execution context** in the destination



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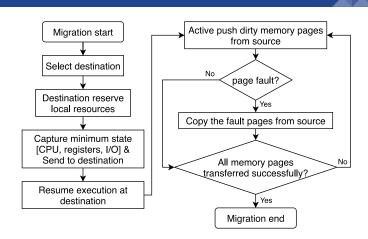
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Post-Copy Scenario



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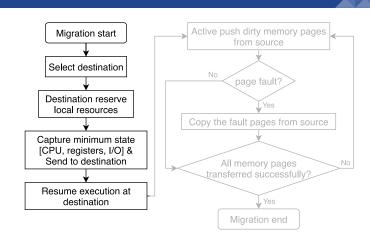
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### Sends execution context before memory pages





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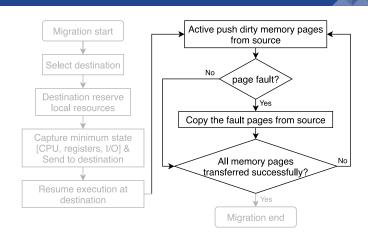
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Merges pull rest of memory pages and any page that generate page faults **Pull Copy Stage** 



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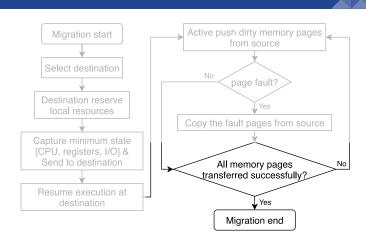
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### Continues execution normally



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## Metrics



## Performance Metrics

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### ■ Total Migration Time

- Downtime
- Pages Transfered
- Preparation Time
- Resume Time
- Application Degradation
- Page Dirty Rate
- Link Degradation



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## **LW Processors**



## Lightweight Manycores Processors

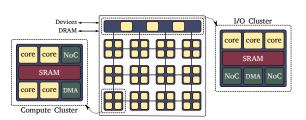
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Overview of a Manycore

### Hundreds of Lightweight Cores

- Expose Massive thread-level parallelism
- Feature low-power consumption
- Target MIMD workloads
- Distributed Memory Architecture
- On-Chip Heterogeneity



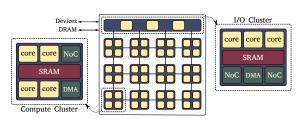
## Lightweight Manycores Processors

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### LW Processors



Overview of a Manycore

- Hundreds of Lightweight Cores
- **Distributed Memory Architecture** 
  - Grants scalability
  - Relies on a Network-on-Chip (NoC)
  - Has constrained memory systems
- On-Chip Heterogeneity



## Lightweight Manycores Processors

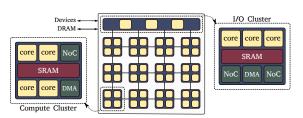
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Overview of a Manycore

- Hundreds of Lightweight Cores
- Distributed Memory Architecture
- On-Chip Heterogeneity
  - Features different components



## Kalray MPPA-256

A Lightweight Manycore Processor

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### ■ 288 processing cores

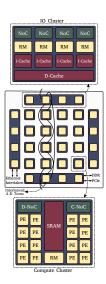
- 16 Compute Cluster (CC)
- 4 I/O Cluster (IO)

## Data NoC (D-NoC)

- 256 RX slots
- 8 TX channels
- $\blacksquare$  8  $\mu$ threads for async TX

## Control NoC (C-NoC)

- 128 RX slots
- 4 TX channels





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# Motivation and Justification



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- Load balancing
- Fault tolerance
- System administration
- Data locality
- Power management
- Security



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#### Motivation and Justification

■ Bring the benefits of migration and process virtualization to lightweight manycores

# Load balancing

- Better work distribution
- Fault tolerance
- System administration
- Data locality
- Power management
- Security



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#### Motivation and Justification

- Load balancing
- Fault tolerance
  - Save/restore processes state
- System administration
- Data locality
- Power management
- Security



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#### Motivation and Justification

- Load balancing
- Fault tolerance
- System administration
  - Allow multiuser execution and management
- Data locality
- Power management
- Security



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### Motivation and Justification

- Load balancing
- Fault tolerance
- System administration
- **Data locality** 
  - Move processes to closer to the data source
- Power management
- Security



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# Virtualization

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## Metrics

LW Processors

### Motivation and Justification

- Load balancing
- Fault tolerance
- System administration
- Data locality
- Power management
  - Separate CPU-bound processes to improve heat dissipation
- Security



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### Motivation and Justification

- Load balancing
- Fault tolerance
- System administration
- Data locality
- Power management
- Security
  - Better isolation and kill suspicious processes



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**IW Processors** 

### Motivation and Justification

■ Parallel and distributed nature of lightweight manycores

- Memory restrictions do not support a complete virtual environment like VMs
- Explore lighter forms of virtualization as at the OS level
- Integrate virtualization into a distributed OS designed for lightweight manycores



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Parallel and distributed nature of lightweight manycores

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Parallel and distributed nature of lightweight manycores

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# References I

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MILOJIclć, D. S. et al. Process migration. *ACM Comput. Surv.*, Association for Computing Machinery, New York, NY, USA, v. 32, n. 3, p. 241–299, set. 2000. ISSN 0360-0300. Disponível em: <a href="https://doi.org/10.1145/367701.367728">https://doi.org/10.1145/367701.367728</a>.



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#### Motivation and Justification

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### Motivation and Justification

- (MILOJIclć et al., 2000)
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- (Tao et al., 2019)
- (Hosseini Shirvani; RAHMANI; SAHAFI, 2020)
- (WANG et al., 2019)

