TESTBENCH

TOP

ARM

CONDLOGIC

CONTROLLER

DECODER

DATAPATH

DMEM

IMEM

Clk

Reset

Instr
31:0

ReadData
31:0

Clk

Reset

Clk

Reset

Instr
31:12

Aluflags

ReadData
31:0

ALUSrc

ALUControl

MemWrite

MemToReg

PCSrc

ImmSrc

RegSrc

RegWrite

PC

ALUFlags

ALUResult

WriteData

Clk

We

a
31:0

wd
31:0

rd
31:0

MemWrite

ALUResult; WriteData; PC
31:0

a
31:0

rd
31:0

MemWrite

DataAdr

WriteData