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The transmittal letter should identify the BAA number, the proposal by name, and the organizations proposal reference number (if any), and should be signed by an individual who is authorized to submit proposals to the Government.

1 Statement of Work (SOW)

The project’s aim is to develop a minaturized high-sensitivity, low-noise magnetic gradiometer. Our approach is to mimic the mechanism found in magnetosomes, the specialized cells four from bacteria to higher vertebrates such as fish and birds (see 2). This is comprised of four main tasks: modeling and simulation, microfabrication process design, circuit design, and device manufacture and testing. Each Phase (1,2,3) will include these four tasks.

Metric	Phase 1	Phase 2	Phase 3
Power consumption	150 mW	50 mW	100 mW
Sensor Volume	3x3x10 cm	1x1x7 cm	1x1x7 cm
Control Electronics Volume	N/A	N/A	1 20cm ²
Ambient Magnetic Field	$\pm 100 \mu\text{T}$	$\pm 100 \mu\text{T}$	$\pm 100 \mu\text{T}$
Ambient Operating Temperature	N/A	0° C to 50° C	0° C to 50° C
Gradient Full-scale Range	1 nT/cm	1 nT/cm	1 nT/cm
Gradient Sensitivity	10 fT/cm/ $\sqrt{\text{Hz}}$	3 fT/cm/ $\sqrt{\text{Hz}}$	1 fT/cm/ $\sqrt{\text{Hz}}$
Gradient Accuracy	100 fT/cm	30 fT/cm	10 fT/cm
Total Field Range	100 μT	100 μT	100 μT
Total Field Sensitivity	100 pT/ $\sqrt{\text{Hz}}$	30 pT/ $\sqrt{\text{Hz}}$	10 pT/ $\sqrt{\text{Hz}}$
Total Field Accuracy	1 nT	500 pT	100 pT
Data Rate	100/s	200/s	500/s
3 dB Bandwidth	200 Hz	400 Hz	1000 Hz

Table 1: Design objectives, by phase.

1.1 Phase 1

AMBIENT Phase 1 will demonstrate sensor functionality and performance in a laboratory setting meeting the performance metrics as indicated in Table 1.

1.1.1 Modeling and simulation

- The objective in this phase is to simulate the MEMS device, taking into account geometry, material properties, and multiphysics interactions, in order to determine the range of acceptable design options for a single element. Multiple options can be fabricated for real-world testing.
- Our approach here is to initially specify geometry and physical properties by hand calculation, then investigate more deeply in a finite-element multiphysics solver.
- This task will be accomplished at UF by Joaquin Casanova.
- Completion of this task is specified by successful simulation of the MEMS device that meets the physical requirements specified in the BAA.
- Deliverables include successful simulation results and design parameters.

- No government equipment is required.
- To reduce risk of later failure due to non-manufacturability, this task will be accomplished within fabrication constraints specified by the MEMS team at UF. This ensures the design is physically realizable. In parallel, other sensing modalities could be explored in the case that the MEMS cantilever is not practical.

1.1.2 Microfabrication

- The objective in this phase is to develop a microfabrication strategy (materials, deposition, patterning) that can meet the requirements found from simulation.
- Our approach here is to find a range of dimensional constraints and material types that could be physically realizable, and use those options to guide simulation and design of a sensor.
- This task will be accomplished at UF by Dr. YK Yoon.
- Completion of this task is successful specification of a microfabrication plan for the sensor described by the simulation results.
- Deliverables include successful microfabrication plan.
- No government equipment is required.
- To reduce risk of later failure due to non-manufacturability, this task will be accomplished with parallel investigating of alternative MEMS sensing modalities.

1.1.3 Circuit design

- The objective in this phase is to develop a PCB-based detection circuit which amplifies and digitizes the the voltage produced by the MEMS elements. In addition to a COTS circuit, we will simultaneously begin investigation and design of an SiGe IC that can be used in later phases.
- Our approach here is to drive the cantilevers at their resonant frequency to enhance sensitivity, amplifying the signal, then demodulate. Banks of sensors will be individually addressable.
- This task will be accomplished at TTU by Dr. Changzi Li.
- Completion of this task is specified by successful design of a circuit capable of amplifying and digitizing MEMS output voltage with input-referred voltage noise low enough to meet the overall sensitivity requirement.
- Deliverables include successful design and prototype of the circuit, usable with the fabricated MEMS sensor head, using COTS components.
- No government equipment is required.
- To reduce risk of later failure due to excessive noise, this task will investigate only specifically low-noise designs. Dynamic element matching will permit cancellation of manufacturing variation cancellation. The Phase 1 circuit will be completed with COTS components.

1.1.4 Manufacture and testing

- The objective in this phase is to construct the MEMS design using microfabrication processes and design developed in 1.1.1,1.1.2, which includes pads for connection to circuitry.
- Our approach here is to test the circuit and sensor independently and together after manufacturing as in 1.1.2. Multiple MEMS design options can be fabricated for testing.
- This task will be accomplished at UF by Dr. Yoon and Dr. Joaquin Casanova.
- Completion of this task is specified by successful construction and testing of a sensor head, which satisfies Table 1, using the circuitry developed in 1.1.3.
- Deliverables include successful prototype of the sensor and sensor electronics.
- No government equipment is required.
- To reduce risk of failure, fabrication will begin early in Phase 1, enabling us to revise our design to reduce manufacturing defects orenhance sensivity.

1.2 Phase 2

AMBIIENT Phase 2 will develop and demonstrate an integrated sensor head meeting the performance and SWaP metrics of Table 1, and including all vacuum, photonic, and thermal control components.

1.2.1 Modeling and simulation

- The objective in this phase is to optimize the MEMS device, to decrease size, increase sensitivity, and construct in arrays, based on results from the Phase 1. Phase 1 measured results will be compared to simlation to refine accuracy of simulations and account for effects that may not have been considered.
- Our approach here is to first compared measured vs. simulated results, update our simulations, and then use the refined model to optimize a new design.
- This task will be accomplished at UF by Joaquin Casanova.
- Completion of this task is specified by successful simulation of the MEMS device that meets the physical requirements specified in the BAA for Phase 2.
- Deliverables include successful simulation results and design parameters.
- No government equipment is required.
- To reduce risk of later failure due to non-manufacturability, this task will be accomplished within fabrication constraints specified by the MEMS team at UF, and considering the results of the Phase 1 testing. This ensures the design is physically realizable and modeling is accurate.

1.2.2 Microfabrication

- The objective in this phase is to optimize the MEMS fabrication process, through material selection and process design, considering problems encountered in Phase 1.
- Our approach here is to improve the material selection and process design over Phase 1, in conjunction with Phase 2 simulations. For instance, incorporating higher-energy magnetic materials, or denser interconnects.
- This task will be accomplished at UF by YK Yoon.
- Completion of this task is specified by successful fabrication of the Phase 2 MEMS design.
- Deliverables include successful MEMS sensor, with pads for connection to the Phase 2 circuit.
- No government equipment is required.
- To reduce risk of later failure due to non-manufacturability (as a result of interconnect density, or damping effects, for instance), this task will also investigate an architecture with fewer, larger sensors.

1.2.3 Circuit design

- The objective in this phase is to optimize the PCB-based circuit based on the Phase 1 results and test this with a MEMS array, and incorporate the amplifier design into an integrated circuit, for testing with the Phase 2 MEMS array.
- Our approach here is to take the Phase 1 circuit design, modifying based on the modified MEMS design, and design from this an integrated circuit, to reduce size. SiGe is a likely process choice due to low noise.
- This task will be accomplished at TTU by Changzhi Li.
- Completion of this task is specified by successful design and fabrication of a sensor amplification IC.
- Deliverables include successful IC and design.
- No government equipment is required.
- To reduce risk of later failure due to missing deadlines, or circuit/MEMS mismatch, preliminary IC design will begin in Phase 1. Additionally, multiple designs can be incorporated into one IC.

1.2.4 Manufacture and testing

- The objective in this phase is to manufacture the IC and MEMS for Phase 2, and test these in the lab.

- Our approach here is to do MEMS fabrication at UF, and outsource the IC fabrication. The IC and MEMS sensor will be tested separately, and then together, once the functionality of each is established. The IC can be tested independently with dummy sources to verify function, and the MEMS sensor can be tested with a COTS circuit first. Varying field strength, frequency, and gradient using Helmholtz coils can establish sensitivity and noise floor of the sensor.
- This task will be accomplished at UF by Dr. Yoon and Dr. Joaquin Casanova.
- Completion of this task is specified by successful construction and testing of a sensor head, which satisfies Table 1, using the circuitry developed in 1.2.3 and MEMS in 1.2.1.
- Deliverables include successful prototype of the sensor and sensor electronics.
- No government equipment is required.
- To reduce risk of failure, fabrication will begin early in Phase 2, enabling us to revise our design to reduce manufacturing defects and enhance sensitivity.

1.3 Phase 3

AMBIENT Phase 3 will demonstrate a fully integrated gradiometer comprising all control electronics, power conditioning, and packaging, meeting all performance metrics of Table 1

1.3.1 Modeling and simulation

- The objective in this phase is to optimize the MEMS device, to decrease size and increase sensitivity, based on results from the Phase 2. Phase 2 measured results will be compared to simulation to refine accuracy of simulations and account for effects that may not have been considered. The Phase 3 design will be completed with circuit integration specifically in mind.
- Our approach here is to first compare measured vs. simulated results, update our simulations, and then use the refined model to optimize a new design.
- This task will be accomplished at UF by Joaquin Casanova.
- Completion of this task is specified by successful simulation of the MEMS device that meets the physical requirements specified in the BAA for Phase 3.
- Deliverables include successful simulation results and design parameters.
- No government equipment is required.
- To reduce risk of later failure due to non-manufacturability, this task will be accomplished within fabrication constraints specified by the MEMS team at UF, and considering the results of the Phase 2 testing. This ensures the design is physically realizable and modeling is accurate.

1.3.2 Microfabrication

- The objective in this phase is to optimize the MEMS fabrication process, through material selection and process design, considering problems encountered in Phase 2.
- Our approach here is to improve the material selection and process design over Phase 2, in conjunction with Phase 2 simulations. For instance, incorporating higher-energy magnetic materials, or denser interconnects. Additionally, the Phase 3 MEMS and Phase 3 circuit will be integrated into the same substrate, and packaged. The Phase 3 IC will be sliced and bonded onto a common substrate with the MEMS.
- This task will be accomplished at UF by YK Yoon and at TTU by Changzhi Li.
- Completion of this task is specified by successful fabrication of the Phase 3 MEMS design.
- Deliverables include successful MEMS sensor, with integrated Phase 3 sensor.
- No government equipment is required.
- To reduce risk of later failure because the IC and MEMS can't be integrated as part of the same substrate, we will also pursue the option of completing each separately and connecting.

1.3.3 Circuit design

- The objective in this phase is to optimize the SiGe IC based on the Phase 2 results and refined Phase 3 design, and full-scale integration of the IC and MEMS sensor array.
- Our approach here is to take the Phase 2 circuit design, modifying based on the modified MEMS design, adding an ADC, and design from this a single integrated circuit, to reduce size.
- This task will be accomplished at TTU by Changzhi Li.
- Completion of this task is specified by successful design and fabrication of a sensor amplification IC.
- Deliverables include successful IC and design.
- No government equipment is required.
- To reduce risk of later failure due to missing deadlines, or circuit/MEMS mismatch, preliminary Phase 3 IC design will begin in Phase 2. Additionally, multiple designs can be incorporated into one IC.

1.3.4 Manufacture and testing

- The objective in this phase is to manufacture the IC and MEMS for Phase 3, and test these in the lab.

- Our approach here is to do MEMS and the IC fabrication at UF, if possible. The IC and MEMS sensor will be tested separately, and then together, once the functionality of each is established. The IC can be tested independently with dummy sources to verify function, and the MEMS sensor can be tested with a COTS circuit first. Varying field strength, frequency, and gradient using Helmholtz coils can establish sensitivity and noise floor of the sensor.
- This task will be accomplished at UF by Dr. Yoon and Dr. Joaquin Casanova.
- Completion of this task is specified by successful construction and testing of a sensor head, which satisfies Table 1, using the circuitry developed in 1.3.3 and MEMS in 1.3.1.
- Deliverables include successful prototype of the sensor and sensor electronics.
- No government equipment is required.
- To reduce risk of failure, fabrication will begin early in Phase 3, enabling us to revise our design to reduce manufacturing defects and enhance sensitivity.

2 Innovative Claims

Our approach is to design a sensor based on a magnetoreceptive mechanism used in nature - magnetite crystals torqued by external magnetic fields open ion channels in the cell wall. To mimic this, we propose a microfabricated MEMS sensor, with a layer of magnetic material on top of piezo electric cantilevers. When forced with an external field, torque induced on the magnet create stress in the piezo, and thus a voltage is produced. There are three advantages to this approach. First, microfabrication allows for a small size. Second, by orienting individual sensing elements in anti-series order, the output is natively a gradiometer. Third, by selecting the resonant frequency of the cantilever carefully, we can create a gradiometer which outputs a spectrogram directly. Though fluxgates can be microfabricated and function as gradiometers, they suffer a size/sensitivity tradeoff. Microfabricated atomic magnetometers are sensitive but don't function natively as gradiometers. Other micro-scale magnetometers, namely Lorentz-type, which operate on a similar mechanism, are not yet sensitive enough and haven't been used as frequency-domain gradiometers, as in the proposed design.

3 Detailed Technical Approach

3.1 Motivation and previous work

Magnetometers serve an important role in investigating biologically generated electromagnetic fields, such as those created by neuronal currents, or geological magnetic fields. Typically, magnetometers are unable to achieve high sensitivity in an ambient, unshielded environment - getting to femtotesla level sensitivity requires magnetic shield and cryogenic sensors, such as SQUID [14]. The novel spin relaxation free magnetometer has been miniaturized and achieves less than $10 \text{ fT}/\sqrt{Hz}$, but still requires shielding and lacks directional sensitivity [18]. Fluxgates have achieved pT level resolution at small size, but this is insufficient for biomagnetic field measurement [16, 17, 23]

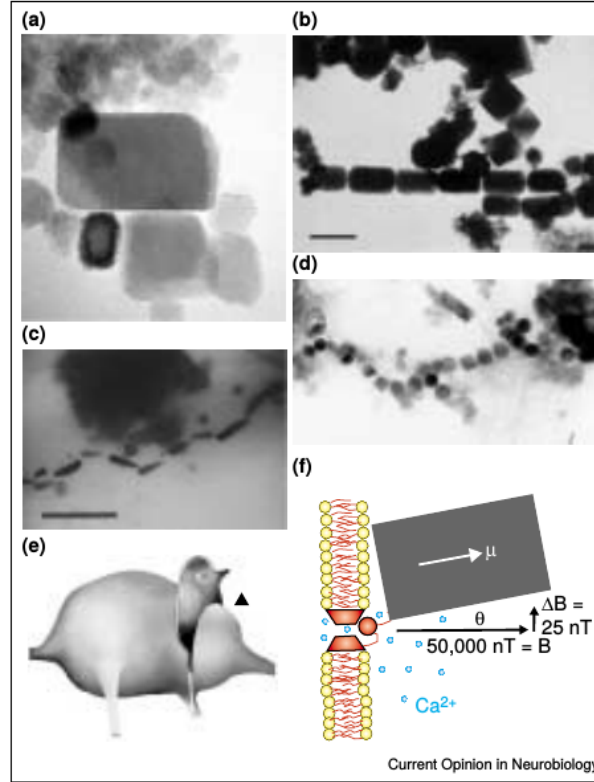


Figure 1: Magnetosomal mechanism [11].

Lorenz-type magnetometers (which translate magnetic fields into mechanical actuation of a magnet or current carrying wire) have been built in MEMS substrates, but are as yet insufficiently sensitive and require shielding [12, 13, 20, 22]

In nature, many organisms have a sense of magnetoreception used for navigation, from magnetotactic bacteria to birds. Two mechanisms have been proposed: a spin-selective (and thus field-sensitive) chemical reaction rate, or magnetite crystals which are actuated by external fields and activate ion channels in the cell membrane (Figure 1) [5, 10, 11]. Measurements of these magnetosomes show a magnetic dipole moment of up to $100\text{fA}/\text{m}^2$ [7, 8].

3.2 Biomimetic sensor design

Our approach is to mimic the approach found in magnetosomes, with some key modifications so that is frequency-selective and functions inherently as a gradiometer and thus does not require shielding. The closest biomimetic sensor is a flow sensor which uses ferromagnetic cilia to detect microfluidic flow rates [1].

To accomplish this, we propose layering a permanent magnetic layer on top of piezoelectric cantilevers. The torque induced on the magnetic layer generates stress in the piezo layer, and thus a voltage and charge. For an estimate of the sensitivity of this type of sensor, we perform a basic analysis. The moment N induced on the magnetic layer with moment $\vec{\mu}$ and field \vec{B} is:

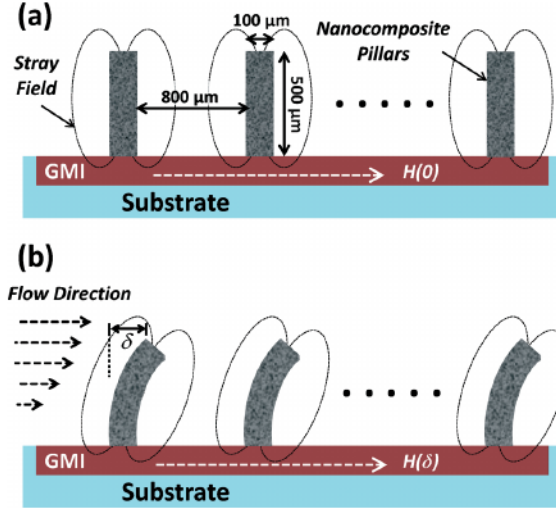


Figure 2: Magnetic cilia flow sensor [1].

$$N = \vec{\mu} \times \vec{B}$$

Interpreted as a point load (M/L) at the cantilever tip, this moment causes a stress distribution on a cantilever of length L , thickness t , second moment I , piezoelectric voltage constant g_{31} , and modulus E , at point (x,y) , of

$$\sigma = \frac{Ny(L-x)}{LI}$$

and n in series generates a voltage

$$V = \frac{\int_0^L \int_{-t/2}^{t/2} \left| \frac{Ny(L-x)}{LI} g_{31} n \right| dy dx}{L}$$

Two features are possible from the cantilever design: frequency selection and gradiometry. As in [19], a cantilever has a resonant frequency, which can be modified through geometrical parameters. Peak response will be achieved at this frequency. By selecting many cantilevers of different dimensions, each corresponding to a separate output, the magnetometer output is a spectrometer. Many cantilevers at the same resonance in series generate a larger voltage; in anti-series, the difference is taken, thus functioning as a gradiometer with very high spatial resolution (Figure 3).

The noise floor of magnetic materials is governed by Barkhausen noise - the random flipping of magnetic domains [3]; this noise is characterized as a flux noise level. The flux noise can be converted into a magnetic moment noise, and thus moment noise. Piezo noise floor is largely a function of piezo losses [15]. Additionally, there is the input-referred noise of any amplifier. With conservative estimates for all of these, and two anti-series banks of 30 cantilevers each with dimensions $400 \times 40 \times 3 \mu\text{m}$, the sensitivity level is less than $10 \text{ fT/cm}/\sqrt{Hz}$.

Even though biological magnetoreception is limited to nT sensitivity, our design will allow us to surpass this. First, by careful selection of materials (such as Co-Pt or rare-earth magnets)

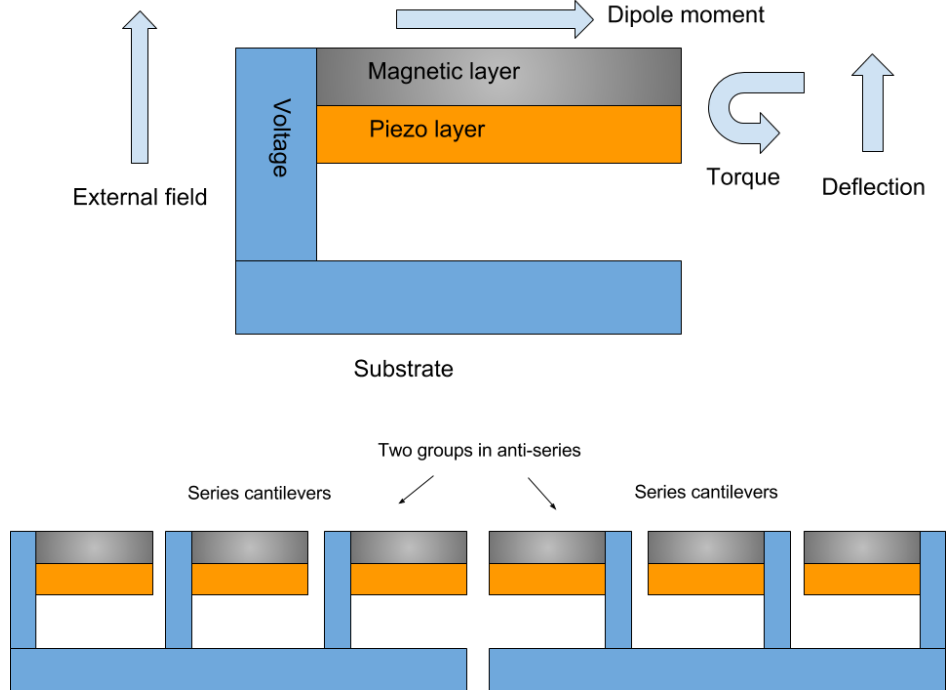


Figure 3: Diagram of proposed design.

[2, 4] we can have much higher magnetic dipole moment, and thus higher moment. Second, by careful selection of geometry, we can employ parametric resonance [24]. Third, by using many elements in series and summing, we can increase signal-to-noise ratio (SNR). Finally, using two banks of cantilevers in series in anti-series, we both boost the voltage and create a high resolution gradiometer.

Some potential pitfalls include effects of viscous damping (limiting mechanical vibration at resonance), demagnetization factor (due to the particular geometry of the magnetic layer), and layer separation from stress differentials (if strain at the interface between layers is not equal, they can peel apart). To assess these more closely, multiple physical effects, with non-linear, anisotropic materials must be considered simultaneously, for a complex geometry. A large part of the MEMS sensor design process must therefore be completed in simulation, allowing us to determine the impact of the above effects. Additionally, we can optimize the geometry and material selection for maximum response. Key design variables include the coercivity and remanence of the magnetic layer, the piezoelectric coefficients of the piezoelectric layer, the Young's modulus of each, and the length, width, and thickness of the cantilever. The physics of the system can be described by the PDEs for magnetostatics, mechanical deformation, fluid flow, and the piezoelectric constitutive relations:

$$\nabla \vec{B} = 0$$

$$\vec{B} = \mu_o(\vec{H} + \vec{M}(\vec{H}))$$

where \vec{B} is the magnetic flux, \vec{H} the sum of external and internal fields, and \vec{M} the magnetization.

$$EI \frac{\partial^4 u}{\partial^4 x} + m(x) \frac{\partial^2 u}{\partial^2 t} + \gamma \frac{\partial u}{\partial t} = q(x)$$

$$EI \frac{\partial^2 N(x)}{\partial^2 x} = -q(x)$$

$$N(x) = \vec{\mu}(x) \times B_{ext}, \vec{\mu}(x) = \vec{M}(x) dx dy dz$$

$$\sigma_{xx}(x, y) = N(x, y) y / I(x)$$

where N is the moment, q is load, u is vertical displacement of the beam, EI is the modulus/moment product, m is mass per length, γ is damping, and σ is stress. This can (and should) be expanded to consider stress in multiple axes. Finally, to relate field \vec{E} , strain \vec{S} , displacement field \vec{D} , and stress $\vec{\sigma}$, in the piezo layer:

$$S_{ij} = s_{ijkl} \sigma_{kl} + d_{kij} E_k$$

$$D_i = d_{ikl} \sigma_{kl} + \epsilon_{ik} E_k$$

Together, these relations allow us to estimate resonance, stress, and sensitivity of our sensor. FeniCS provides an open-source finite-element multiphysics solver [6]; though less user-friendly, it is flexible, and free, and sufficient for our needs.

3.3 Circuit design

From circuits and systems point of view, this could be modeled using a very-large-scale oversampling sensing architecture. A simple example is laid out as follows:

Assuming a sensor of area A generates a signal of S , and it has intrinsic noise of N_i . In comparison, two sensors each with an area of $A/2$ would each generate a signal of $S/2$ and noise of $N_i/2$. When the two $A/2$ sensors are combined, their total signal will be S . But since the noises from the two $A/2$ sensors are uncorrelated, the combined total noise will be:

$$\sqrt{(N_i/2)^2 + (N_i/2)^2} = N_i / \sqrt{2}$$

Therefore, splitting a sensor into two with equal sizes will result in a factor of $\sqrt{2}$ increase in the signal-to-noise ratio (SNR). Likewise, splitting a sensor into an array of M small units that sum up to the same total size would lead to a factor of $\sqrt{2}$ increase to the SNR. Because of this oversampling gain to the SNR, magnetosomes with thousands of magnetite crystals are able to have a superb sensitivity to small fluctuations of magnetic field, which can hardly be achieved by conventional man-made tools.

Ideally, if a sensor can be split into an infinite number of miniaturized elements, it would approach the capability of magnetosomes to achieve ultra-high field sensitivity. On the other hand, it is the most desirable to implement this bio-inspired oversampling mechanism in the analog front-end because any added back-end circuitry would introduce parasitics and noise that would otherwise hinder the effectiveness of the ideal oversampling theory. Inspired by this bio-mechanism

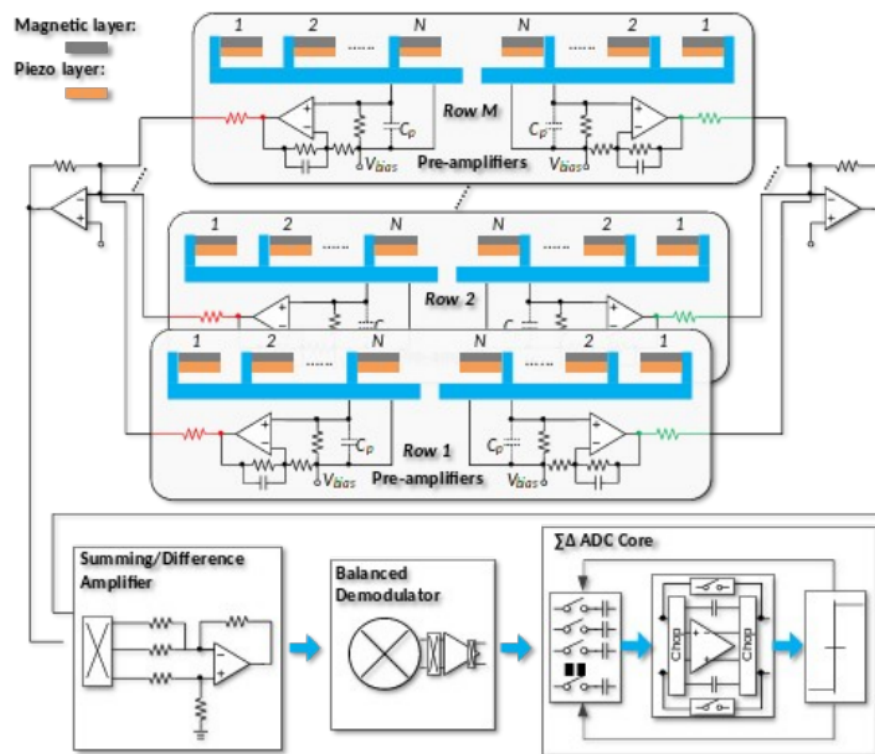


Figure 4: System design.

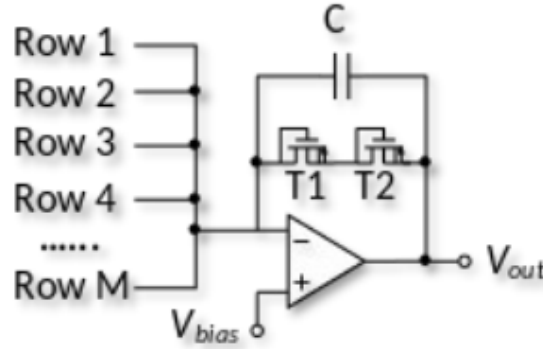


Figure 5: Charge-mode amplifier with pseudo-resistor feedback and summing capability.

of oversampling, we propose to combine the magnetic/piezo cantilevers in groups, and use low-noise amplifiers to pick up signals directly from the elements. The low-noise amplifiers will be reconfigurable in summing and difference modes to detect total field and field gradient. An overview of the system is shown in 4.

Voltage-mode and charge-mode amplifications are two main approaches to signal conditioning of piezoelectric sensors. Voltage-mode amplification is useful when the amplifier is very close to the sensor and thus minimal parasitic capacitance is presented by the interconnection between the sensing device and the amplifier input. Charge-mode amplification is useful when the amplifier is remote to the sensor because it is robust against parasitic capacitance of interconnections. Depending on the characterization and optimization of the MEMS device front-end, the team is ready to investigate both approaches and fully integrate the final solution with special circuit techniques for the optimal performance.

3.3.1 Voltage-mode approach

The simplified structure for voltage-mode amplifiers is depicted in Fig. 1, represented by the circuits that directly interface with each row of the MEMS device. Depending on the fabricated and optimized MEMS size, either one or multiple amplifiers in parallel will be implemented for each row to collect the piezoelectric voltage signal. The goal of using multiple amplifiers in parallel is to enhance the SNR because of the benefit of oversampling. However, constraint exists due to the area available under the MEMS device. The circuit team will work closely with the device team in this project to determine the ideal placement of local voltage-mode amplifiers in Phase I of this project, and compare the performance with the charge-mode approach discussed next.

3.3.2 Charge-mode approach

Charge-mode amplifier approach will also be investigated because it has advantages in handling interconnect parasitics and the convenience of adding signals from different rows of the MEMS device together. Inspired by existing works in neural recording [9], the team will use MOS-bipolar pseudo-resistors as the feedback resistors in the charge-mode amplifier. As shown in Fig. 5, two back-to-back connected PMOS transistors (T1 and T2) that can easily achieve resistance of GOhm

will be implemented as the feedback resistor of the charge-mode amplifier, enabling processing brain signals with a frequency as low as 0.025 Hz. With negative VGS, T1 and T2 function as diode-connected PMOS transistors. With positive VGS, the parasitic source-well-drain p-n-p bipolar junction transistor (BJT) is activated, such that T1 and T2 act as diode-connected BJTs. For small voltages across the pseudo-resistors, the small-signal resistance is extremely high and thus moves the corner frequency to low enough for bio-signal processing. On the other hand, a large voltage across the pseudo-resistors will reduce the small-signal resistance and result in a fast settling time for the amplifier. As a result, this pseudo-resistor-based design can amplify low-frequency signals down to the millihertz range while properly rejecting large DC offsets. The theoretical noise-power tradeoff limit, i.e., the noise efficiency factor, have already been derived and demonstrated by selectively operating MOS transistors in either weak or strong inversion [9]. The only potential drawback of this configuration is that the amplifier cannot have a large output swing, because otherwise the pseudo-resistors may become two diodes. However, this is not a concern for this work as the amplifier is implemented as pre-amplifier in the front-end, where signal to be handled is very weak.

3.3.3 Re-configurable summing and difference amplifiers

After the piezoelectric signal is captured by arrays of preamplifiers located near the MEMS devices, the obtained voltage signals will be further processed by a re-configurable summing and difference amplifier. This amplifier will be digitally controlled in real-time to either add or subtract signals coming from different regions of the fabricated device. This will enable both common-mode detection, which senses the total field, and differential-mode detection, which senses the field gradient.

One potential challenge for the summing and difference amplifier is the tradeoff between high sensitivity and large dynamic range. Being able to perform both summing and difference detection, as well as the existence of strong earth field, requires the amplifier to have a very large dynamic range. However, the DARPA specification also mandate high sensitivity for brain signal recovery. To this end, a 3-op-amp topology will be customized and implemented in GlobalFoundries 180-nm SiGe BiCMOS 7WL process for both low-noise and high-linearity operation. Bipolar PNP transistors will be utilized as the input buffer to minimize the input-referred noise. A programmable resistor array will be implemented between the outputs of the two first-stage amplifiers to provide a large tunable gain range. In a previous DARPA project (MELD Mind Electromagnetic Localization Device), the team has evaluated various topologies and different semiconductor processes (e.g., SOI versus BiCMOS) for the amplifier of brain magnetic field detector, and verified through both Cadence simulation and fabricated chip measurement that the 3-op-amp topology with bipolar transistor input buffer offers the best combined sensitivity and linearity performance.

3.3.4 Solution to low-frequency noise and device mismatch in electronics

Because the magnetic field induced by brain activities not only is extremely weak, but also has very low frequencies (below 100 Hz), $1/f$ noise and device mismatch have significant impact to the success of the proposed work. To resolve the challenge, the bio-inspired MEMS device will operate at a resonant frequency which carries the signal of interest. A double-balanced demodulator featuring low noise at the baseband output, high rejection to harmonic distortion, and low LO/Rf leakage will be implemented after the summing/difference amplifier to recover the field information of interest.

After the signal is demodulated to the baseband, circuit techniques including dynamic offset cancellation, dynamic element matching (DEM), and chopping will be applied extensively to the remaining signal processing circuits to cancel the mismatch errors and low-frequency noises. On the data acquisition side, the analog-to-digital converter (ADC) is an important component for correctly interpreting the field information. Nyquist ADC accuracy is limited by the matching of components in ADCs, while oversampling ADCs, such as sigma-delta ADC, trade speed for resolution. Because brain signals have low frequencies, sigma-delta ADC is a very good candidate for precision sensing. Another advantage of sigma-delta ADC is that chopping technology and dynamic element matching are suitable for its architecture to minimize the impact of low-frequency noise and offset errors.

3.4 MEMS fabrication process

MEMS fabrication processes are capable of constructing the above-described sensor. This consists of two main fabrication tasks: piezo cantilever construction, and magnetic material integration. PZT has a high voltage coefficient and is highly suitable for the piezo elements [21] and can be deposited with pulsed laser deposition or solution-based deposition and patterned with chemical etching or ion etching. Rare-earth magnetic materials, such as alloys of Sm-Co, offer high magnetic energy product at room temperature and can be integrated into MEMS using sputtering or pulsed laser deposition [2]. However, patterning of these materials is slow, using wet etching or ion-beam milling. A likely strategy will be to use a sacrificial polymer pattern, depositing electrodes, piezo, then magnet layers, and then etching the sacrificial layers.

The process described in [19] can be used for our task, with an additional step to apply the magnetic layer. To construct an energy-harvesting piezo cantilever, [19] used a four-mask process.

- SiO₂ was grown on both sides of a silicon wafer.
- Ti and bottom electrode Pt were deposited by sputtering, followed by sol-gel deposition of PZT (Fig. 6(a)).
- A top Pt electrode was patterned by a first mask and deposited by a lift-off process (Fig. 6(b)):
 - A sacrificial photoresist layer was deposited and patterned
 - Pt was deposited on the patterned photoresist
 - The sacrificial layer was removed
- The bottom electrode (the first Pt layer) was exposed from the topside with a second mask and etched with inductively coupled plasma reactive ion etching (RIE) (Fig. 6(c)).
- The cantilever was patterned from above with a third mask via RIE (Fig. 6(d)).
- The cantilever proof mass (used here to enhance resonance) was patterned and released through backside RIE (Fig. 6(e)).

In our case, we would need an additional ferromagnetic layer on top. This could be deposited, then patterned and etched, or deposited according to some pattern, followed by annealing. In either case, of critical importance is selected a material whose magnetic properties are optimal

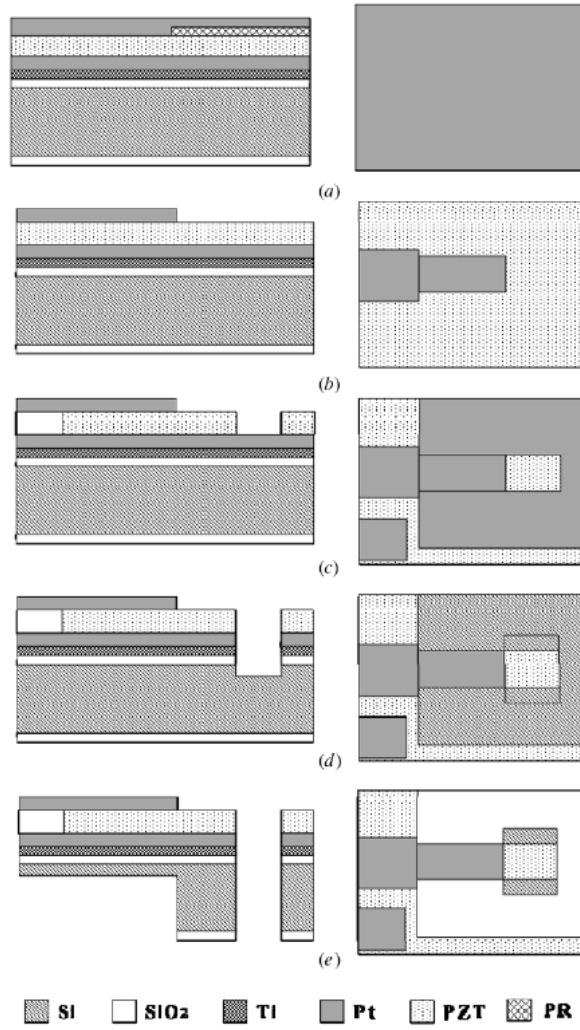


Figure 6: Cantilever fabrication [19].

(high remanence and coercivity), whose annealing temperatures are compatible with the other materials, and which can be etched easily enough. It should also maintain magnetic properties within the operating temperature range (has a high Curie temperature), and be oxidation resistant (for instance, some rare-earth magnets are prone to oxidation and must be tungsten-coated).

4 Risk Analysis and Mitigation Plan

Risk	Probability	Impact	Plan
Insufficient sensitivity in simulation	3	10	Optimize design architecture.
Best available MEMS processes infeasible	4	10	Begin design process within constraints of available processes.
Interconnects infeasible	3	5	Operate fewer, larger cantilevers
Viscous damping hurts sensitivity	3	5	Operate in vacuum packaging
Resonance frequency infeasibly high	3	5	Operate fewer, larger cantilevers
Fabrication unreliable/mismatch	3	5	Begin fabrication early, so process can be adjusted; dynamic element matching.
Fabrication time/window exceeds deadlines	3	5	Fabricate COTS scale model instead.
Circuit/sensor sensitive to mechanical vibration	5	8	Use additional sensors without magnetic layer to independently measure mechanical vibrations.
Circuit/sensor fails final tests	3	9	Identify points of failure for next design.

Table 2: Risk matrix

There are several risks and challenges associated with this design. Most can be addressed simply by doing careful analysis of the problem within the range of constraints on materials and processes. Others (the density of interconnects, or resonant frequency, is too high) can be address by reducing the number of elements and increasing their size. Damping due to the viscosity of air could hurt sensitivity could be addressed by vacuum packaging the sensor head. Manufacturing mismatch of the sensing elements can be effectively removed by dynamic element matching, where the transducers are switched electronically to average out fabrication differences.

Phase	Milestone	Date
1	Cantilever simulation and design	
1	Fabrication plan	
1	Circuit design	
1	Scale model design and testing	
2	Cantilever simulation and design	
2	Fabrication plan	
2	Circuit design	
2	MEMS fabrication and testing	
3	Cantilever simulation and design	
3	Fabrication plan	
3	Circuit design	
3	MEMS fabrication and testing	

Table 3: Milestones schedule

5 Schedule and Milestones

Include a high-level Gantt chart outlining major technical tasks and measureable milestones by phase. At a minimum, the schedule should include each SOW task of Volume 1, Section II.A. Where risk reduction tasks are proposed, the schedule should include a milestone for assessment and removal of redundant tasks.

6 Test Plan

To test, in Phase 1, the sensor will first be connected to the circuitry and powered to established basic functionality. Prior to testing, noise from the Helmholtz coils, power supplies, and any other necessary signal generation sources will be characterized. Further, the sensor will be placed in a pair of Helmholtz coils driven with precision current sources, inside magnetic shield, so we can gauge accuracy and sensitivity in a controlled environment. Field gradient, intensity, and frequency will be swept over the range specified in 1. Finally, the same test will be repeated in an unshielded environment. In each case, we will monitor power consumption, signal output, and measure sensor and sensor control electronics volume. Subsequent phases will follow a similar testing protocol, except using specifications for those phases. Additionally, in Phases 2 and 3, where an IC will be developed and integrated with the sensor head, the IC will be tested independently to verify basic input/output functionality before slicing and bonding to the MEMS substrate.

7 Results and Technology Transfer

The sensor and circuit design is novel and likely patentable, even if it fails to meet all requirements in the BAA. This can be considered fundamental research and thus not ITAR-controlled.

8 Ongoing Research

Presently, Drs. Lin, Li, and Casanova are presently involved (until 6/2017) in a DARPA project for detection and inversion of MEG/EEG signals. They have worked on all phases, from sensor design to circuit design to inversion algorithm. Additionally, Drs. Lin and Li have worked extensively on vital signs radar, and Dr. Casanova has worked on electromagnetic sensors for agriculture and analytical chemistry. Dr. Yoon has conducted research in MEMS systems of all types, including piezoelectronics and magnetics.

9 Facilities

Facilities at UF for sensor development and testing include magnetic shielding, circuit testing instrumentation (power supplies, oscilloscopes, function generators, VNA). For fabrication, the Nanoscale Research Facility at UF includes equipment for a variety of processes: plasma, vapor, and sputtering deposition, annealing, wafer bonding, wire bonding, UV and e-beam lithography, reverse ion etching, and wet etching.

PI Lis research group at the Texas Tech University is located in a fully ESD-protected Microwave and Analog Circuits Laboratory. The PI has obtained full support from the Texas Tech University Provost Office and Electrical and Computer Engineering Department to establish this well-equipped lab and enhance the departments research strength on the microwave, millimeter-wave, and analog circuits. Equipment includes high-frequency network analyzers, signal generators, spectrum analyzer, and oscilloscopes; software useful to this project includes Cadence, Altium, MATLAB, and Labview.

10 Teaming/Proposer Accomplishments

UF The UF team includes the following personnel:

Jenshan Lin will be project lead, and supervise work on the AMBIENT project. His time commitment is 0%. He received the B.S. degree in Electrophysics from National Chiao Tung University (NCTU), Hsinchu, Taiwan, R.O.C., in 1987, and the M.S. and Ph.D. degrees in Electrical Engineering from the University of California at Los Angeles (UCLA), in 1991 and 1994, respectively. His current research interests include sensors and biomedical applications of microwave and millimeter-wave technologies, wireless energy transfer and conversion, RF system-on-chip integration, and integrated antennas. Dr. Lin has authored or co-authored over 250 technical publications in refereed journals and conference proceedings. He holds 15 patents and has several other patent applications. Since joining University of Florida, he has graduated 22 PhD students.

Joaquin Casanova will conduct electromagnetic simulation and design of the MEMS sensor, and work with Dr. Yoon on process/material selection, and Dr. Li on requirements for the interface and control circuits. His commitment is 50%. He received a B.S. and M.E. in Agricultural and Biological engineering in 2006 and 2007 from UF, and PhD in Electrical Engineering in 2010 from UF. His work has primarily focused on sensors and electromagnetic design, including wireless power, microwave remote sensing, agricultural sensors, analytical chemistry equipment design, and low-field magnetic sensing. He is

currently a Research Assistant Professor in UF's Department of Electrical and Computer Engineering.

YK Yoon will guide selection of a microfabrication process and fabrication of the sensor. His commitment is 10%. He received his BS and MS degrees in electrical engineering from Seoul National University in Korea. He also earned an MSEE degree from the New Jersey Institute of Technology, Newark, NJ in 1999 and the Ph.D. degree in electrical and computer engineering from the Georgia Institute of Technology, Atlanta, GA in 2004. He is currently an Associate Professor in the Department of Electrical and Computer Engineering at the University of Florida, Gainesville, FL. His current research interests include three dimensional (3-D) micromachining and nano fabrication; design and implementation of metamaterial for radio frequency (RF) and microwave applications; micromachined millimeter wave and terahertz antennas and waveguides; bio/microfluidic systems for the lab-on-a-chip applications; wireless telemetry systems for biomedical applications; and ferroelectric material development for high density memory devices and/or tunable RF devices.

TTU The TTU team includes:

Changzhi Li will design the electronics for amplifying and digitizing the signals from the sensor head, working with Dr. Casanova to determine circuit requirements. His time commitment is 10%. He received the B.S. degree in electrical engineering from Zhejiang University, China, in 2004 and the Ph.D. degree in electrical engineering from the University of Florida, Gainesville, FL, in 2009. In the summers of 2007 and 2008, he worked at Alereon Inc., Austin, TX, on ultrawideband (UWB) transceiver. In the summer of 2009, he worked at Coherent Logix Inc., Austin, TX, on software-defined radio. His research interests include analog circuits, microwave circuits, and biomedical applications of microwave/RF.

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BAA number HR001117S0025

Lead Organization Jenshan Lin, University of Florida

Type of organization Other Educational

Proposers internal reference number

Other team members :

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YK Yoon University of Florida

Joaquin Casanova University of Florida

Proposal title Biomimetic microfabricated magnetic gradiometer

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Total funds requested

Submitted 06/01/2017

Award instrument requested Cost-Plus-Fixed Fee (CPFF), Cost-contractno fee, cost sharing contractno fee,or other type of procurement contract (specify) or Other Transaction

Place(s) and period(s) of performance: Univeristy of Florida, Texas Tech University, 10/01/2017-4/01/2021

Total proposed cost separated by basic award and option(s), if any, by calendar year and by government fiscal year

Defense Contract Management Agency (DCMA) administration office (if known) Name, address, and telephone number

Defense Contract Audit Agency (DCAA) audit office (if known) Name, address, and telephone number

Date proposal was prepared

DUNS

TIN

CAGE

Subcontractor Information

Proposal validity period (120 days is recommended)

Any Forward Pricing Rate Agreement other such approved rate information, or such documentation that may assist in expediting negotiations (if available). Attachment 1, the Cost Volume Proposer Checklist, must be included with the coversheet of the Cost Proposal.

Detailed Cost Information (Prime and Subcontractors)

The proposers (to include FFRDCs and Government Labs) cost volume shall provide cost and pricing information, or other than cost or pricing information if the total price is under the referenced threshold (See Note 1), in sufficient detail to substantiate the program price proposed (e.g., realism and reasonableness). In doing so, the proposer shall provide, for both the prime and each subcontractor, a Summary Cost Breakdown by phase and performer fiscal year, and a Detailed Cost Breakdown by phase, technical task/sub-task, and month. The breakdown/s shall include, at a minimum, the following major cost item along with associated backup documentation: Total program cost broken down by major cost items:

11 Direct Labor

A breakout clearly identifying the individual labor categories with associated labor hours and direct labor rates, as well as a detailed Basis-of-Estimate (BOE) narrative description of the methods used to estimate labor costs

12 Indirect Costs

Including Fringe Benefits, Overhead, General and Administrative Expense, Cost of Money, Fee, etc. (must show base amount and rate)

13 Travel

Provide the purpose of the trip, number of trips, number of days per trip, departure and arrival destinations, number of people, etc. See Section IV.B.13 for travel funding restrictions

14 Other Direct Costs

Itemized with costs; back-up documentation is to be submitted to support proposed costs

15 Material/Equipment

(i) For IT and equipment purchases, include a letter stating why the proposer cannot provide the requested resources from its own funding. (ii) A priced Bill of Material (BOM) clearly identifying, for each item proposed, the quantity, unit price, the source of the unit price (i.e., vendor quote, engineering estimate, etc.), the type of property (i.e., material, equipment, special test equipment, information technology, etc.), and a cross-reference to the Statement of Work (SOW) task/s that require the item/s. At time of proposal submission, any item with a unit price that exceeds \$1,000 must be supported with basis-of-estimate (BOE) documentation such as a copy of catalog price lists, vendor quotes or a detailed written engineering estimate (additional documentation may be required during negotiations, if selected). (iii) If seeking a procurement contract and items of Contractor Acquired Property are proposed, exclusive of material, the proposer shall clearly demonstrate that the inclusion of such items as Government Property is in keeping with the requirements of FAR Part

45.102. In accordance with FAR 35.014, Government property and title, it is the Governments intent that title to all equipment purchased with funds available for research under any resulting contract will vest in the acquiring nonprofit institution (e.g., Nonprofit Institutions of Higher Education and Nonprofit Organizations whose primary purpose is the conduct of scientific research) upon acquisition without further obligation to the Government. Any such equipment shall be used for the conduct of basic and applied scientific research. The above transfer of title to all equipment purchased with funds available for research under any resulting contract is not allowable when the acquiring entity is a for-profit organization; however, such organizations can, in accordance with FAR 52.245-1(j), be given priority to acquire such property at its full acquisition cost.

16 Consultants

If consultants are to be used, proposer must provide a copy of the consultants proposed SOW as well as a signed consultant agreement or other document which verifies the proposed loaded daily / hourly rate and any other proposed consultant costs (e.g. travel);

17 Subcontracts

Itemization of all subcontracts. Additionally, the prime contractor is responsible for compiling and providing, as part of its proposal submission to the Government, subcontractor proposals prepared at the same level of detail as that required by the prime. Subcontractor proposals include Interdivisional Work Transfer Agreements (ITWA) or similar arrangements. If seeking a procurement contract, the prime contractor shall provide a cost reasonableness analysis of all proposed subcontractor costs/prices. Such analysis shall indicate the extent to which the prime contractor has negotiated subcontract costs/prices and whether any such subcontracts are to be placed on a sole-source basis. All proprietary subcontractor proposal documentation (fully disclosed subcontract proposal), prepared at the same level of detail as that required of the prime, which cannot be uploaded to the DARPA BAA website (<https://baa.darpa.mil>, BAAT) as part of the proposers submission, shall be made immediately available to the Government, upon request, under separate cover (i.e., mail, electronic/email, etc.), either by the proposer or by the subcontractor organization. This does not relieve the proposer from the requirement to include, as part of their submission (via BAAT), subcontract proposals that do not include proprietary pricing information (rates, factors, etc.). A Rough Order of Magnitude (ROM), or similar budgetary estimate, is not considered a fully qualified subcontract cost proposal submission. Inclusion of a ROM, or similar budgetary estimate, may result in the full proposal being deemed non-compliant or evaluation ratings may be lowered;

18 Cost-Sharing

The amount of any industry cost-sharing (the source and nature of any proposed cost-sharing should be discussed in the narrative portion of the cost volume); AND

19 Fundamental Research

Written justification required per Section II.B, Fundamental Research, pertaining to prime and/or subcontracted effort being considered Contracted Fundamental Research.