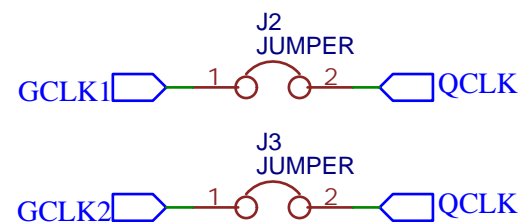
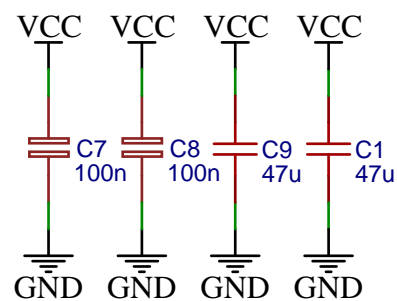
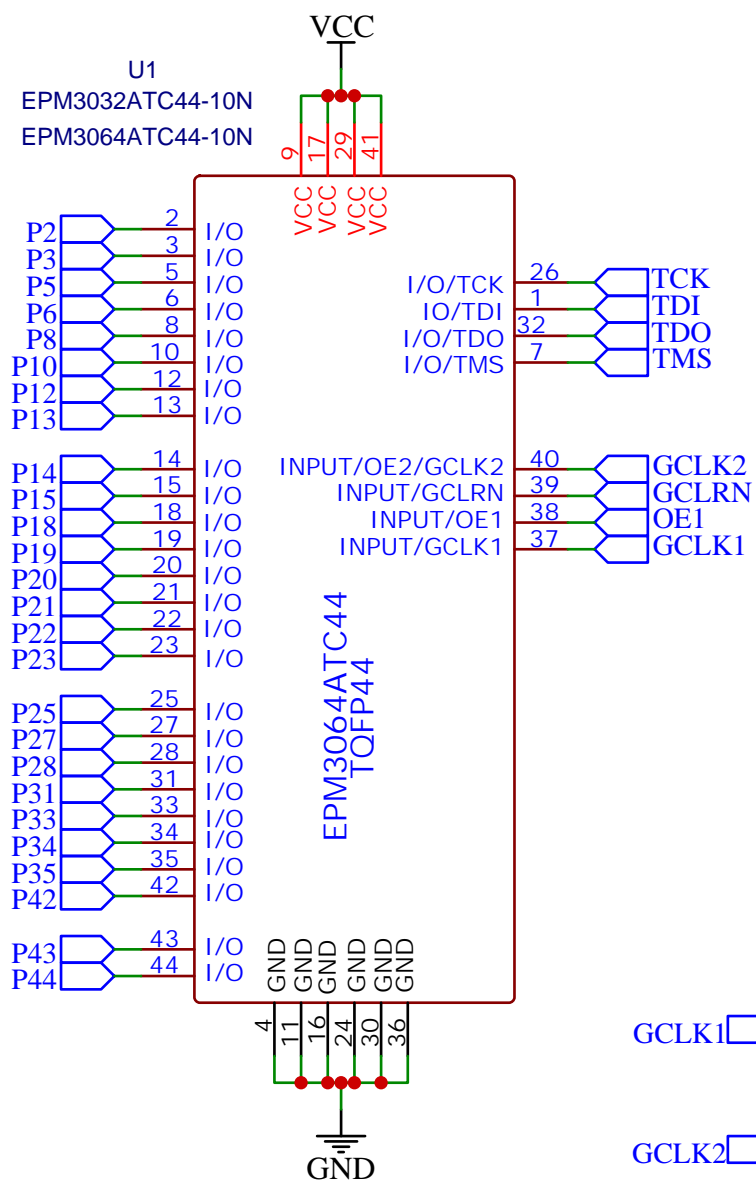
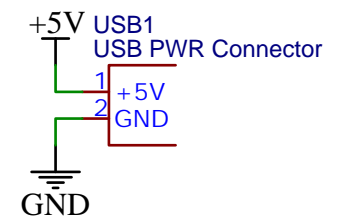
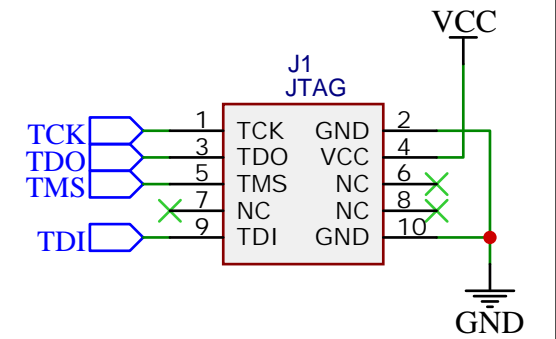
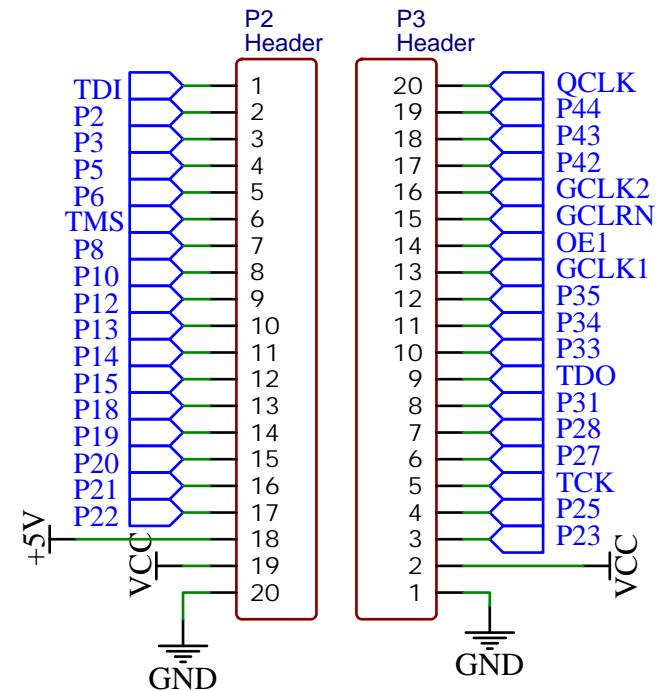


CPLD

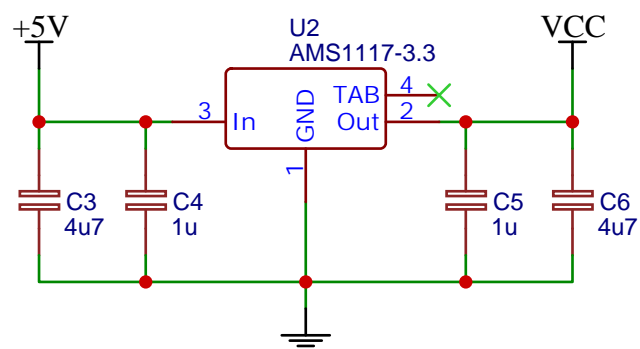
U1
EPM3032ATC44-10N
EPM3064ATC44-10N



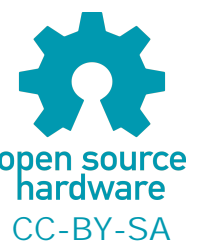
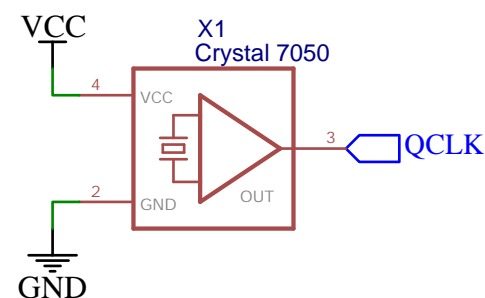
Headers and Connectors



Voltage Regulator



Clock Generator



TITLE: ALTERA MAX3000 CPLD Development Board		REV: 1.0
EasyEDA	Company: wagiminator	Sheet: 1/1
	Date: 2019-09-03	Drawn By: Stefan Wagner