

PCB #3: ALU Design Optimization and PCB Layout with OrCAD PCB Editor

COVER SHEET

Honor Code: I have neither given or received nor have I tolerated others use of unauthorized aid.

Name: Jon Bayert **Signature:** Jonathan Bayert

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Name: Joe Leveille **Signature:** Joseph Leveille

A complete assignment will contain:

- 1) A bill of materials for your original ALU design, with chip-count noted**
- 2) A bill of materials for your optimized ALU design, with chip-count noted**
(make sure to re-annotate just before generating the bill of materials)
- 3) Output waveforms from PSpice for your optimized ALU, verifying correct operation. You MUST have outputs (not necessarily inputs) in HEX form... combine outputs into busses. Use ALL four 8-bit input combinations. Make 4 pages, one for each input combination, each page containing the 5 operations.**
- 4) Printout of your routed PCB artwork for the small design.**

Label each of these at the top of the 1st page of each part.

Note: You MUST have outputs (not necessarily inputs) in HEX form... combine outputs into busses. You do not need to add busses in OrCad Capture, just in the Pspice simulation window. Use “Add Trace” and put your signals in braces for a bus: { r7 r6 r5 ... }.