

PCB #4: Generating PCB Artwork for Manufacture

COVER SHEET

Honor Code: I have neither given or received nor have I tolerated others use of unauthorized aid.

Name: Jonathan Bayert Signature: Jonathan Bayert

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Name: Joe Leveille Signature: Joe Leveille

A complete assignment will contain:

- 1) Printed .pdf files for each of your 6 layers for the simple PCB board, plus the board outline.
- 2) Updated schematic prints for your ALU
- 3) Printed .pdf files for your ALU showing silkscreen, top copper, and bottom copper

Label each of these at the top of the 1st page of each part.

Make sure your .zip file contains a BOARD OUTLINE. Ensure to set the undefined line width to 10 on both board_outline and silkscreen.

Make sure you have changed ALL spacing on EVERY layer to 10.0.
Make sure your outputs include the 8-bit result, Zero, Carry, and Overflow.

Double check your padstacks. Make sure they have the correct values, and generate the drill and layers AFTER you verify the padstacks are correct. The vias will be smaller than the holes for the components. Make sure your component hole sizes aren't changed!!!

Work with Professor Will to get your ALU ordered by the week before Thanksgiving. We will do this in class one of the days.