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<program> ==> <globalVars> <otherFunctions> <mainFunction>
    ==> <global_assignment><global_vars> <otherFunctions>
    <mainFunction>
        ==> <primitive_type> ID ASSIGN_OP <const_value> SEMI_CL
    <global_vars> <otherFunctions> <mainFunction>
        ==> <primitive_type> ID ASSIGN_OP <const_value> SEMI_CL
    <global_vars> <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP <const_value> SEMI_CL <global_vars>
    <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL <global_vars>
    <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL <global_assignment>
    <global_vars> <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL <primitive_type> ID
    ASSIGN_OP <const_value> SEMI_CL <global_vars> <otherFunctions>
    <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL <primitive_type> ID
    ASSIGN_OP <const_value> SEMI_CL <global_vars> <otherFunctions>
    <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP
    <const_value> SEMI_CL <global_vars> <otherFunctions>
    <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL <global_vars> <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL <otherFunctions> <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <mainFunction>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <stmts>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <stmt><stmts>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <gen_stmt><stmts>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <other_stmt><stmts>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE
    <primitive_declaration_stmt><stmts>
        ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
    SEMI_CL MAIN LPARA RPARA LBRACE <primitive_type> ID
    <assign_more><stmts>

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==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID <assign_more><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP <expression>
<assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP
<and_expr><expression'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP
<reln_expr><and_expr'><expression'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP
<add_sub_expr><reln_expr'><and_expr'><expression'>
<assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP
<mul_div_expr><add_sub_expr'><reln_expr'><and_expr'><expression
'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP <un_expr>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP <typecast>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP <value>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP <const_value>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<add_sub_expr'><reln_expr'><and_expr'><expression'>
<assign_comma><stmts>

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==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<reln_expr'><and_expr'><expression'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<and_expr'><expression'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<expression'> <assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM
<assign_comma><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<stmt> <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<gen_stmt> <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<other_stmt> <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<bot_stmt> <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
<bot_decl_stmt><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID <bot_rest><stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL <stmt><stmts>

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==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL <gen_stmt> <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL <other_stmt> <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL <bot_stmt> <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <expression> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <and_expr><expression'> SEMI_CL
<stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <reln_expr><and_expr'><expression'>
SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA
<add_sub_expr><reln_expr'><and_expr'><expression'> SEMI_CL
<stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA
<mul_div_expr><add_sub_expr'><reln_expr'><and_expr'><expression
'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <un_expr>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <typecast>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <value>

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<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA <const_value>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM
<add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL
<stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM
<reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM <and_expr'><expression'> SEMI_CL
<stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM <expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <stmt><stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <gen_stmt> <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <other_stmt> <stmts>

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==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL <bot_stmt> <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA <expression>
SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA
<and_expr><expression'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA
<reln_expr><and_expr'><expression'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA
<add_sub_expr><reln_expr'><and_expr'><expression'> SEMI_CL
<stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA
<mul_div_expr><add_sub_expr'><reln_expr'><and_expr'><expression
'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA <un_expr>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA <typecast>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA <value>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expressio
n'> SEMI_CL <stmts>

==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA

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<const_value>
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM
<mul_div_expr'><add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM
<add_sub_expr'><reln_expr'><and_expr'><expression'> SEMI_CL
<stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM
<reln_expr'><and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM
<and_expr'><expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM
<expression'> SEMI_CL <stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM SEMI_CL
<stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM SEMI_CL
<stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM SEMI_CL
<stmt><stmts>
    ==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL

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==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM SEMI_CL
COMMENT_ST ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR
ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR
ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR COMMENT_END <stmts>
==> INT ID ASSIGN_OP NUM SEMI_CL INT ID ASSIGN_OP NUM
SEMI_CL MAIN LPARA RPARA LBRACE INT ID ASSIGN_OP NUM SEMI_CL
BOT ID SEMI_CL FW ID COMMA NUM SEMI_CL FW ID COMMA NUM SEMI_CL
COMMENT_ST ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR
ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR
ASCII_CHAR ASCII_CHAR ASCII_CHAR ASCII_CHAR COMMENT_END RBRACE
```