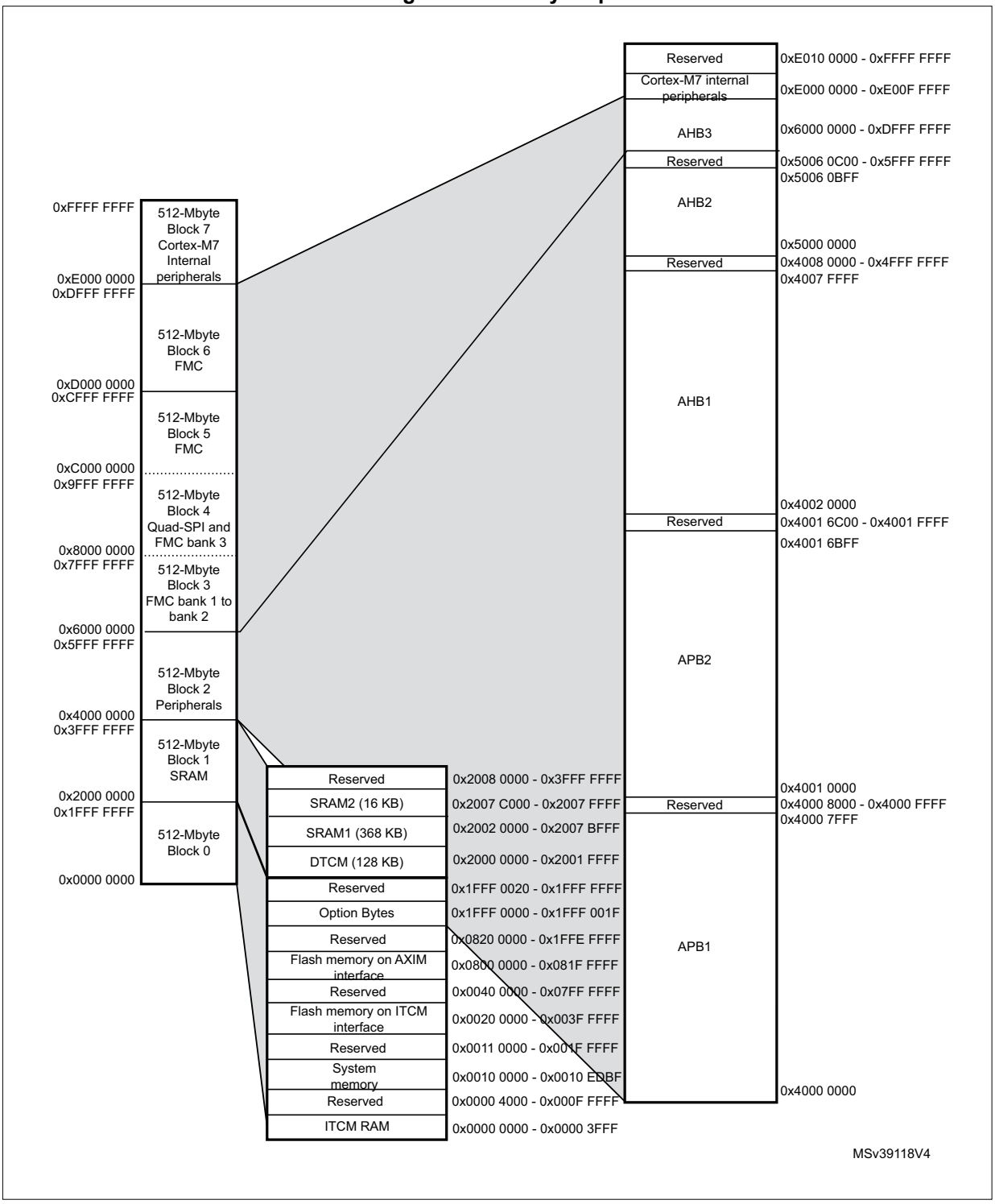
2.2.2 Memory map and register boundary addresses

Figure 2. Memory map



All the memory map areas that are not allocated to on-chip memories and peripherals are considered “Reserved”. For the detailed mapping of available memory and register areas, refer to the following table.

The following table gives the boundary addresses of the peripherals available in the devices.

Table 1. STM32F76xxx and STM32F77xxx register boundary addresses

Boundary address	Peripheral	Bus	Register map
0xA000 1000 - 0xA0001FFF	QUADSPI Control Register	AHB3	*QUADSPI register map*
0xA000 0000 - 0xA000 0FFF	FMC control register		*FMC register map*
0x5006 0800 - 0x5006 0BFF	RNG	AHB2	*RNG register map*
0x5006 0400 - 0x5006 07FF	HASH		*HASH register map*
0x5006 0000 - 0x5006 03FF	CRYP		*CRYP register map*
0x5005 1000 - 0x5005 1FFF	JPEG		*JPEG codec register map*
0x5005 0000 - 0x5005 03FF	DCMI		*DCMI register map*
0x5000 0000 - 0x5003 FFFF	USB OTG FS		*OTG_FS/OTG_HS register map*
0x4004 0000 - 0x4007 FFFF	USB OTG HS	AHB1	*OTG_FS/OTG_HS register map*
0x4002 B000 - 0x4002 BBFF	Chrom-ART (DMA2D)		*DMA2D register map*
0x4002 8000 - 0x4002 93FF	ETHERNET MAC		*Ethernet register maps*
0x4002 6400 - 0x4002 67FF	DMA2		*DMA register map*
0x4002 6000 - 0x4002 63FF	DMA1		
0x4002 4000 - 0x4002 4FFF	BKPSRAM		*RCC register map*
0x4002 3C00 - 0x4002 3FFF	Flash interface register		*Flash interface register map*
0x4002 3800 - 0x4002 3BFF	RCC		*RCC register map*
0x4002 3000 - 0x4002 33FF	CRC		*CRC register map*
0x4002 2800 - 0x4002 2BFF	GPIOK		*GPIO register map*
0x4002 2400 - 0x4002 27FF	GPIOJ		
0x4002 2000 - 0x4002 23FF	GPIOI		*GPIO register map*
0x4002 1C00 - 0x4002 1FFF	GPIOH		
0x4002 1800 - 0x4002 1BFF	GPIOG		
0x4002 1400 - 0x4002 17FF	GPIOF		
0x4002 1000 - 0x4002 13FF	GPIOE		
0x4002 0C00 - 0x4002 0FFF	GPIOD		
0x4002 0800 - 0x4002 0BFF	GPIOC		
0x4002 0400 - 0x4002 07FF	GPIOB		
0x4002 0000 - 0x4002 03FF	GPIOA		

Table 1. STM32F76xxx and STM32F77xxx register boundary addresses (continued)

Boundary address	Peripheral	Bus	Register map
0x4001 7800 - 0x4001 7BFF	MDIOS	APB2	*MDIOS register map*
0x4001 7400 - 0x4001 77FF	DFSDM1		*DFSDM register map*
0x4001 6C00 - 0x4001 73FF	DSI Host		*DSI register map*
0x4001 6800 - 0x4001 6BFF	LCD-TFT		*LTDC register map*
0x4001 5C00 - 0x4001 5FFF	SAI2		*SAI register map*
0x4001 5800 - 0x4001 5BFF	SAI1		*SAI register map*
0x4001 5400 - 0x4001 57FF	SPI6		*SPI/I2S register map*
0x4001 5000 - 0x4001 53FF	SPI5		
0x4001 4800 - 0x4001 4BFF	TIM11		*TIM10/TIM11/TIM13/TIM14 register map*
0x4001 4400 - 0x4001 47FF	TIM10		
0x4001 4000 - 0x4001 43FF	TIM9		*TIM9/TIM12 register map*
0x4001 3C00 - 0x4001 3FFF	EXTI		*EXTI register map*
0x4001 3800 - 0x4001 3BFF	SYSCFG		*SYSCFG register map*
0x4001 3400 - 0x4001 37FF	SPI4		*SPI/I2S register map*
0x4001 3000 - 0x4001 33FF	SPI1		*SPI/I2S register map*
0x4001 2C00 - 0x4001 2FFF	SDMMC1		*SDMMC register map*
0x4001 2000 - 0x4001 23FF	ADC1 - ADC2 - ADC3		*ADC register map*
0x4001 1C00 - 0x4001 1FFF	SDMMC2		*SDMMC register map*
0x4001 1400 - 0x4001 17FF	USART6		*USART register map*
0x4001 1000 - 0x4001 13FF	USART1		
0x4001 0400 - 0x4001 07FF	TIM8		*TIM8 register map*
0x4001 0000 - 0x4001 03FF	TIM1		*Section 25.4.28: TIM1 register map on page 948*

Table 1. STM32F76xxx and STM32F77xxx register boundary addresses (continued)

Boundary address	Peripheral	Bus	Register map
0x4000 7C00 - 0x4000 7FFF	UART8	APB1	*USART register map*
0x4000 7800 - 0x4000 7BFF	UART7		
0x4000 7400 - 0x4000 77FF	DAC		*DAC register map*
0x4000 7000 - 0x4000 73FF	PWR		*PWR register map*
0x4000 6C00 - 0x4000 6FFF	HDMI-CEC		*HDMI-CEC register map*
0x4000 6800 - 0x4000 6BFF	CAN2		*bxCAN register map*
0x4000 6400 - 0x4000 67FF	CAN1		
0x4000 6000 - 0x4000 63FF	I2C4		*I2C register map*
0x4000 5C00 - 0x4000 5FFF	I2C3		
0x4000 5800 - 0x4000 5BFF	I2C2		
0x4000 5400 - 0x4000 57FF	I2C1		
0x4000 5000 - 0x4000 53FF	UART5		*USART register map*
0x4000 4C00 - 0x4000 4FFF	UART4		
0x4000 4800 - 0x4000 4BFF	USART3		
0x4000 4400 - 0x4000 47FF	USART2		
0x4000 4000 - 0x4000 43FF	SPDIFRX		*SPDIFRX interface register map*
0x4000 3C00 - 0x4000 3FFF	SPI3 / I2S3		*SPI/I2S register map*
0x4000 3800 - 0x4000 3BFF	SPI2 / I2S2		
0x4000 3400 - 0x4000 37FF	CAN3		*bxCAN register map*
0x4000 3000 - 0x4000 33FF	IWDG		*IWDG register map*
0x4000 2C00 - 0x4000 2FFF	WWDG		*WWDG register map*
0x4000 2800 - 0x4000 2BFF	RTC & BKP Registers		*RTC register map*
0x4000 2400 - 0x4000 27FF	LPTIM1		*LPTIM register map*
0x4000 2000 - 0x4000 23FF	TIM14		*TIM10/TIM11/TIM13/TIM14 register map*
0x4000 1C00 - 0x4000 1FFF	TIM13		
0x4000 1800 - 0x4000 1BFF	TIM12		*TIM9/TIM12 register map*
0x4000 1400 - 0x4000 17FF	TIM7		*TIMx register map*
0x4000 1000 - 0x4000 13FF	TIM6		
0x4000 0C00 - 0x4000 0FFF	TIM5		*TIMx register map*
0x4000 0800 - 0x4000 0BFF	TIM4		
0x4000 0400 - 0x4000 07FF	TIM3		
0x4000 0000 - 0x4000 03FF	TIM2		