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ESP32-DevKitC

Getting Started Guide



Version 1.0

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About This Guide

This user guide introduces the basic features and operations of the ESP32-DevKitC.

The document is structured as follows:

Chapter	Title	Content
Chapter 1	Overview	Introduction to the ESP32-DevKitC.
Chapter 2	Download Process	Introduction to the boot modes and download operations.

Release Notes

Date	Version	Release notes
2016.09	V1.0	Initial release.

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1.

Overview

ESP32-DevKitC is a small sized ESP32-based development board developed by Espressif. Most of the I/O pins are led out to the pin headers on both sides for easy interfacing. Developers can connect these pins to peripherals as needed. Standard headers also make development easy and convenient when using a breadboard.

Figure 1-1 shows the layout of the board.

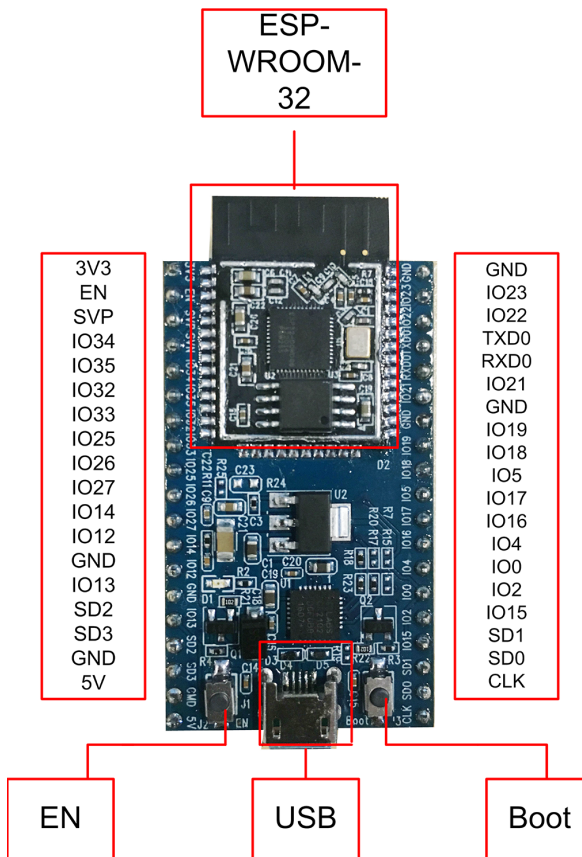


Figure 1-1. ESP32-DevKitC Layout



2. Download Process

You will need the hardware listed below:

- 1 × ESP32-DevKitC
- 1 × PC (with Windows OS as an example in this document)
- 1 × USB cable

2.1. Create Serial Communication

Connect the ESP32-DevKitC to the PC using the USB cable. Check the list of identified external COM ports in the Windows Device Manager and confirm the COM port number of the ESP32-DevKitC.

2.2. Set Download Mode and SPI Boot Mode

As shown in Figure 1-1, the ESP32-DevKitC features two buttons: one marked as “EN” located on the bottom left corner is the reset button and the other “Boot” button on the bottom right corner is the download button.

- Download mode:

While the “Boot” button is pressed, press the “EN” button to initiate download mode. The system should then enter the download mode and output the following on the terminal:

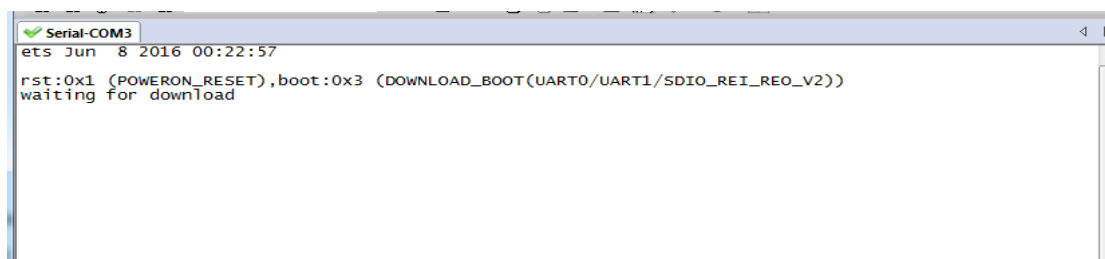


Figure 2-1. Download Mode Output

- SPI boot mode:

Simply press the “EN” button and the ESP32-DevKitC will enter the SPI boot mode. If download operation was completed successfully, the system will output some information on the terminal as shown in Figure 2-3.

2.3. Download Methods

Use ESP32 DOWNLOAD TOOL

Please download [ESP32 DOWNLOAD TOOL V3.4.1](#) from Espressif website.



Open the ESP32 DOWNLOAD TOOL. Select *bootloader.bin*, *partitions_singeapp.bin*, and *testje.bin* to be downloaded to addresses 0x1000, 0x4000, and 0x10000 respectively. Press “**START**” and wait for the prompt that indicates the download result.

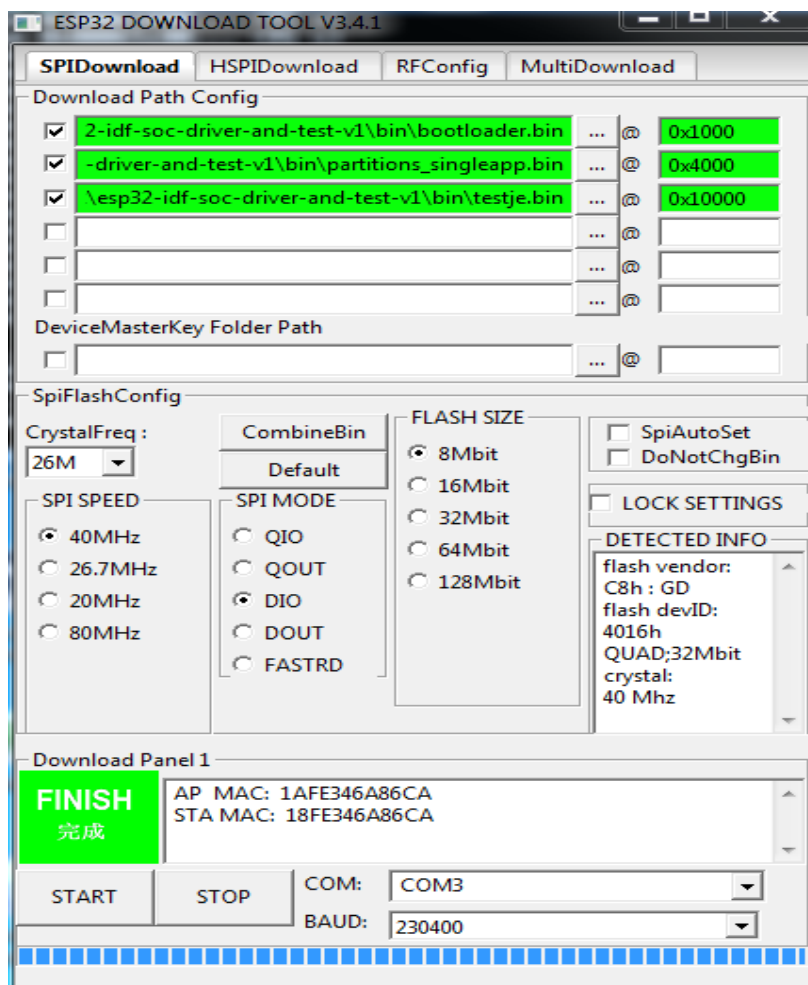


Figure 2-2. ESP32 DOWNLOAD TOOL Interface

Use Python Scripts

Get Python scripts from *esp-idf/components/esptool_py/esptool/esptool.py* in the ESP-IDF.

The download process will require the system to have a command terminal. For the Windows System, open the “Windows PowerShell” or “Windows Command Terminal”.

On the terminal, set the current directory to where the downloaded Python scripts are saved, and then type in the following commands:

```
python esptool.py -b 115200 -p COM3 write_flash -ff 40m -fm dio -ih  
0x0 -il 0x00 0x1000 bootloader.bin 0x4000 partitions_singleapp.bin  
0x10000 testje.bin
```

**Notes:**

- The parameters highlighted in blue represent the baud rate, serial port, flash frequency, and the flash mode, respectively. You can change these parameters as needed.
- Currently only **DIO** mode is supported. Espressif will upgrade the download tool and scripts to support both **QIO** and **DIO** modes.
- Type in `python esptool.py -h` for help information, `python esptool.py write_flash -h` for download commands.

2.4. Check the Serial Output

Launch the serial terminal on the PC and press the “EN” button on the development board. The system is now expected to enter the SPI boot mode and output the following:

```
Serial-COM3
ets Jun  8 2016 00:22:57

rst:0x1 (POWERON_RESET),boot:0x13 (SPI_FAST_FLASH_BOOT)
ets Jun  8 2016 00:22:57

rst:0x10 (RTCWDT_RTC_RESET),boot:0x13 (SPI_FAST_FLASH_BOOT)
configsip: 0, SPIWP:0x00
clk_drv:0x00,q_drv:0x00,d_drv:0x00,cs0_drv:0x00,hd_drv:0x00,wp_drv:0x00
mode:DIO, clock div:4
load:0x3ffc0000,len:0
load:0x3ffc0000,len:2412
load:0x40078000,len:3788
load:0x40098000,len:532
entry 0x4009813c

*****
*   hello espressif ESP32!   *
*   2nd boot is running!     *
*   version (v0.1)          *
*****
compile time 14:13:14

SPI Speed      : 20MHz
SPI Mode       : DIO
SPI Flash Size : 1MB

Partition Table:
## Label      Usage              Type            Offset   Length
0 factory    factory app           00 00 00010000 00100000
1 rfddata    RF data              01 01 00110000 00040000
2 wifidata   WiFi data           01 02 00150000 00040000
End of partition table
Loading app partition at offset 00010000
section 0: paddr=0x00000020 vaddr=0x00000000 size=0x0ffe8 ( 65512)
section 1: paddr=0x00010010 vaddr=0x3f400010 size=0x09774 ( 38772) map
section 2: paddr=0x0001978c vaddr=0x3ffb8860 size=0x03200 ( 12800) load
section 3: paddr=0x0001c994 vaddr=0x40080000 size=0x00400 ( 1024) load
section 4: paddr=0x0001cd9c vaddr=0x40080400 size=0x147e4 ( 83940) load
section 5: paddr=0x00031588 vaddr=0x00000000 size=0x0ea88 ( 60040)
section 6: paddr=0x00040018 vaddr=0x400d0018 size=0x30460 (197728) map
start: 0x40080788

Initializing heap allocator:
Region 19: 3FFBDA60 len 000225A0 tag 0
```

Figure 2-3. SPI Boot Mode Output

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