Joey Ah-kiow

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Education

University of Calgary | Calgary, AB

Sep. 2018 – Present

Expected Graduation: May 2023

B.Sc. in Electrical Engineering, GPA 3.75

- Minor in Computer Engineering
- Relevant coursework: Digital Systems Design, Digital Electronic Circuits, Computer Organization, Data Structures and Algorithms

Skills

Programming: Java, Python, C#, SQL, C, C++, VHDL, Verilog, SystemVerilog

Hardware: FPGA, PIC, Arduino, MIPS, RISC-V

Software: Quartus Prime, ModelSim, Verilator, GTKWave, GitHub, Linux

Experience

University of Calgary | Calgary, AB

Feb 2022 - Present

Research Assistant

- Conducting research in the domain of Hardware Security under Dr. Benjamin Tan
- Developing foundation in hardware security concepts and topics such as confidentiality, integrity, availability, access control, speculative execution attacks, side-channel attacks, threat modelling
- Completing security validation of compromised RISC-V SoCs at the RTL to find hardware security bugs, build an intuition in the hardware security validation process and find methods to detect potential vulnerabilities earlier in the design cycle

TC Energy | Calgary, AB May 2021 – Present

Field Data Program Management Intern

• Supporting the management of the Field Data program by revising official engineering documents, assisting internal and external stakeholders, maintaining and ensuring data quality, and completing various improvement initiatives

- Creating and managing various Power BI reports to enable data-driven decision making and improve workflow processes
- Developed a Python script to automate the data extraction from ~150 Excel-based forms, saving ~80 hours of work
- Developed a new reporting tool adopted by the Pipe Integrity department (~200 employees) to automate the escalation of reporting, resulting in 60-70% timesaving for management per week

Canadian Natural Resources Limited (CNRL) | Calgary, AB Data Provisioning Intern

May 2020 – Aug. 2020

- Developed and implemented SQL scripts to load, transform, and correct data for internal stakeholders
- Developed two applications using C# and .NET 4.8 to automate (1) the deployment of SSRS reports, and (2) the management of our Tableau server groups and users

Projects

IEEE Host Security Challenge – SoC track | GitHub

Apr. 2022 – Present

Competition

- Simulating and completing manual code review of an open-source RISC-V SoC to find potential security bugs and concerns, possible exploits and mitigations
- Familiarizing with the RISC-V unprivileged, privileged and debug specifications to understand and develop security requirements of the system

Countdown timer | GitHub

Jan. 2021 – Jan. 2021

Personal Project

- Designed and implemented a countdown timer using SystemVerilog, switches, pushbuttons, seven-segment displays, and LEDs
- Developed and integrated various modules like a BCD encoder, a Mealy FSM, a pushbutton handler/synchronizer and an LED blinker

Proximity-controlled System | GitHub

Jan. 2021 - Apr. 2021

Course Project

- Developed and simulated using VHDL, Quartus and ModelSim, and implemented on the Terasic DE10-Lite FPGA board
- Used an ADC to interface a proximity sensor and output the readings to seven-segment displays in voltage or distance units, and controlled the frequency of a buzzer and the brightness of an LED array using PWM
- Utilized shift registers to store the last 256 proximity sensor readings and average them to reduce the effect of outliers and stabilize the system input