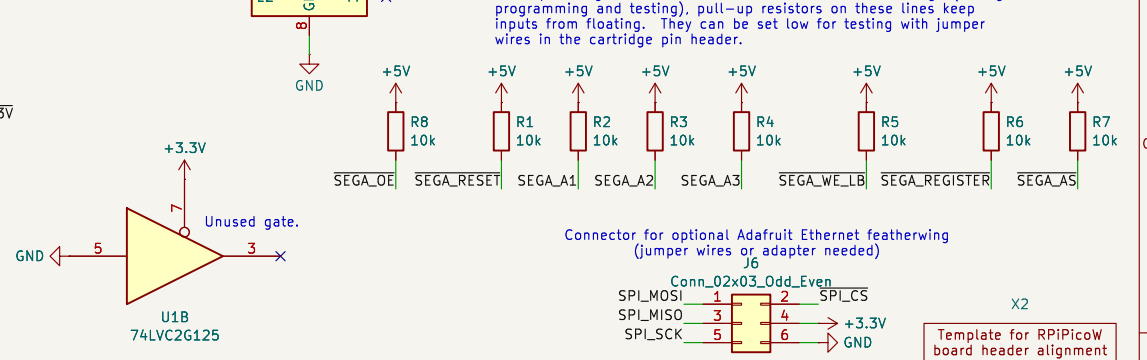
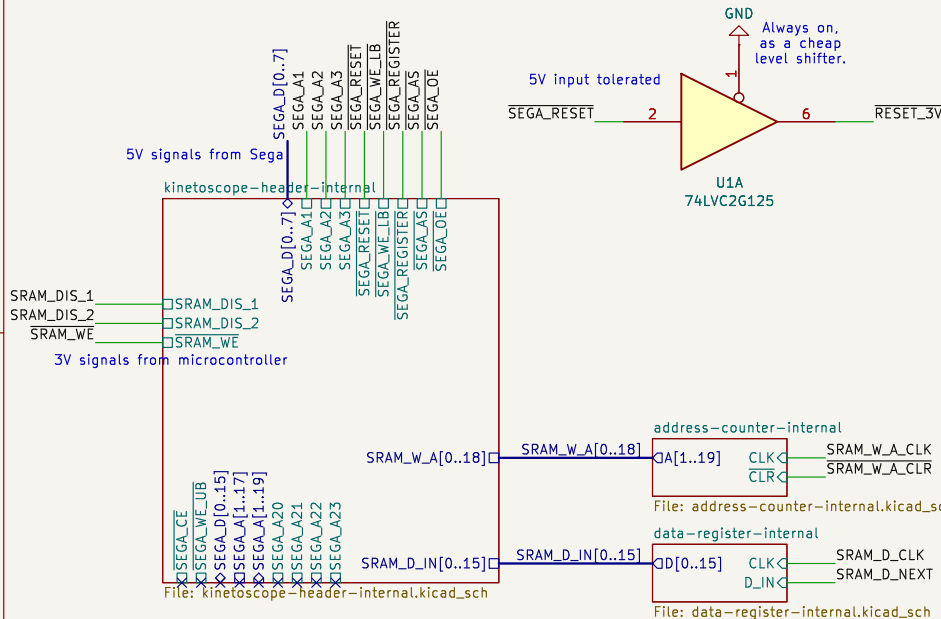
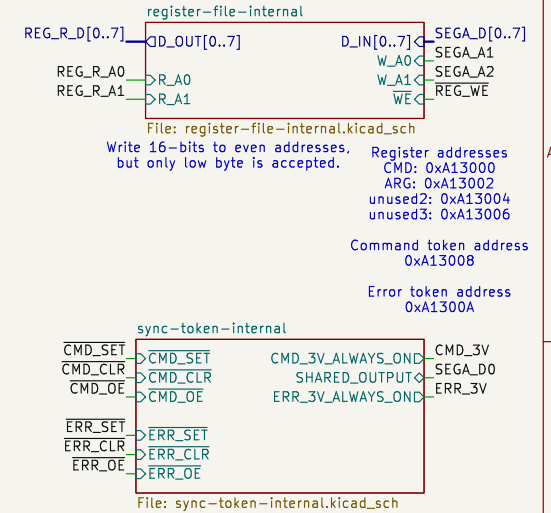
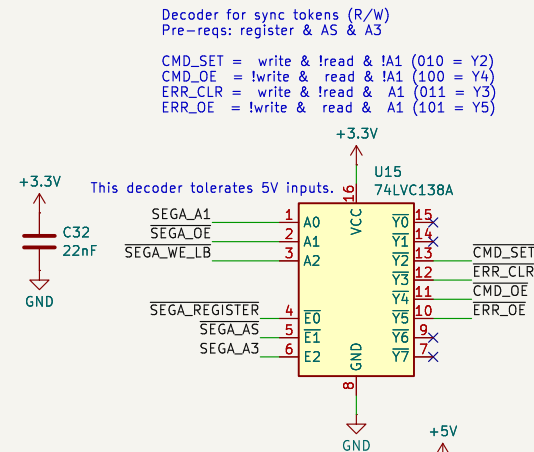
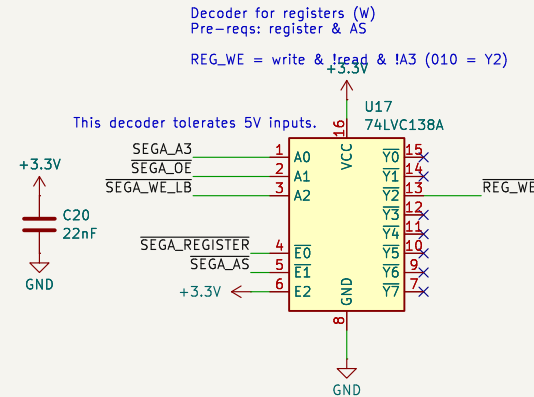
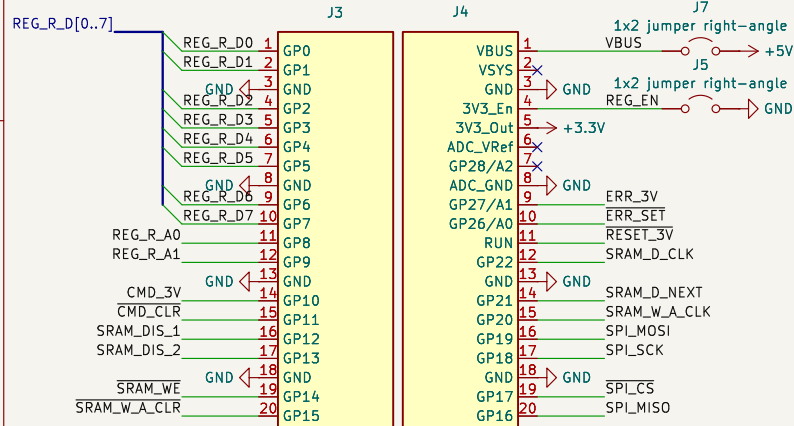


The 3.3V regulator built into the microcontroller is documented as only supplying a max of 300mA to external circuitry.

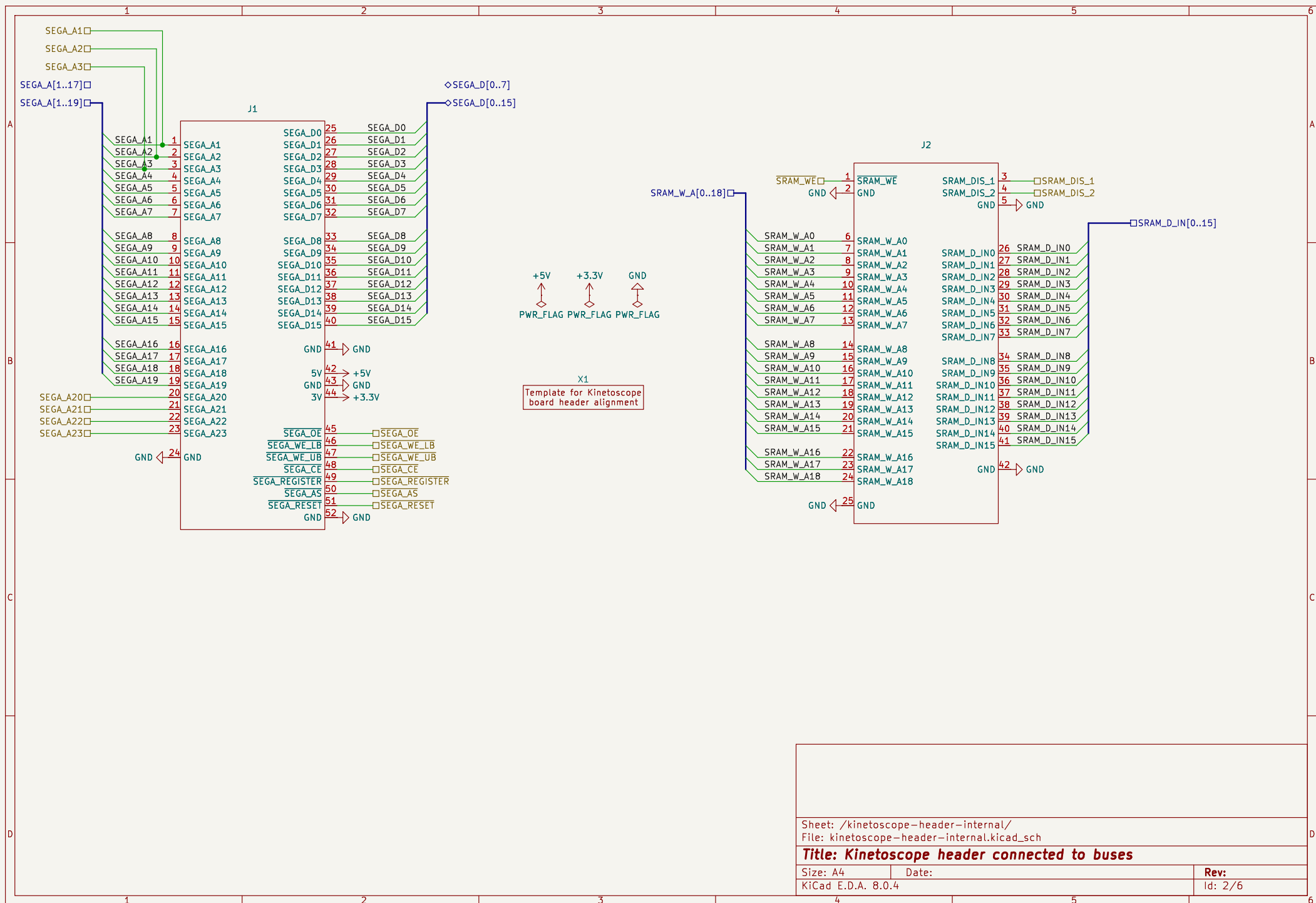
Rather than use that, we use our own regulator in the cartridge board. It can power the microcontroller and all 3V logic chips. Then we power the microcontroller through the 3V3_OUT and GND pins.

One jumper connects 3V3_EN connects to GND to disable the board's regulator. Another jumper connects VBUS to the +5V supply to power the 5V chips.

While programming the microcontroller in-place via USB, the jumper should be on the VBUS pins. While operating in the Sega, the jumper should be on the 3V3_EN pins.



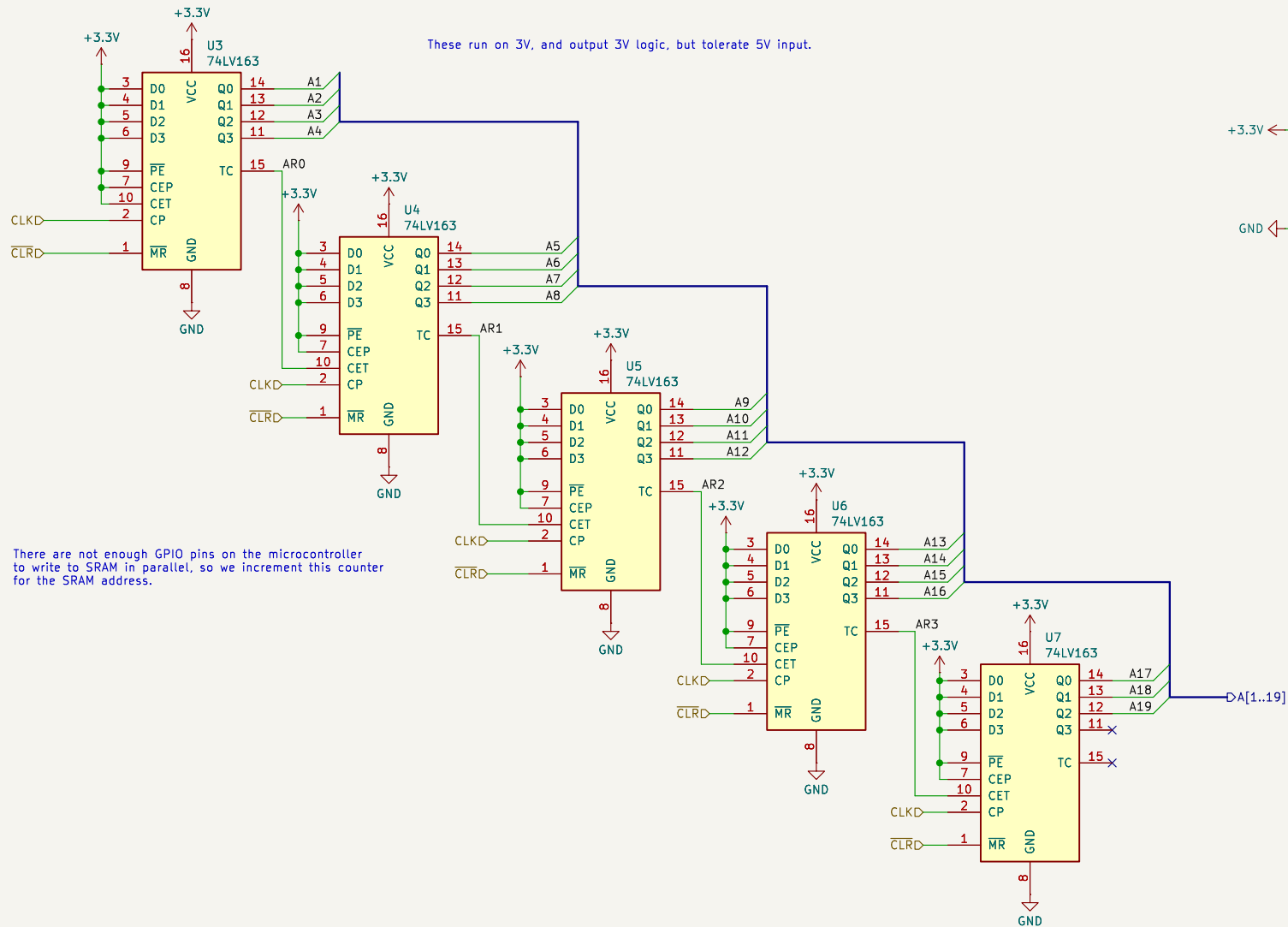
Sheet: /	File: microcontroller.kicad_sch
Title: Kinetoscope Microcontroller and Register Board	
Size: A4	Date:
KiCad E.D.A. 8.0.4	Rev: 1/6



Sheet: /kinetoscope-header-internal/
File: kinetoscope-header-internal.kicad_sch

Title: Kinetoscope header connected to buses

Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4		Id: 2/6



Sheet: /address-counter-internal/
File: address-counter-internal.kicad_sch

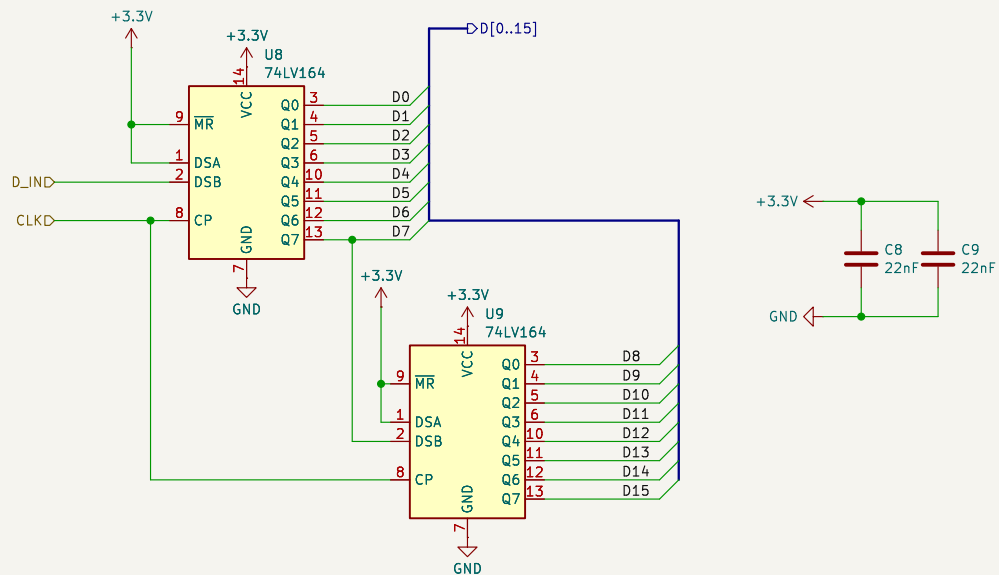
Title: Kinetoscope SRAM Address Counter

Size: A4
KiCad E.D.A. 8.0.4

Date:

Rev:
Id: 4/6

There are not enough GPIO pins on the microcontroller to write to SRAM in parallel, so we fill this data register serially.



Sheet: /data-register-internal/
File: data-register-internal.kicad_sch

Title: Kinetoscope SRAM Data Register

Size: A4
KiCad E.D.A. 8.0.4

Date:

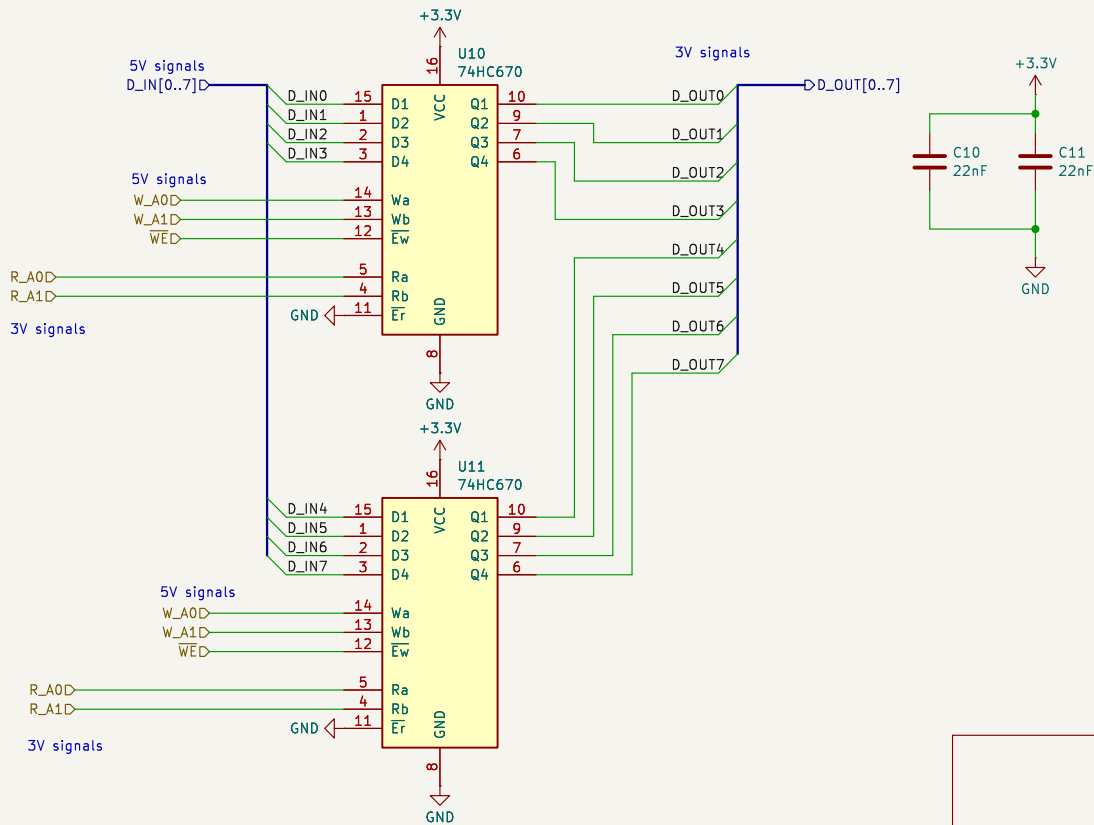
Rev:

Id: 5/6

This is a set of four 8-bit registers that can be independently read and written by the two processors (one reads, one writes).

The registers are assumed to be written by a device with a shared data bus (Sega Genesis) and read by a device with dedicated pins (microcontroller). The read output is always enabled.

Everything runs on +3.3V, but must be compatible with 5V inputs. If using a slightly different part number, please check input voltages in the data sheet. The data outputs are 3.3V.



Sheet: /register-file-internal/		
File: register-file-internal.kicad_sch		
Title: Kinetoscope Register File connected to buses		
Size: A4	Date:	Rev:
KiCad E.D.A. 8.0.4	Id: 6/6	

These are synchronization tokens between the M68k and the microcontroller.

The tokens will be cleared on boot/reset.

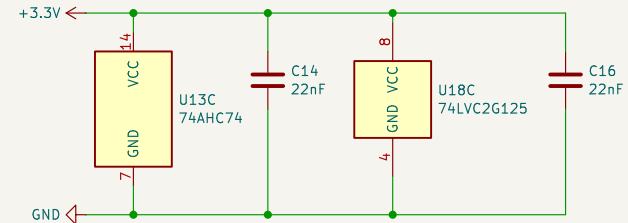
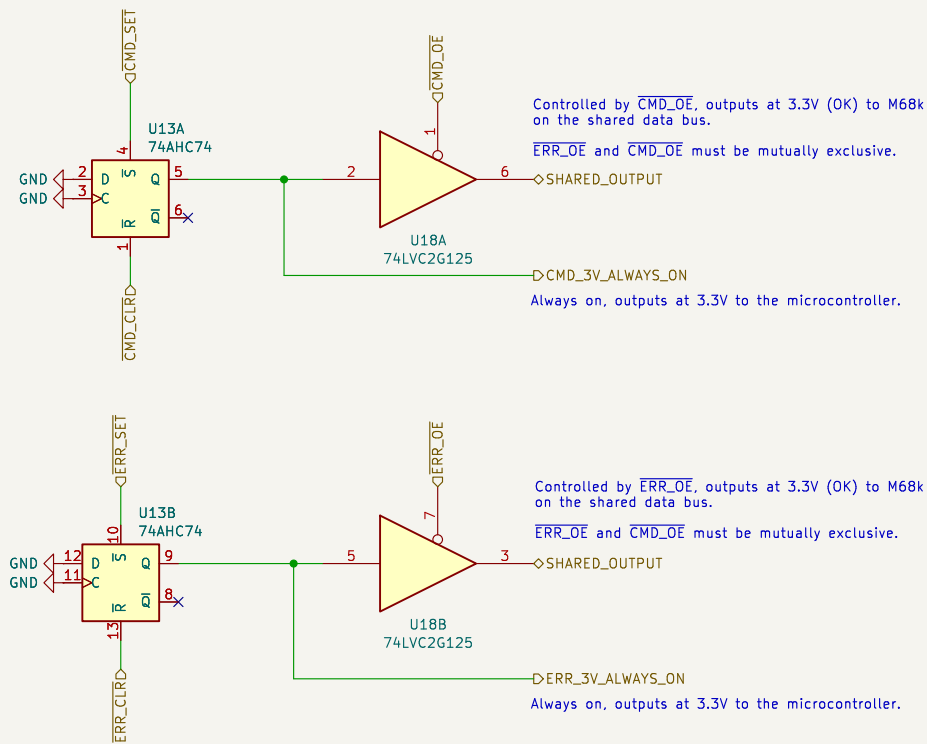
The M68k will be able to set the command token ($\overline{\text{CMD_SET}}$), indicating to the microcontroller that a command is ready to be executed.

The microcontroller will be able to clear the command token (CMD_CLR), indicating to the M68k that the command has been executed.

Command codes and arguments are passed through a separate register file.

The microcontroller can also flag an error ($\overline{\text{ERR_SET}}$) to the M68k, who can clear it (ERR_CLR) after noticing.

The M68k and microcontroller both will be able to read the current state of either token through OUTPUT_5V , which goes to the shared data bus with 5V logic. 3.3V version of both tokens are output to the microcontroller on dedicated lines.



Sheet: /sync-token-internal/
File: sync-token-internal.kicad_sch

Title: Kinetoscope Sync Token

Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 7/6