

NOTE: Both of these data buffers can be active at once.

SRAM\_CE is decoded from Sega address and control signals only.  
UC\_CONTROL is from the microcontroller only.

If this happens, the Sega will end up reading whatever word is being actively written by the microcontroller. This would not be catastrophic, and it could only happen if the microcontroller were unable to write to SRAM fast enough, and the Sega's read caught up to the write position.













