```
* Name: Square.c
      ^{\star} Purpose: Pulse gerator, with a duty cyle variable by the user.
 3
      * Note(s): Code modified from ST MicroElectronics Application Teams,
                 TIM_PWM_Output example project.
 6
7
 8
    #include "STM32F4xx.h"
10
    #include "LCD.h"
11
     #include "Sqaure.h"
12
13
     #define TIM3_CLK_OUT 42000
14
     #define TIM3_CNT_CLK 28000000
#define TIM3_ARR 665 //((TIM3_CNT_CLK / TIM3_CLK_OUT) - 1)
15
16
     #define FIFTY_PERCENT 333
17
18
19
    void Pulse_Config (void) {
20
       // Run timer config and initialise pulses to 50:50 duty cycle
2.1
       TIM3 Config();
22
       PWM_Config(TIM3_ARR);
2.3
24
25
     void TIM3 Config (void) {
       GPIO_InitTypeDef GPIO_InitStructure;
26
27
28
       /* TIM3 clock enable */
29
       RCC_APB1PeriphClockCmd(RCC_APB1Periph_TIM3, ENABLE);
30
31
       /* GPIOC clock enable */
32
       RCC_AHB1PeriphClockCmd(RCC_AHB1Periph_GPIOC, ENABLE);
33
       /* GPIOC Configuration: TIM3 CH1 (PC6) */
34
3.5
       GPIO_InitStructure.GPIO_Pin = GPIO_Pin_6 ;
36
       GPIO_InitStructure.GPIO_Mode = GPIO_Mode_AF;
       GPIO_InitStructure.GPIO_Speed = GPIO_Speed_100MHz;
GPIO_InitStructure.GPIO_OType = GPIO_OType_PP;
37
38
       GPIO InitStructure.GPIO PuPd = GPIO PuPd UP;
39
40
       GPIO Init(GPIOC, &GPIO InitStructure);
41
       /* Connect TIM3 pins to AF2 */
42
       GPIO PinAFConfig(GPIOC, GPIO PinSource6, GPIO AF TIM3);
43
44
45
46
     void PWM_Config(int period)
47
48
       TIM TimeBaseInitTypeDef TIM TimeBaseStructure;
       TIM OCInitTypeDef TIM OCInitStructure;
49
50
       uint16_t PrescalerValue = 0;
51
52
     /* Compute the prescaler value */
53
       PrescalerValue = (uint16 t) ((SystemCoreClock /2) / 28000000) - 1;
54
55
       /* Time base configuration */
       TIM TimeBaseStructure.TIM Period = 665;
56
57
       TIM_TimeBaseStructure.TIM_Prescaler = PrescalerValue;
58
       TIM_TimeBaseStructure.TIM_ClockDivision = 0;
59
       TIM TimeBaseStructure.TIM CounterMode = TIM CounterMode Up;
60
61
       TIM_TimeBaseInit(TIM3, &TIM_TimeBaseStructure);
62
       /* PWM1 Mode configuration: Channel1 */
63
64
       TIM OCInitStructure.TIM OCMode = TIM OCMode PWM1;
       TIM_OCInitStructure.TIM_OutputState = TIM_OutputState_Enable;
65
       TIM_OCInitStructure.TIM_Pulse = FIFTY PERCENT;
                                                                            // 50:50 duty cyle
66
67
       TIM OCInitStructure.TIM OCPolarity = TIM OCPolarity High;
68
69
       TIM OC1Init(TIM3, &TIM OCInitStructure);
70
71
       TIM OC1PreloadConfig(TIM3, TIM OCPreload Enable);
72
73
       TIM_ARRPreloadConfig(TIM3, ENABLE);
74
75
       /* TIM3 enable counter */
76
       TIM Cmd(TIM3, ENABLE);
77
78
```

D:\GitHub\Design---Construction\code\The Project\Project\Sqaure.c

```
void PWM_SetDC(uint16_t dutycycle)
80
81
      uint16_t newDutyCycle;
82
83
      // Calculate the new duty cycle
84
      newDutyCycle = (dutycycle * TIM3_ARR) / 100;
85
86
      // set the new duty cycle into the capture compare register
87
      TIM_SetCompare1(TIM3, newDutyCycle);
88
89
```