PRODUCT SPECIFICATION

Product Name	MSWH 1xN Optical Switch Series		
Product Model	Singlemode		
Description	Module 1		

1 DESCRIPTION

MSWH 1xN optical switch is based on micro-electro-mechanical system technology. It allows channel selection between an input fiber and up to N output fibers by rotating the MEMS mirror.

The switch is bi-directional and can also be used as a Nx1 selector switch. The optical switch offers highly reliable, durable, long-life operation in a compact package.

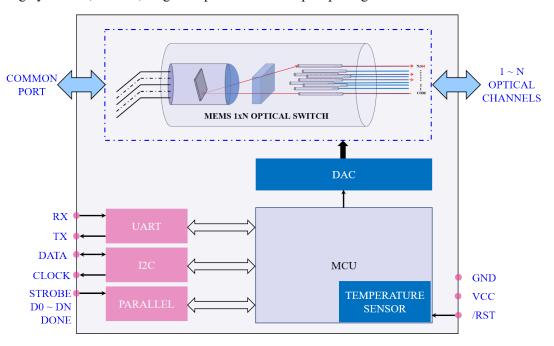


Figure 1 Functional Block Diagram

1.1 Features

- ✓ Proven MEMS durability and reliability
- ✓ Compact form factor
- ✓ Fast switching time
- ✓ Qualified to Telcordia GR-1073-CORE and RoHS requirements

1.2 Applications

- ✓ Fiber monitoring (Working with OTDR or OCM)
- ✓ Optical network routing
- ✓ Fiber sensing
- ✓ Resource sharing

✓ Optical network protection and restoration

2 MAIN SPECIFICATIONS

Table 1 Optical Specifications¹

PARAM	METER	VALUE	UNIT	NOTE
Wavelength		13:1290~1330 15:1525~1568 16:1600-1650	nm	Single-band: 13 or 15 or 16 Dual-band: 13&15 or 15&16 Full-band: 13&15&16 or 1290~1650
Test Wa	velength	1310/1550/1625 or 1650	nm	
Chai	nnels	2~64		N
	1x2	≤0.6, typical 0.5		
	1x4	≤0.8, typical 0.6		
	1x8	<u>≤</u> 0.8, typicai 0.0		1. IL is measured at CWL, 23°C
	1x12	≤1.0, typical 0.9		2. IL is for Single-band. Dual-band adds
Insertion Loss	1x16	<u></u>	dB	0.2dB
	1x24	≤1.3, typical 1.1		3. If with connectors, IL increases by
	1x32	≤1.3, typicar 1.1		0.2~0.3dB
	1x48	≤1.5, typical 1.2		
	1x64	≥1.3, typicai 1.2		
Return	n Loss	≥45	dB	Or customer specify
Repeat	ability	≤0.1	dB	Repeatability is defined after 100 cycles
Cros	stalk	≥40	dB	Or customer specify
Polarization D	ependent Loss	≤0.2	dB	
Wavelength D	ependent Loss	≤0.3	dB	WDL is measured at CWL±20nm, 23°C
Temperature Dependent	N≤16	≤0.3	dB	
Loss	16 <n≤64< td=""><td>≤0.4</td><td>uD.</td><td></td></n≤64<>	≤0.4	uD.	
Switch Time	N≤16	≤5		 Excluding protocol transmission time When using optimized voltage ramp
Switch Thire	16 <n≤64< td=""><td>≤10</td><td>ms</td><td>Faster 1xN switching time version available as a special request</td></n≤64<>	≤10	ms	Faster 1xN switching time version available as a special request
Dura	bility	≥1x10 ⁹	cycle	
Maximum O	ptical Power	≤500	mW	

^{1.} All specifications are without connectors.

Table 2 Electrical and Mechanical Specifications¹

PARAMETER	VALUE	UNIT	NOTE
Latching Type	Non-latching		
Control Type	UART or parallel		
Supply Voltage	5±0.25	V	
Power Consumption	≤0.7, typical 0.5	W	
	68x30x13		2≤N≤16
Dimension	75x30x13		16 <n≤32< td=""></n≤32<>
Dimension	75x30x16	mm	32 <n≤48< td=""></n≤48<>
	80x34x18		48 <n≤64< td=""></n≤64<>

1. All specifications belong to MS M1.

3 OPERATION/STORAGE TEMPERATURE/HUMIDITY

Table 3 Environmental conditions

PARAMETER	VALUE	UNIT	NOTE
Operation Temperature	-5~65	°C	
Storage Temperature	-40~85	°C	
Operation Humidity	5~95	%RH	
Storage Humidity	5~95	%RH	

4 PIGTAIL AND CONNECTOR

Table 4 Pigtail and connector type/length

PARAMETER	VALUE	UNIT	NOTE	
F:1 T	G657A2 or G657B3			
Fiber Type	250um bare fiber			
Fiber Pigtail	250um fiber or			
(All Ports)	900um loose tube			
Fiber Length	1 00+0 05		On avetamen anacify	
(All Ports)	1.00 ± 0.05	m	Or customer specify	
Optical Connector	or Name		O	
(All port)	None		Or customer specify	

5 MECHANICAL DIMENSIONS

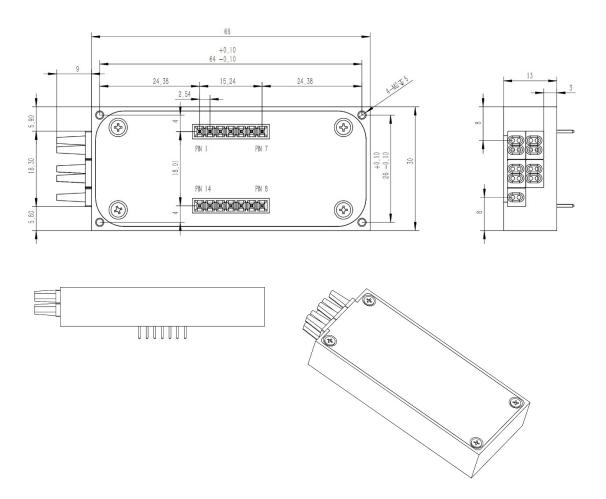


Figure 2 Module Type 1 (1xN, $2 \le N \le 16$)

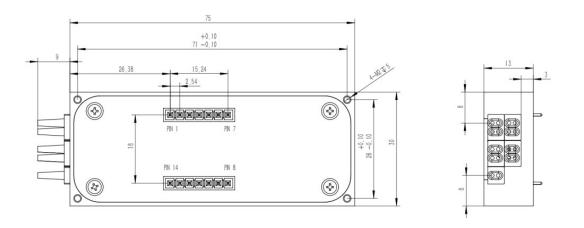


Figure 3 Module Type 1 (1xN, $16 < N \le 32$)

6 ELECTRONIC PINS DEFINITION

6.1 UART definition (Default)

Table 5 Electronic PIN Definition

Pin NO.	Name	Input/Output	Level	Function	
1	NC	No connect			
2	VCC	Power supply		+(5.0±5%) V Power Supply Max 150mA	
3	I/O		LVTTL	Reserved	
4	GND			Power supply ground	
5	I/O		LVTTL	Reserved	
6	TXD	Output	LVTTL	UART serial data output	
7	RXD	Input	LVTTL	UART serial data input	
8	I/O		LVTTL	Reserved	
9	I/O		LVTTL	Reserved	
10	I/O		LVTTL	Reserved	
11	Case GND			Case ground	
12	I/O		LVTTL	Reserved	
13	I/O		LVTTL	Reserved	
14	Reset	Input	LVTTL	Reset, low active, the pulse width needs 4ms	

6.2 UART & parallel definition (Optional)

Table 6 Electronic PIN Definition

Pin NO.	Name	Input/Output	Level	Function
1	NC	No connect		
2	VCC	Power supply		+(5.0±5%) V Power Supply Max 150mA
3	STROBE	Input	LVTTL	Falling edge active
4	GND			Power supply ground
5	D0	Input	LVTTL	
6	TXD	Output	LVTTL	UART serial data output
7	RXD	Input	LVTTL	UART serial data input
8	D4	Input	LVTTL	
9	D2	Input	LVTTL	
10	DONE	Output	LVTTL	Switch done, active low

Pin NO.	Name	Input/Output	Level	Function
11	Case GND			Case ground
12	D1	Input	LVTTL	
13	D3	Input	LVTTL	
14	Reset	Input	LVTTL	Reset, low active, the pulse width needs 4ms

6.3 UART Control Setting

Baud Rate: 115200

Start Bits: 1
Data Bits: 8

Parity: None Stop Bits: 1

Flow Control: None

6.4 Parallel digital I/O control

TTL control can only be used for optical switches with up to 32 channels with 5 TTL control pins ($D0 \sim D4$).

Channel	D4	D3	D2	D1	D0
CH1	0	0	0	0	0
CH2	0	0	0	0	1
СН3	0	0	0	1	0
CH4	0	0	0	1	1
CH8	0	0	1	1	1
CH16	0	1	1	1	1
CH32	1	1	1	1	1

NOTE:

When optical switch is 1x8, D3 and D4 is not used. Similarly for other 1xN.

7 COMMUNICATION PROTOCOL

7.1 Protocol 1 (Default)

Command

FLAG LEN RES	CMD	DATA	SUM
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2 Byte	1 Byte	1 Byte	1 Byte	1 Byte
2 Dyte	1 Dyte	1 Dyte	1 Dyte	1 Dyte

FLAG: 0xEFEF or 0xAAAA

LEN: Total number of bytes from RES to SUM

RES: 0xFF

SUM: Checksum, SUM=FLAG+LEN+RES+CMD+DATA

Response

FLAG	LEN	RES	RESP	DATA	SUM
2 Byte	1 Byte	1 Byte	1 Byte		1 Byte

FLAG: 0xEDFA

LEN: Total number of bytes from RES to SUM

RES: 0xFF

SUM: Checksum, SUM=FLAG+LEN+RES+ RESP+DATA

Command List

1) Set Channel							
	FLAG1	LEN	RES	CMD	DATA	SUM	
Command	0xEFEF	0x04	0xFF	0x04	1 byte	SUM	
	DATA = Channel						
	FLAG2	LEN	RES	RESP	DATA	SUM	
	0xEDFA	0x04	0xFF	0x04	1 byte	SUM	
Response	DATA = Resu	ılt					
	Result = $0xE$	E Success					
	Result = $0xE$	F Fail					

2) Get Channel							
Command	FLAG1	LEN	RES	CMD	DATA	SUM	
	0xEFEF	0x03	0xFF	0x02		SUM	
Response	FLAG2	LEN	RES	RESP	DATA	SUM	
	0xEDFA	0x04	0xFF	0x02	1 byte	SUM	
	DATA = Char	nnel					

3) Read Module Information						
Command	FLAG1	LEN	RES	CMD	DATA	SUM

	0xEFEF	0x03	0xFF	0x01		SUM
	FLAG2	LEN	RES	RESP	DATA	SUM
Response	0xEDFA	0x27	0xFF	0x01	36 bytes	SUM

DATA = Module information

Item	Bytes	Type	Note
Vendor Code	10 ASCII		
Reserved	10	ASCII	Information about the channel and the type
Hardware Version 2		Hex	X.Y (X—byte0 Y—byte1)
Firmware Version 2		Hex	X.Y (X—byte0 Y—byte1)
Production Date	4	Hav	YYYY—MM—DD
Production Date	4	Hex	YYYY—byte0 byte1 MM—byte2 DD—byte3
Serial Number	8	ASCII	

4) Reset Module							
	FLAG1	LEN	RES	CMD	DATA	SUM	
Command	0xEFEF	0x03	0xFF	0x03		SUM	
	FLAG2	LEN	RES	RESP	DATA	SUM	
Response	0xEDFA	0x03	0xFF	0x03		SUM	

7.2 Protocol 2 (Optional)

1) Set Channel					
	<chn_xxx></chn_xxx>	XXX: 000~N (From 1 to maximun channel)			
Commond	Set channel 1: <chn_< td=""><td>001></td></chn_<>	001>			
Command	Set channel 8: <chn_008></chn_008>				
	Set channel 64: <chn< td=""><td>_064></td></chn<>	_064>			
	<chn_ok></chn_ok>	Set channel successfully			
Response	<chn_e1></chn_e1>	Channel overflow			
	<chn_e2></chn_e2>	Other error			

2) Get Channel				
Command	<status_?></status_?>			
Response	<sta_m_c></sta_m_c>	M: The maximum channel		
		C: The current channel		

8 FIBER LENGTH

 $1.00\pm0.05M$

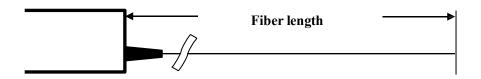


Figure 4 Fiber Length Definition

9 LABEL DEFINITION

9.1 On the module

P/N: xxxxxxxxxx

S/N: xxxxxxxx

9.2 Label Definition



X=com,CH1,CH2,CH3.....CHN

Figure 5 Label Definition

9.3 Others

ESD Packing

RoHS and GR-1073 compliance