

## Content Indexing

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# RK3328

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### Note:

#### Component parameter description


1. DNP stands for component not mounted temporarily

2. If Value or option is DNP, which means the area is reserved without being mounted



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5					4					3					2					1				
Version		Date		Author		Change Note										Approved								
V1.0		20170718		ZDZ		First edictor																		
V1.0		20200509		DCL		1.Page5, 增加led指示模块, KEY-RESET, FAN模块, 2.Page9, 增加EEPROM WITH MAC ADDRESS模块, SPI-FLASH模块; 3.Page13, 14, 网口座子增加ESD器件,网口两指示灯增加上拉电阻; USB3.0的RX,TX与CPU管脚顺序对调; 4.Page12, CON12座子网络定义同ZERO (1组I2C,1组UART,LINEOUTR-L,TV-OUT),MIC没有导出来; PAGE10 GMAC_TXCLK修改,HOLE15接PGND,OTG接口增加ESD器件, 去掉HDMI1V8电源。 5.增加HDMI1V8电源, PMU处晶振封装修改,RTL8153B 的RX,TX顺序交换; DC5V供电座子修改成TYPE-C接口。  6.USB2.0座子封装修改, 工厂容易贴片; R906 NC,R908贴片; 13PIN座子转180°, 4个螺丝孔内移																		
V1.1		20200811																						
V1.2		20200929																						



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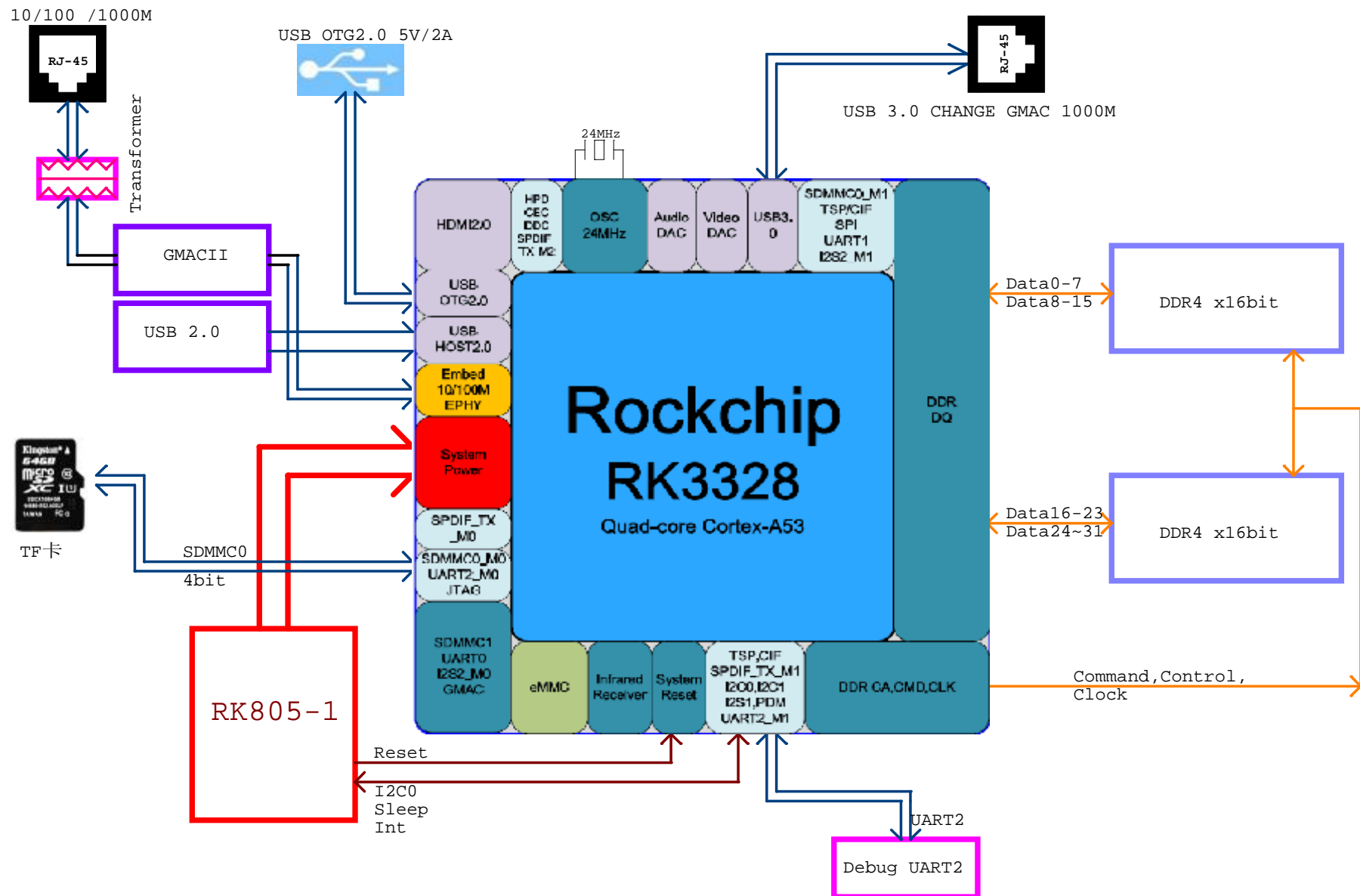
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of

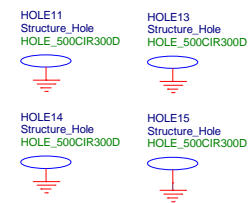
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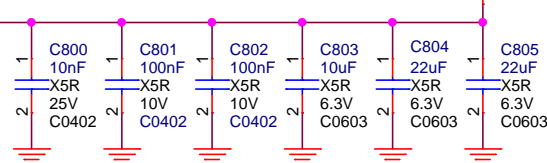
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U800N

ARM

VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE

P13  
P14  
R13  
R14  
T13  
T14  
T15

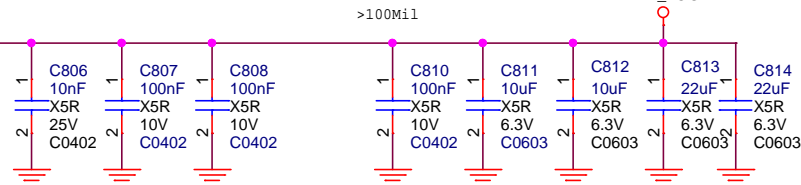


PCB LAYOUT注意: 电容都要靠近主控, 其中一个22uF电容需要放在管脚正下方。

GPU/Logic

VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC  
VDD\_LOGIC

N9  
N10  
N11  
P9  
P10  
P11  
R9  
R10  
R11

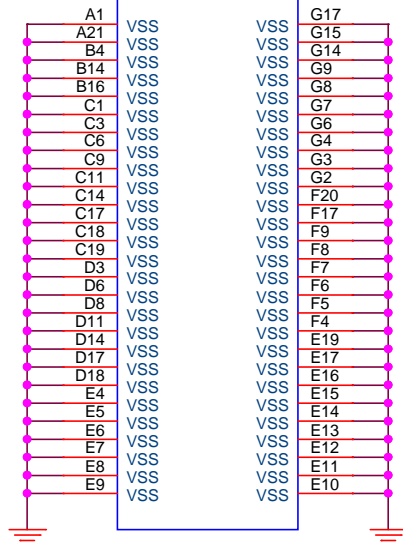


PCB LAYOUT注意: 电容都要靠近主控, 其中一个22uF电容需要放在管脚正下方。

RK3328

BGA395\_14R00X14R00X1R24

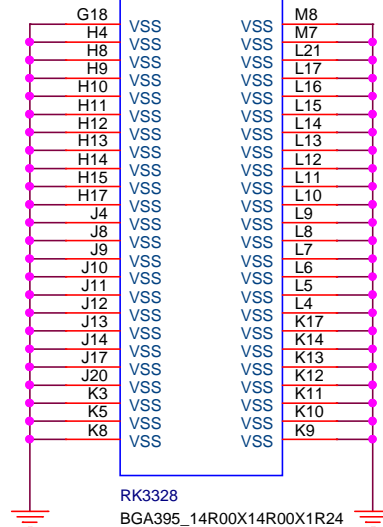
U800O



RK3328

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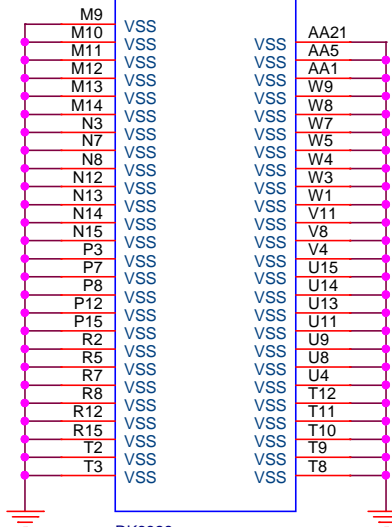
U800P



RK3328

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U800Q



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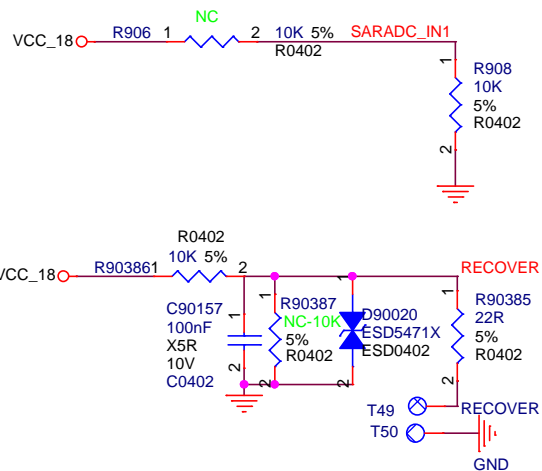
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CPU Power

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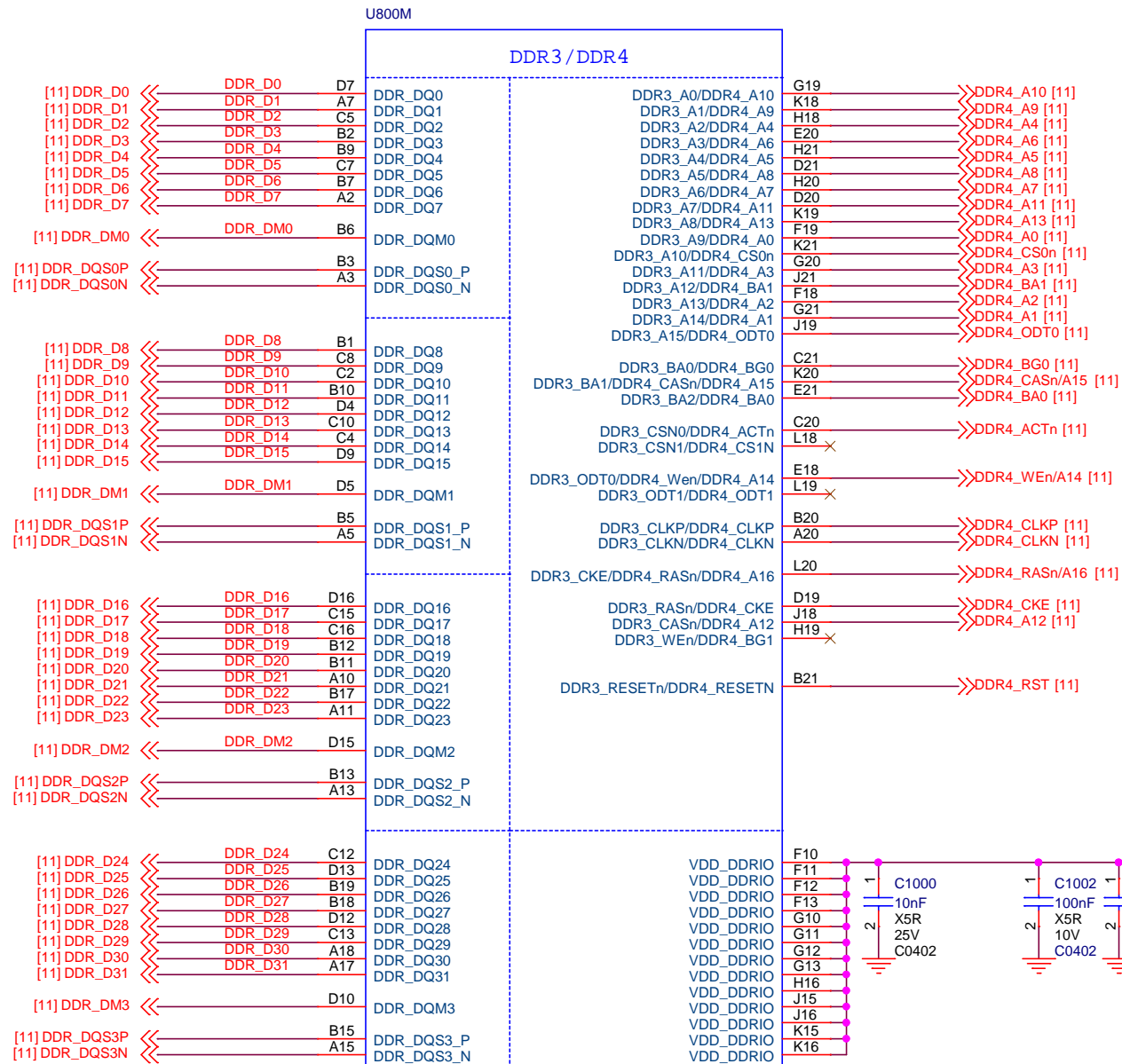
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SARADC_IN0 BOM版本	SARADC_IN1 PCB版本	Up阻值	Down阻值	
Recover		DNP	10K	
BOM1		110K	10K	
BOM2		100K	20K	
BOM3		100K	33K	
BOM4		36K	18K	
BOM5		51K	36K	
BOM6		51K	51K	
BOM7		36K	51K	
BOM8		18K	36K	
BOM9		33K	100K	
BOM10		20K	100K	
BOM11		10K	110K	
BOM12		10K	DNP	



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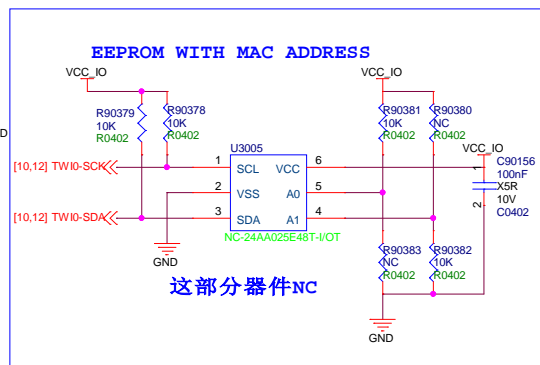
RK3328  
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U800G

```
001
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FDMT2_0_out
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BGA395\_14R00X14R00X1R24

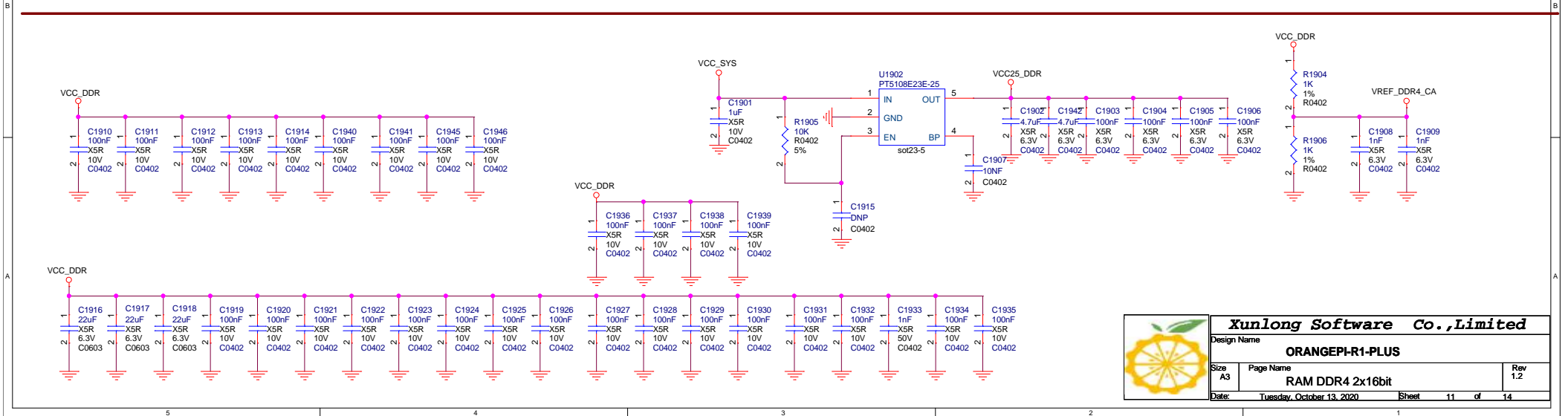
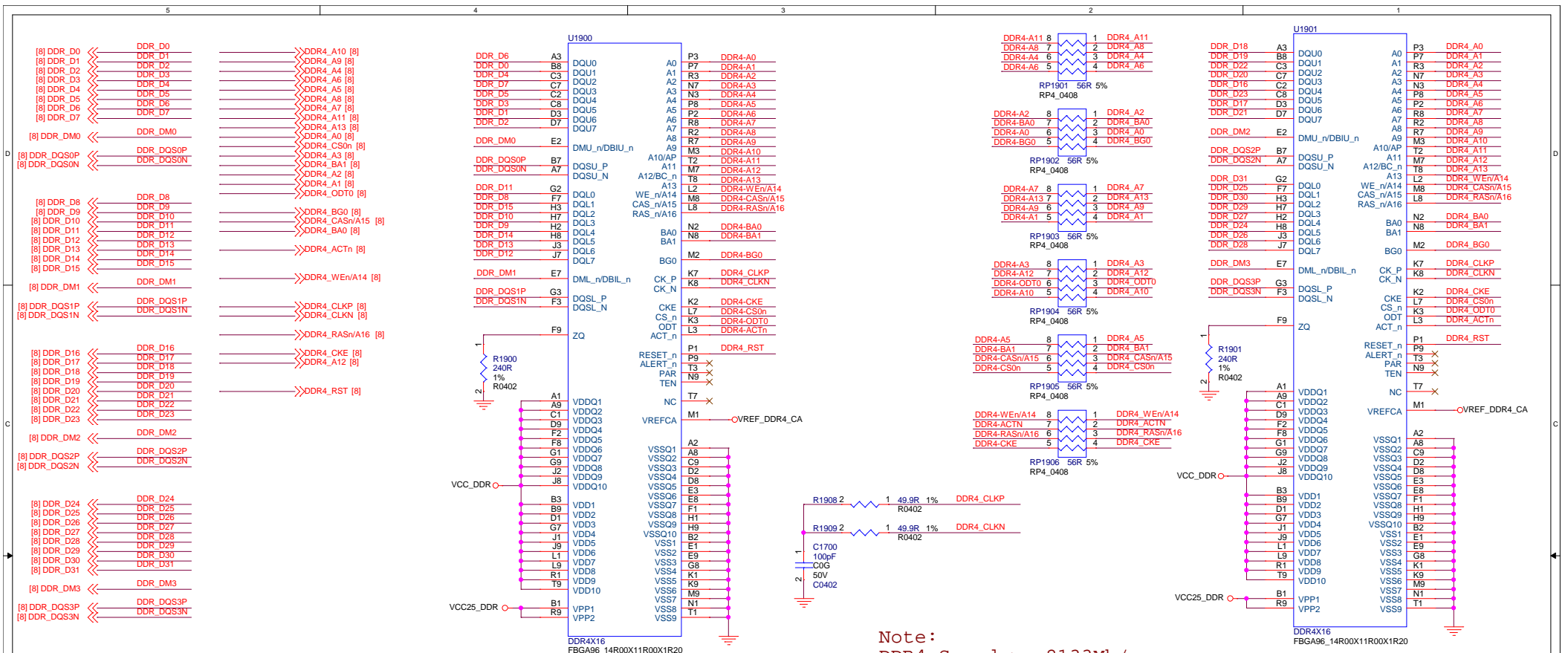
RK3328  
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RK3328  
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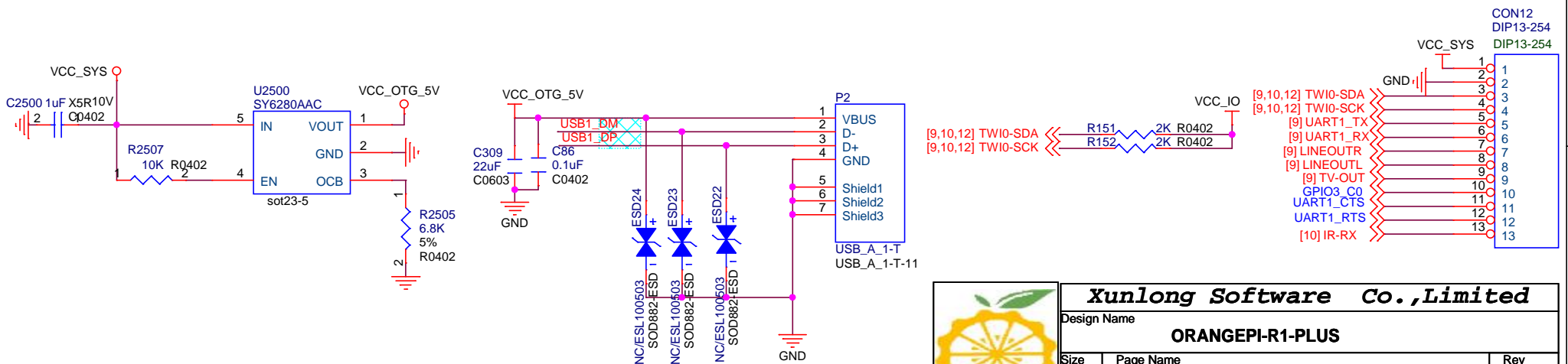
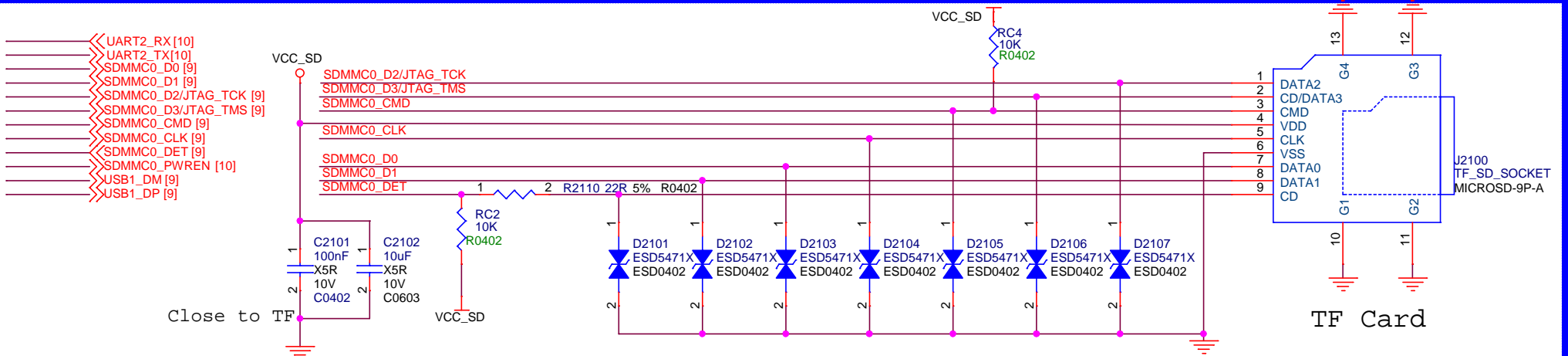
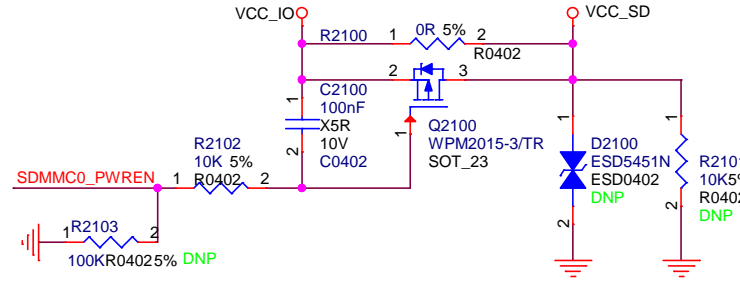
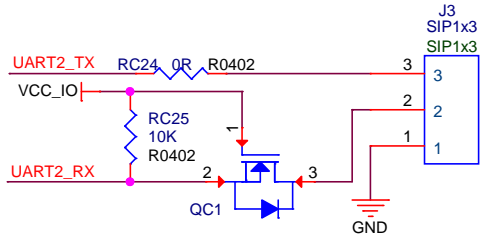
45 SPI FLASH







## Debug UART2



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TF/SD Card/debug/usb2.0

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10/100/1000 MII Ethernet PHY  
EMAC-MII