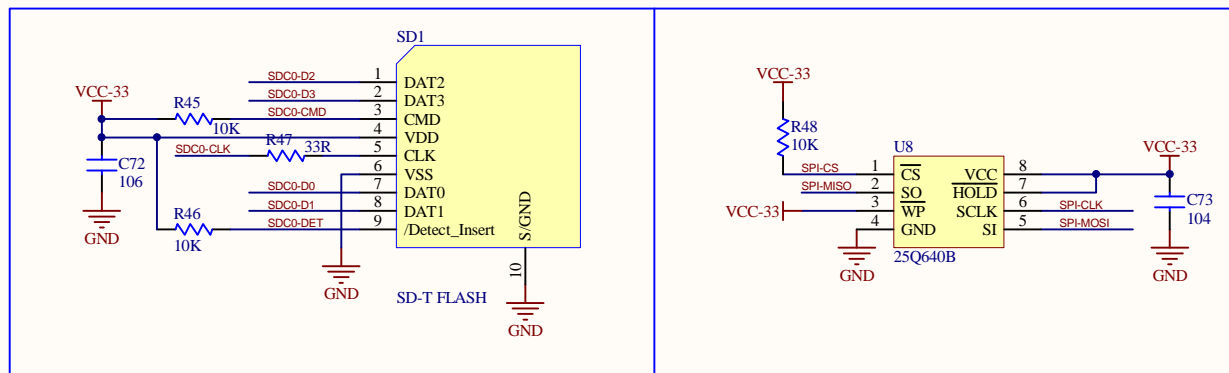
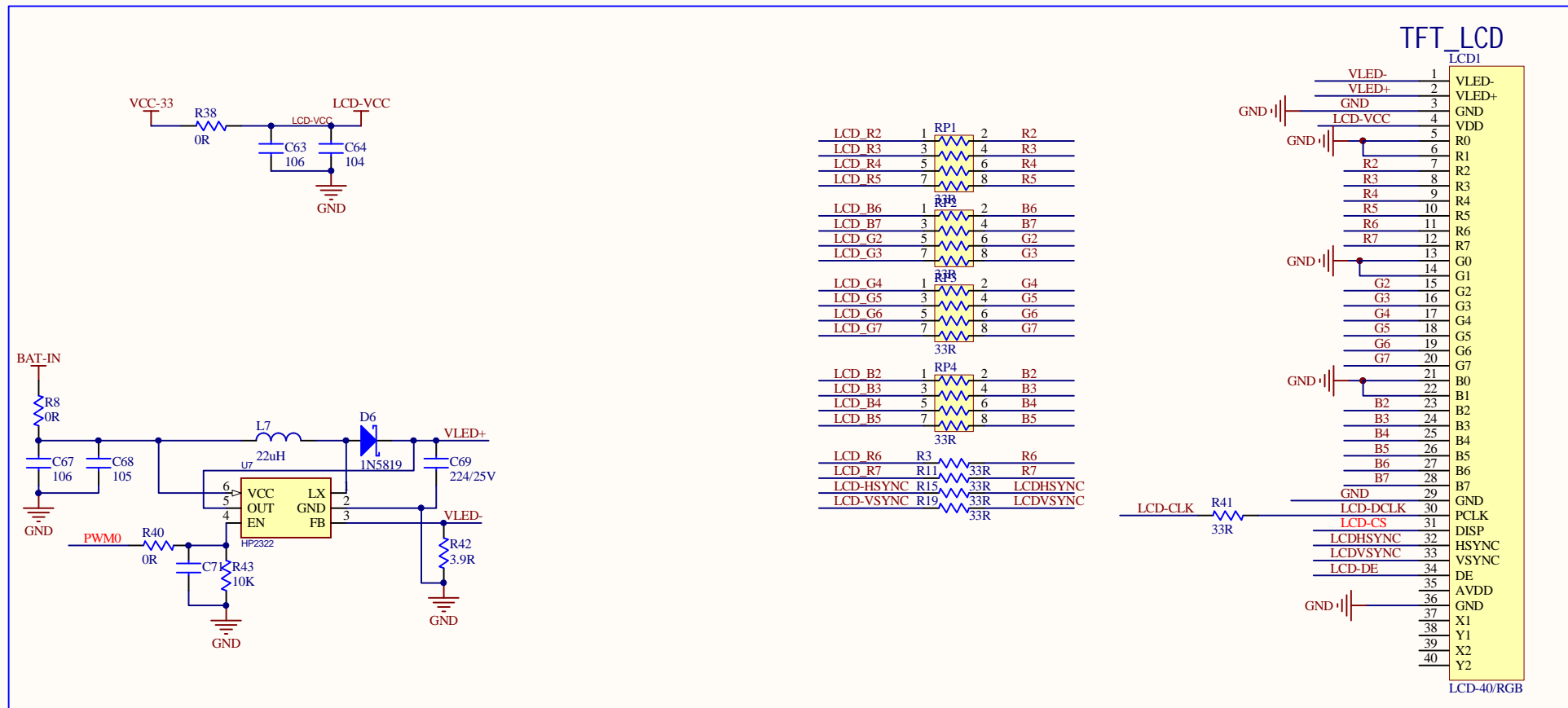


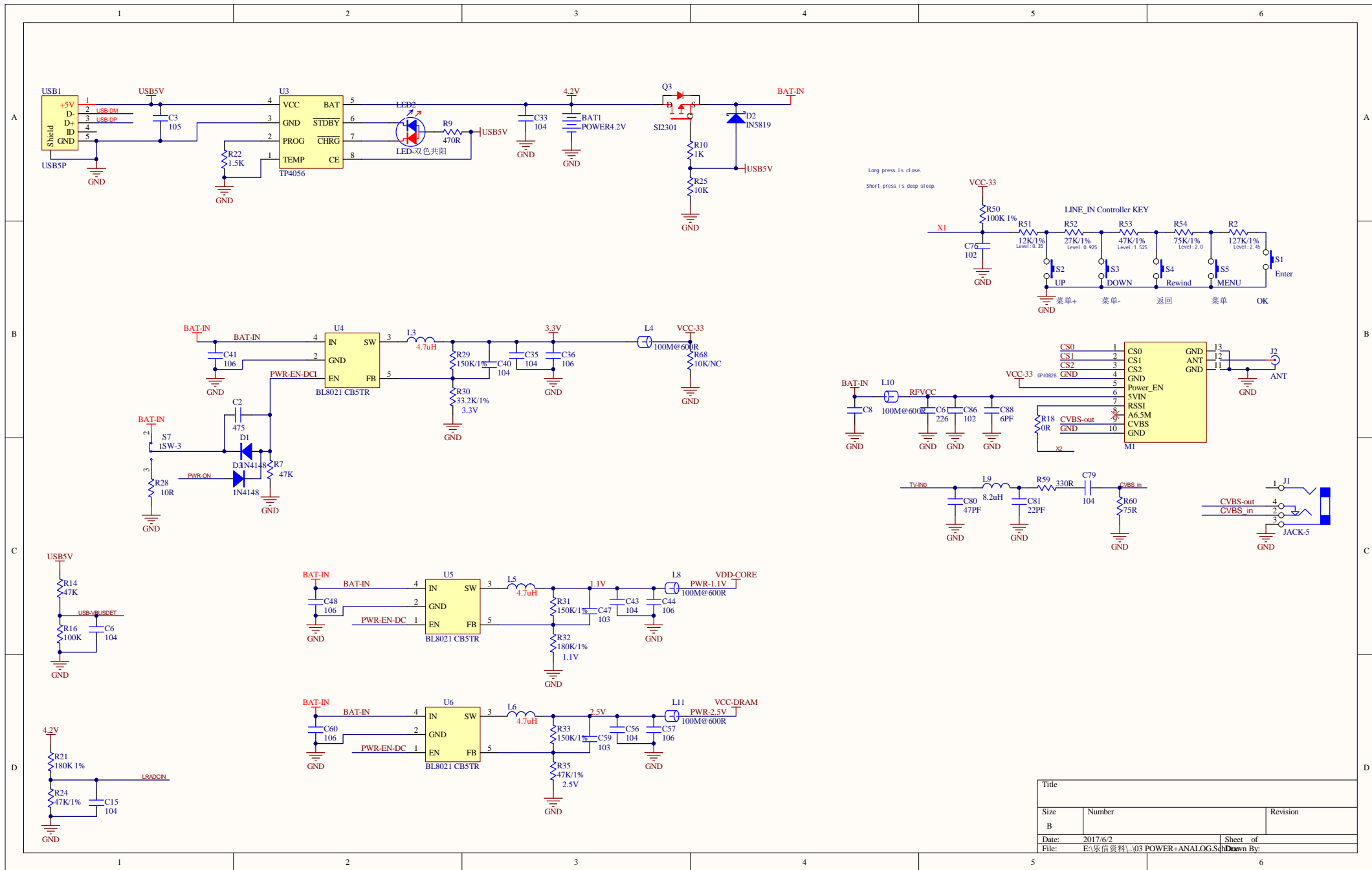
MCU

F1C100S

Title		
Size	Number	Revision
B	Ensure one Power pin with a cap place caps closed to CPU	
Date:	2017/6/2	Sheet of
File:	弘泰昌资料\01 CPU\SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2017/6/2	Sheet of
File:	E:\乐信资料\02 TFT LCD.SchDoc	Drawn By:



Title		
Size	Number	Revision
B		
Date:	2017/6/2	Sheet of
File:	E:\东信资料\1_03 POWER+ANALOG.SchDoc	Drawn By: