

axis_data_width_converter.v

AUTHORS

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DATES

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INFORMATION

Brief

AXIS DATA WIDTH CONVERTER

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axis_data_width_converter

```
module axis_data_width_converter #(
    parameter
    SLAVE_WIDTH
    =
    1,
    parameter
    MASTER_WIDTH
    =
    1,
    parameter
    REVERSE
```

```

    =
    0
) ( input aclk, input arstn, output [(MASTER_WIDTH*8)-1:0] m_axis_tdata, out

```

Change size of streaming bus in even integers of. 1/2 2/1 2/4 4/2 etc.

Parameters

SLAVE_WIDTH parameter	Width of the slave input bus in bytes
MASTER_WIDTH parameter	Width of the master output bus in bytes
REVERSE parameter	Change byte order

Ports

aclk	Clock for AXIS
arstn	Negative reset for AXIS
m_axis_tdata	Output data
m_axis_tvalid	When active high the output data is valid
m_axis_tready	When set active high the output device is ready for data.
m_axis_tlast	Indicates last word in stream.
s_axis_tdata	Input data
s_axis_tvalid	When set active high the input data is valid
s_axis_tready	When active high the device is ready for input data.
s_axis_tlast	Is this the last word in the stream (active high).