

tb_axis.v

AUTHORS

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DATES

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INFORMATION

Brief

Test bench for axis_fifo using axis stim and clock stim.

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tb_axis

```
module tb_axis #(
  parameter
  IN_FILE_NAME
  =
  "in.bin",
  parameter
  OUT_FILE_NAME
  =
  "out.bin",
  parameter
  FIFO_DEPTH
  =
  128,
  parameter
```

```
RAND_READY  
=  
0  
)
```

Test bench for axis_fifo. This will run a file through the system and write its output. These can then be compared to check for errors. If the files are identical, no errors. A FST file will be written.

Parameters

IN_FILE_NAME parameter	File name for input.
OUT_FILE_NAME parameter	File name for output.
FIFO_DEPTH parameter	Number of transactions to buffer.
RAND_READY parameter	0 = no random ready. 1 = randomize ready.

INSTANTIATED MODULES

clk_stim

Generate a 50/50 duty cycle set of clocks and reset.

slave_axis_stim

Device under test SLAVE stimulus module.

dut

Device under test, axis_fifo

master_axis_stim

Devie under test MASTER stimulus module.