tb axis.v

AUTHORS

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DATES

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INFORMATION

Brief

Test bench for axis_string_to_axis_data using axis stim and clock stim.

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tb axis

module tb_axis

Test bench for axis_string_to_axis_data. This will run a file through the system and write its output. These can then be compared to check for errors. If the files are identical, no errors. A FST file will be written.

INSTANTIATED MODULES

clk_stim

Generate a 50/50 duty cycle set of clocks and reset.

slave axis stim

Device under test SLAVE stimulus module.

dut

```
axis_string_to_axis_data #(

DELIMITER(";"),

TERMINATION("\n"),

STRING_LEN(4),

MBUS_WIDTH(BUS_WIDTH),

USER_WIDTH(USER_WIDTH),

DEST_WIDTH(DEST_WIDTH),

PREFIX_LEN(1),

DATA_PREFIX("#"),

DEST_PREFIX("&"),

USER_PREFIX("&"),

KEYWORD_LEN(3),
```

```
SET_KEYWORD("set"),

CLR_KEYWORD("clr")
) dut ( .aclk(tb_dut_clk), .arstn(tb_dut_rstn), .m_axis_tdata(tb_dut_data),
```

Device under test, axis_string_to_axis_data

master_axis_stim

Devie under test MASTER stimulus module.