

tb_axi_lite_slave.v

AUTHORS

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DATES

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INFORMATION

Brief

Test bench for axi lite slave

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tb_axi_lite_slave

```
module tb_axi_lite_slave ()
```

Test bench for axi lite slave

axi_lite_block_ram

```
axi_lite_block_ram #(
```

```
ADDRESS_WIDTH(32),  
BUS_WIDTH(4),  
DEPTH(256)  
) dut ( .aclk(tb_data_clk), .arstn(tb_rstn), .s_axi_awvalid(tb_s_axi_awvalid
```

Module instance of axi_lite_block_ram