

tb_wishbone_slave.v

AUTHORS

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DATES

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INFORMATION

Brief

Test bench for wishbone_slave

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tb_wishbone_slave

```
module tb_wishbone_slave #(
  parameter
    ADDRESS_WIDTH
    =
    32,
  parameter
    BUS_WIDTH
    =
    4,
  parameter
    DEPTH
```

```
    =  
    512,  
    parameter  
    RAM_TYPE  
    =  
    "block",  
    parameter  
    HEX_FILE  
    =  
    ""  
    )
```

Test bench for wishbone slave

Parameters

ADDRESS_WIDTH parameter	Width of the axi address bus in bits.
BUS_WIDTH parameter	Bus width for data paths in bytes.
DEPTH parameter	Depth of the RAM in terms of data width words.
RAM_TYPE parameter	Used to set the ram_style attribute.
HEX_FILE parameter	Hex file to write to RAM.

inst_dc_block_ram

Module instance of dc_block_ram