

tb_clk.v

AUTHORS

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DATES

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INFORMATION

Brief

Generic clock test bench top.

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tb_clk

```
module tb_clk
```

Generic clock test bench top.

INSTANTIATED MODULES

clk_stim

```
clk_stimulus #(
    CLOCKS(NUM_CLKS),
    CLOCK_BASE(1000000),
    CLOCK_INC(1000),
    RESETS(NUM_RSTS),
    RESET_BASE(2000),
    RESET_INC(100)
) clk_stim ( .clkv(tb_stim_clk), .rstnv(tb_stim_rstn), .rstv() )
```

Generate a clock for the modules.