

# test.v

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## AUTHORS

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## DATES

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## INFORMATION

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### Brief

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Test bench for apb using cocotb

### License MIT

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### test

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```
module test #(
  parameter
  ADDRESS_WIDTH
  =
  32,
  parameter
  BUS_WIDTH
  =
  4
) ( input clk, input rstn, inout [ADDRESS_WIDTH-1:0] apb_paddr, inout apb_ps
```

Test bench loop for apb

## Parameters

<b>ADDRESS_WIDTH</b> <small>parameter</small>	Width of the APB3 address port in bits.
<b>BUS_WIDTH</b> <small>parameter</small>	Width of the APB3 bus data port in bytes.

## Ports

<b>clk</b>	Clock
<b>rstn</b>	Negative reset
<b>apb_paddr</b>	APB3 address bus, up to 32 bits wide.
<b>apb_psel</b>	APB3 select per slave (1 for this core).
<b>apb_penable</b>	APB3 enable device for multiple transfers after first.
<b>apb_pready</b>	APB3 ready is a output from the slave to indicate its able to process the request.
<b>apb_pwrite</b>	APB3 Direction signal, active high is a write access. Active low is a read access.
<b>apb_pwdata</b>	APB3 write data port.
<b>apb_prdata</b>	APB3 read data port.
<b>apb_pslverror</b>	APB3 error indicates transfer failure, not implimented.