

# absbus.py

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## AUTHORS

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## DATES

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## INFORMATION

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### Brief

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abstraction of the xilinx fifo bus

### License MIT

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## xilinxFIFOtrans

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transaction

xilinxFIFOtrans

create an object that associates a data member and ? for operation.

## xilinxFIFOsourceState

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enum.IntEnum

**xilinxFIFOsourceState**

An enum class that provides the current state and will change states per spec.

## xilinxFIFOsinkState

enum.IntEnum

**xilinxFIFOsinkState**

An enum class that provides the current state and will change states per spec.

## xilinxFIFObase

busbase

**xilinxFIFObase**

xilinxFIFOsink

xilinxFIFOsource

abstract base class that defines Xilinx FIFO signals

## FUNCTIONS

### \_\_init\_\_

```
def __init__(
    self,
    entity,
    name,
    clock,
    resetn,
    fwft
    =
    False,
    ack
    =
    False,
    args,
    kwargs
)
```

Setup defaults and call base class constructor.