driver.py

AUTHORS

JAY CONVERTINO

DATES

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INFORMATION

Brief

Bus Driver for Xilinx FIFO

License MIT

xilinxFIFOsource

Drive xilinx FIFO write interfaces

VARIABLES

_signals

```
_signals
```

List of signals that are required

_optional_signals

```
_optional_signals
```

List of optional signals, these will never be required but will be used if found.

FUNCTIONS

__init__

```
def __init__(
self,
entity,
name,
clock,
resetn,
fwft
=
False,
ack
=
False,
*
args,
**
kwargs
)
```

Setup defaults and call base class constructor.

write

```
async def write(
self,
data
)
```

Write to a address some data

_check_type

```
def _check_type(
self,
trans
)
```

Check and make sure we are only sending xilinxFIFOtrans

_run

```
async def _run(
self
)
```

_run thread that deals with read and write queues.

xilinxFIFOsink

Drive xilinx FIFO read interfaces

VARIABLES

_signals

```
_signals
```

List of signals that are required

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List of optional signals, these will never be required but will be used if found.

FUNCTIONS

__init__

```
def __init__(
self,
entity,
name,
clock,
resetn,
fwft
=
False,
                                                                *
args,
                                                               **
kwargs
)
```

Setup defaults and call base class constructor.

write

```
async def write(
self,
data
)
```

Write to a address some data

read

```
async def read(
self,
data
)
```

Read from a address and return data

_check_type

```
def _check_type(
self,
trans
)
```

Check and make sure we are only sending xilinxFIFOtrans

_run

```
async def _run(
self
)
```

_run thread that deals with read and write queues.