absbus.py
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DATES
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INFORMATION
Brief
abstraction of the Analog Devices uP bus
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uptrans
transaction
uptrans
create an object that associates a data member and address for operation.
upState
enum.IntEnum

```
upState
```

An enum class that provides the current state and will change states per spec.

upBase

```
upBase
upEchoSlave
upMaster
upMonitor
```

abstract base class that defines uP signals

VARIABLES

_signals

```
_signals
```

List of signals that are required

FUNCTIONS

init

```
def __init__(
    self,
    entity,
    name,
    clock,
    resetn,
    args,
    kwargs
)
```

Setup defaults and call base class constructor.

_restart_rw

```
def _restart_rw(
    self
)
```

kill and restart _run thread.

_run

```
async def _run(
self
)
```

_run thread that deals with read and write.

_run_read

```
async def _run_read(
self
)
```

Abstract method for read thread

_run_write

```
async def _run_write(
self
)
```

Abstract method for write thread