

# tb\_piso.v

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## AUTHORS

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## DATES

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## INFORMATION

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### Brief

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Test bench for parallel to serial core.

### License MIT

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## tb\_piso

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```
module tb_piso ()
```

Test bench for piso. Pump in data to parallel port on each count == 0.

## INSTANTIATED MODULES

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### piso

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```
piso #(
```

```
BUS_WIDTH(1)
) dut ( .clk(tb_clk), .rstn(tb_rstn), .ena(tb_ena), .load(tb_load), .pdata(
```

Device under test, parallel to serial