

# tb\_sipo.v

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## AUTHORS

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## DATES

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## INFORMATION

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### Brief

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Test bench for Serial in Parallel out core.

### License MIT

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## tb\_sipo

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```
module tb_sipo ()
```

Test bench for serial in parallel out

## INSTANTIATED MODULES

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### sipo

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```
sipo #(
```

```
BUS_WIDTH(1)
) dut ( .clk(tb_clk), .rstn(tb_rstn), .ena(tb_ena), .rev(1'b0), .load(tb_lo
```

Device under test, serial to parallel data conversion.