



# **Intel® Open FPGA Stack (Intel® OFS) GitHub & Documentation Access**

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## 1.0 Request GitHub Source Code Access

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This section details how you can request access to Intel® OFS Source Code and documentation on GitHub.

1. Create a GitHub account by joining GitHub: <https://github.com/join>.
2. Fill out and submit the Intel® OFS Questionnaire available at [intel.com/ofc](https://intel.com/ofc). You should receive an email confirmation once the form is submitted.

**Note:** By submitting your responses to the questionnaire, **you are agreeing to the Intel® Open FPGA Stack license terms**. If you would like to review the license terms prior to submitting the form, contact your Intel Sales Representative or [ofc.marketing@intel.com](mailto:ofc.marketing@intel.com) if you do not have an Intel Sales Representative. Prior review of the license terms is not required to complete the questionnaire.

3. Once your request has been reviewed, you will receive an email from the Intel® OFS Marketing advising status within 2 business days.
4. If approved, you'll receive an email inviting you to join the OTC Share GitHub. Please select "Join@otcshare" on the email invitation **within 7 days of receiving the email**.
5. After accepting the GitHub email invitation, you will have access to the following repositories that apply to your design goals:

Repository	Contents
Documents	Contains Intel® OFS collateral for FPGA and software developers
FIM/RTL Shell	Contains FIM or shell RTL, automated compilation scripts, unit tests and UVM test framework
If Applicable High Level Design (HLD) Shim	Contains the hardware and software components that you need to develop your own oneAPI board support package for the Intel® Stratix® 10 and Intel® Agilex™ FPGAs
If Applicable Board Management Controller (BMC)	Provides the Intel® OFS Board Management Controller RTL, firmware, scripts, and collateral targeting Intel® OFS for Intel® Agilex™ FPGAs or Intel® Stratix® 10 FPGAs



## 2.0 Request Access to RDC Documentation

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This section details how you can request, find, and view Intel® OFS related technical documentation in the Intel Resource & Documentation Center (RDC).

**Note:** The latest Intel® OFS technical documentation can be found in the OTC Share GitHub repository, except for the subsystem user guides (Memory, PCIe, Transceiver) and Intel® OFS Docker image source files and user guide for Intel® Agilex™ FPGA. You **do not** need RDC access unless accessing one of these documents. You may also optionally request RDC access to review the Intel® OFS **license terms** prior to submitting the questionnaire.

1. If you need access to RDC documents, notify your local Intel Sales Representative or [ofs.marketing@intel.com](mailto:ofs.marketing@intel.com) if you do not have an Intel Sales Representative. In email, indicate which documents you would like access to.

**Note:** You must have access to the Intel® OFS GitHub repository before requesting access to RDC documents. If you have not done so already, complete and submit the Intel® OFS questionnaire available at [intel.com/ofs](https://intel.com/ofs) to request access to our repository.

2. Wait for an email confirmation to confirm you have been granted access. Allow up to 1 hour after confirmation for the documents to appear in RDC.

### 2.1 Access Documentation on RDC

**Option 1:** You must be logged into myIntel to view RDC Documentation.

1. After receiving confirmation from [ofs.access@intel.com](mailto:ofs.access@intel.com) or your Intel Sales Representative that you have been granted access, go to [rdc.intel.com](https://rdc.intel.com).
2. Expand the following accordions: "For Hardware" > "FPGAs & Programmable Devices" > "Acceleration Cards".
3. Open the Intel® Open FPGA Stack RDC home page.



Top Resources

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6 Results Search this list

Title	Description	Status
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Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) D5005	Filterable collection of FPGA Resources and Documentation for Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) D5005.	Released
Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) N3000	Filterable collection of FPGA Resources and Documentation for Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) N3000.	Released
Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) with Arria® 10 GX	Filterable collection of FPGA Resources and Documentation for Intel® FPGA Programmable Acceleration Card (Intel® FPGA PAC) with Arria® 10 GX.	Released
Intel® Open FPGA Stack	Intel® Open FPGA Stack (OFS) - Resources and Documentation	Pre-release

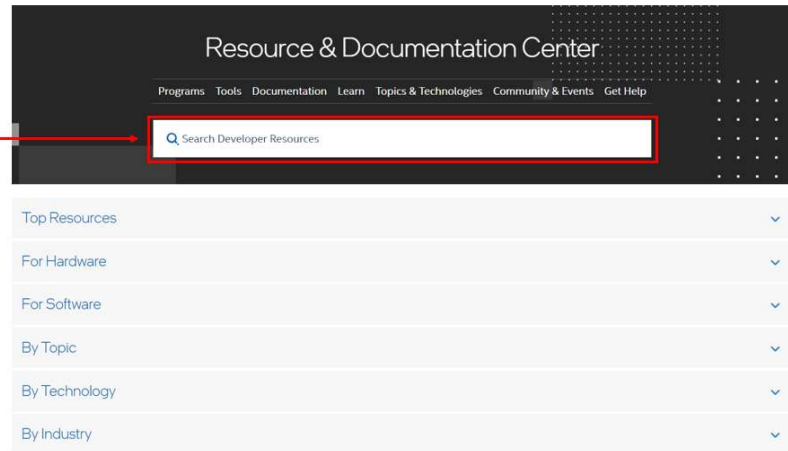
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1. Search directly for documents by typing in the Document ID in the search bar.

Resource Title	Resource ID
Intel® Open FPGA Stack C2A Limited Distribution License	732859
Intel® OFS Docker Image for Intel® Agilex™ FPGA (source files)	741480
User Guide: Intel® OFS Docker Image for Intel® Agilex™ FPGA	742334
PCIe Subsystem User Guide for Intel® Agilex™ OFS	690604
Memory Subsystem User Guide for Intel® Agilex™ OFS	686148
Transceiver Subsystem User Guide for Intel® Agilex™ OFS	686343



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## 2.2 Troubleshooting

Allow 1 hour of processing time after access has been granted.

If you have issues viewing the Intel® OFS documentation on RDC, check that you are logged into your myIntel account. If issues persist, contact your Intel Sales Representative.







## 3.0 Document Revision History

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Date	Version	Changes
5/4/2022	1.0	Initial release
6/16/2022	1.1	Information about Intel® OFS license agreement added
9/20/2022	1.2	RDC Document ID for Docker image added