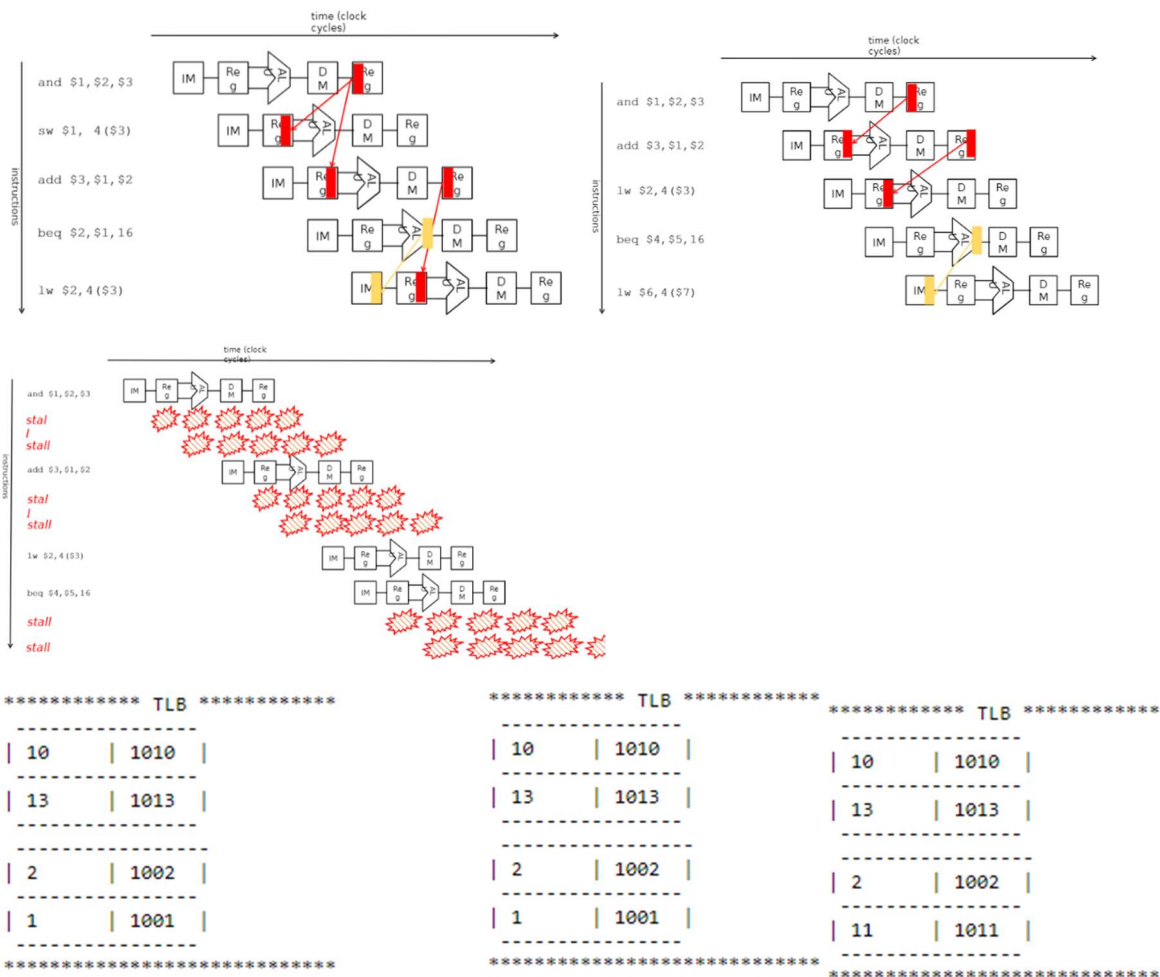Capacitty: If the amount of data accessed between two references to the same memory location M is greater than the cache capacity then the second reference to M is going to be a miss. This type of miss is considered a capacity miss. Most cache misses are capacity misses


```
*********** TLB ************
    ----------------
|  10      |  1010  |
    ----------------
|  13      |  1013  |
    ----------------
    ----------------
|  2       |  1002  |
    ----------------
|  1       |  1001  |
    ----------------
***************************
```

```
*********** TLB ************
    ----------------
|  10      |  1010  |
    ----------------
|  13      |  1013  |
    ----------------
    ----------------
|  2       |  1002  |
    ----------------
|  1       |  1001  |
    ----------------
***************************
```

```
*********** TLB ************
    ----------------
|  10      |  1010  |
    ----------------
|  13      |  1013  |
    ----------------
    ----------------
|  2       |  1002  |
    ----------------
|  11      |  1011  |
    ----------------
***************************
```

--- [TLB] Did not find VPN: 10

--- [pagetable] ending VPN: 10 to TLB

--- [TLB] replacing LRU VPN: 10 with VPN: 10

MEM[10]: TLB Miss; Page Table Hit

--- [TLB] Found VPN: 13 in Way 0, Index 1

MEM[13]: TLB Hit

- [TLB] Did not find VPN: 11

--- [pagetable] Page corresponding to VPN: 11 not in Memory, Fetching from Disk

--- [pagetable] page corresponding to VPN: 11 fetched from Disk

--- [TLB] replacing LRU VPN: 11 with VPN: 11

MEM[11]: TLB Miss; Page Fault

--- [TLB] Did not find VPN: 5

--- [pagetable] Page corresponding to VPN: 5 not in Memory, Fetching from Disk