10 and disk systems

Topics today: 1 Andahl's Law 2. 40 Architectures

(1) Andahl's Law

Suppose we have a system that processes tasks at a fixed rate. (e.g. a dotasase that processes 2,000 represts per second). Suppose we have the option to speed up some component of the system. (e.g. we could rewrite one particular type of over in the datasase system).

Suppose

f - is the fraction of time currently consumed by the component.

K - is the factor by which we can speed up the

Then the speedup factor S from improving the component

$$S = \frac{1}{(1-1) + f/k}$$
 = Amolahl's Law

Example: the queries under consideration examp 30% of processing fine. The rewrite will make them 50% faster. What is resulting speedup?

Solution: do as extrise

See noted example in Section 7.3 of the book for how to do a cost/benefit avalysis of the different proposed improvements.

(2) 1/0 Arhitectures

Detailed knowledge is not required. We just held an awareness of the following basic facts:

- · The nort common from of 1/0 uses interrupts, as described earlier in the severester.
- · An 1/0 device signals an interrupt when ready to send or receive data. CPU surpends regular operation of the fetch-decode-execute cycle and services the interrupt instead.
- · Efficiency is greatly improved using direct-memory access (DMA)

 a special piece of hardware that transfers data

 fetureen devices and main nemony. The CPU must

 whiteate the transfer, but can then do other weful

 tasks while the transfer is being performed.