

BBC MICRO COMPUTER

First Production Selection of LINKS, Model A and B. no disc, no net

| Pin | Link | Link in Cover | Purpose |
|-----|---|---------------|--|
| S1 | Leave empty | South | Have 4 boards on board: SELECTS PRINTER STROBE(3) OR DIRECT I/O P.W.(N) |
| S2 | Fit wire link | OPEN | DISABLES ECONET NMI. NB. DO NOT FIT IC 91 WITH THIS LINK MADE |
| S3 | Leave empty | OPEN | ECONET SELECTS CLOCK BASE FREQUENCY (SELECT 2MHz) |
| S4 | Leave empty | EAST | SELECT SELECT FOR 5K DISC (EAST) OR 8" DISC (WEST) |
| S5 | Leave empty | OPEN | ECONET: INNOVATES (NORTH) OR DIGITALS (SOUTH) ECONET CLOCK |
| S6 | Leave empty | OPEN | ECONET: SELECTS 4L (NORTH) OR 4L (SOUTH) FOR CLOCK RATE |
| S7 | Leave empty | EAST | DISC: PROGRAMS VOLTAGE ON PIN 30 OF IC 70. 75 (WEST) OR 0V (EAST) |
| S8 | Leave empty | MADZ | DISC: LINKS HEAD LOAD SIGNAL TO PIN 16 OF PL 8 |
| S9 | Fit wire link | OPEN | DISABLES DISC NMI. NB. DO NOT FIT IC 78 WITH THIS LINK MADE |
| S10 | Leave empty | WEST | DISC: INDEX FOR 5K DISC (WEST) OR 8" DISC (EAST) |
| S11 | Leave empty | OPEN | ECONET: SETS STATION ID (NORTH IS LSB) |
| S12 | Model A :- Model B :- Leave empty | OPEN | ROM SELECT LINES: TIED LOW WHEN IC 76 NOT FITTED N.B. DO NOT FIT IC 76 WITH THESE LINKS MADE |
| S13 | Model A :- Model B :- Leave empty | OPEN | IF BROKEN WILL ALLOW PAGE FD TO BE READ FROM ROM (I.M.) |
| S14 | Leave empty | MADZ | IF BROKEN WILL ALLOW FAST ACCESS TO PAGE FD. MUST BE MADE IF 51A IS CUT |
| S15 | Leave empty | MADZ | IF BROKEN WILL ALLOW FAST ACCESS TO PAGE FC. MUST BE MADE IF 51A IS CUT |
| S16 | Leave empty | MADZ | IF BROKEN WILL ALLOW PAGE FC TO BE READ FROM ROM (I.M.) |
| S17 | Leave empty | OPEN | SELECTS SLOW (SOUTH) OR FAST (NORTH) ACCESS TO IC 100 |
| S18 | Fit 3 pins | OPEN | SELECTS SLOW (WEST) OR FAST (EAST) ACCESS TO IC 52, 86 + 101. DIODES 212, 211 |
| S19 | Fit 3 pins | OPEN | SELECTS A13 (SOUTH) OR ROMSEL A13 (NORTH) AS ROMSELECT HIGH BIT TO IC 20 DECODEX |
| S20 | Fit 3 pins | OPEN | SELECTS A13 (SOUTH) OR ROMSEL A13 (NORTH) AS ROMSELECT HIGH BIT TO IC 20 DECODEX |
| S21 | Fit two pairs of pins | OPEN | SELECTS MEMORY MAP CONFIGURATION FOR ROMS. A AND B AND C AND D SELECTS IC 51 IN BLOCKS 8 TO B AND IC 52, 86, 100 AND 101 TO BE IN BLOCKS C TO F. PHANTOMING A AND B AND C AND D PUTS IC 51 IN BLOCKS C TO F (THE MORE NORMAL CONFIGURATION FOR MACHINES WITH AN OS ROM) |
| S22 | Fit 3 pins | OPEN | SELECTS A12 (SOUTH) OR ROMSEL A12 (NORTH) AS ROMSELECT LOW BIT TO IC 20 DECODEX |
| S23 | Leave empty | OPEN | POSITIONS FOR OPTIONAL TERMINATION OF RS423 RECEIVERS |
| S24 | Leave empty | OPEN | SEE INTERFACE CHIP DATA. |

continued.....

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|------|-------------|---|-----------------|--------------|
| S25 | Fit 3 pins | Model A | Minishunt SOUTH | MADE IN CORN |
| | | Model B | Minishunt NORTH | OPEN |
| S26 | Fit 3 pins | Minishunt WEST | OPEN | |
| S27 | Leave empty | | WEST | |
| S28 | Leave empty | | WEST | |
| S29 | Leave empty | | EAST | |
| S30a | | | OPEN | |
| S30b | Leave empty | | OPEN | |
| S31 | Fit 3 pins | Minishunt WEST | OPEN | |
| S32 | Fit 3 pins | Minishunt EAST if 32K EPROMS are fitted | OPEN | |
| S33 | | WEST if 128K ROM fitted in IC52 | OPEN | |

MADE IN CORN PURPOSE

SELECTS 16 Kbyte (SOUTH) OR 32 Kbyte (NORTH)

MACHINE CONFIGURATION

SELECTS INVERTED OR NORMAL VIDEO. COMPATIBLE FOR ACORN MONITOR.

DISC. SELECTS CLOCK FOR 5 1/2" (WEST) OR 8" (EAST) DISC.

SELECT BAND CLOCK GENERATOR BASE RATE. LINKS MADE OPPOSITE WAY WILL PRODUCE 1300 BAND CASSETTE BUT WILL ALSO AFFECT OTHER BANDS

FOR FUTURE OPTIONS TO ALLOW TWO OR MORE ROM SELECT SIGNALS TO BE WID. OR ED.

SELECTS POSITIVE (WEST) OR NEGATIVE (EAST) SYNC TO RGB VIDEO.

SELECTS A 13 (WEST) OR +5V (EAST) ON PIN 26 OF IC'S 52 & 58

SELECTS A 7 (WEST) OR -5V (EAST) ON PIN 26 OF IC'S 100 & 101

S32 & S33 ARE TO ALLOW 2732 OR SIMILAR EPROMS TO BE USED IN THEIR RESPECTIVE SOCKETS

KS-J/ML
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ACORN
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BBC Microcomputer System

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