

CHECK WORD GENERATION

In the example shown a 16 bit shift register has as input the modulo-2 sum of an external input and the contents of the 7th, 9th, 12th and 16th stages of the register. Initially the register is cleared to 'all zeros'. During a sequence of 8:92 clock pulses the first 24 character bytes (192 bits) of the page header packet and the following character bytes of packet numbers with Yup to 25, in conventional transmission order form the external input. Any absent packets are considered to contain the character SPACE (2/0) throughout. At the end of this process the contents of the register are the Basic Page Check Word and it is transmitted along the register begining with the bit held in the 16th stage.



					FORMA	1. OF PACK	FORMAL OF PACKETS X/26 X/27 X/28 8/30	98 9/30	
Packet X/26 - X/28	Clock Run-in	Clock Run-in	Framing	Magazine and Packet Address	9	Design- ation Code	First Three or Six Byte Data Group	~~~ ~~~	
Packet X/26	PH PH PH Code	PHPHPHPH Designation Code	2 1 3 1 2 1 3 2 4 P		AAAP AABBBBP		D D D D D D P P Data odd parity	Thirteen Three Byte Groups in Each Packet	A = Address m = Hode P = Hamming
	Packet X/28	L.	ddddddd ₀		saspirp Papabas	F E	Thirteen Three Byte Data Groups in Each Packet	0000	Bit always set to 0 Primary Set Character Protection Bits Supplementary Set Character
Packet X/27	Six byt Page Nu For bit	Six bytes containing Relative Page Number and Page Sub-Code. For bit sequence see NOTS	ning Rela	code.	Code.	, 190	Six Groups of in Each Packet	Six Bytes Each	When Designation Code is 0000 final two bytes are a Basic Page Check Digit
Packet 8/30		Framing Code	Magazine Packet A	and ddrees	Design- ation Code	Programme or Network Label		One 6 byte group Coded as packet // Croup Bytes 15 to 45	is to 45

Figure 2(a)

NOIS Page Number and Page Sub-Code have the same format as bytes 6 to 11 of the Page Header (see figure 1), packet X/O. The bits C4, C5 and C6 in this sequence are used to change the magazine number from that in bytes 4 and 5 of the packet X/27. Setting any of these bits to 1 complements the corresponding angazine number bit.

In all cases the LEAST SIGNIFICANT bit is transmitted first.

FLINK 1 LINK 2	LINK3	LINKS TORE CYCLE	Sewhere Cycle
AA AA E4 60 AS		CYC CYC	check
		1080	Sentre 192 bits
negative 1 E3. Hours and Format of each six-byte link ad	link address contains page number and	24	of row of and
Subcode (Formerly time code)	of a linked base coded as in Figure 6	5 A A 4 5 6 7 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9	25 x 320 bits
	Cir bates of one hander follower Manager		of rows 1-25
	of the state of th	n i	
. 9	7 2846 of the los to wife ?	10002174	
1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2			