IFL

:SS1\ send block to ntm. Registers zapped.

```
LDA WW1        Buffer ptr.
CMP @#D0       Buffer start #D0.
BNE SS2        Buffer has some data in
RTS
:SS2 STA #2879+6
     LDA WW1+1  STA #2879+7
     LDX @#79     End of buffer ptr in TxCB.
     LDY @#28
     LDA @128
     STA ZZ21     temp store for retry count
:SS3 JSR #023D    Transmit
     BIT #2879    test control code
     BVC SS4      O.K.
     DEC ZZ21     retry count
     BNE SS3      no good If too many times drop through & pretend O.K.
\ignore,
SS4  LDA@#80
     STA#2879     repair TxCB
     LDA@#D0  STA WW1
     RTS
```

:SS5\ put char in A in Tx buffer & send it if buffer full. regs saved.

```
     PHA        save char
     STA ZZ21   temp
     TYA  PHA
     LDA ZZ21
     LDY@0
     STA (WW1),Y    put in buffer.
     INC WW1
     BEQ SS6        do carry
     LDA WW1
     CMP @#10
     BEQ SS7        buffer full.
:SS8 PLA
     TAY
     PLA
     RTS
```

```
:SS6  INC WW1+1
      BEQ SS8 unconditional

:SS7  TXA
      AHA
      JSR SS1   read buffer.
      PLA
      TAX
      JMP SS8   return.
```

IF7

13 14 15 16

32 33 34 35 36 37 38 39 40 41

```
:QQ13  BIT #2870     check R+CB
       BMI QQ14      not empty
       RTS           empty

:QQ14  LDY @0
       LDA (WW2),Y   character
       PHA           save it
       INC WW21
       LDA WW21
       CMP #2876     R+CB end of data pointer
       BNE QQ15
       LDA @H>IRQ    Repair R+CB
       STA #2870
       LDA @#D0      end buffer ptr
       STA #2877
```

```
:QQ15  PLA
```

check special chars
etc