

FLOPPY.MAC

VER DEFL 4 ;Version 4 module
DSEG

SUBTTL Skew tables
PAGE

; ****
; * SKEW TABLES *
; ****

; Make skew table using floppy number to establish format
FN DEFL 1 ;Initialise floppy number
DN DEFL NDRVW

REPT NDRV
MLAB1 FM.,FORM,%FN
MLAB2 SKEW.,SKW,%FM.,SECS.,MXS,%FM.
MLAB1 CGS.,CGS,%FM.

IF FM. EQ NULLF
ASGSKW %DN ;Assignable skew

ELSE

IF SKEW. EQ SPSK
SPCSKW %FM. ;;Special skew

ELSE

IF CGS. EQ SECCYL
SECS. DEFL SECS./2 ;;Skew on one side only

ENDIF

IF SKEW. GT 1
MULTI S,%SECS.,%SKEW. ;;Test for multi expansion

IF FIRST
SKEW %SECS.,%SKEW.

ENDIF

ENDIF
ENDIF
ENDIF

FN DEFL FN+1
DN DEFL DN+1
ENDM

SUBTTL Floppy Data Base
PAGE

; ****
; * FLOPPY DATA BASE *
; ****

; * Drive parameters *

LDRIV: DEFB 0FFH ;^ Requested logical drive
FMTLOG: DEFB 0FFH ;^ Logical drive log
PDRIV: DEFB 0FFH ;^ Requested physical drive
DRVLOG: DEFB 0FFH ;^ Physical drive log

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; * Drive parameters *

LDRIV: DEFB 0FFF          ;^ Requested logical drive
FMTLOG: DEFB 0FFF          ;^ Logical drive log
PDRIV:  DEFB 0FFF          ;^ Requested physical drive
DRVLOG: DEFB 0FFF          ;^ Physical drive log

TRKA:   REPT NDRVF        ;^ Floppy track record
        DEFB 0FFF
        ENDM

DBASE:                                ;Data Base start

; * Format parameters *

; YES/NO options
; Bit 0 set = double step
; Bit 1 set = deleted data mark
; Bit 2 set = sectors start at 1
; Bit 3 set = no side 2 flag
; Bit 4 set = tracks are reversed on side 2
; Bit 5 set = tracks are cylindrical
; Bit 6 set = physical sector numbers on side 2 carry on from side 1
; Bit 7 set = assignable drive
FN      DEFL 1
YNOPT:  REPT NDRVF
OPT_    DEFL 0

; ;Get Floppy and format type
MLAB2 FG.,FASG,%FN,FM.,FORM,%FN
MLAB1 FT.,FPPY,%FG.

; ;Test for assignable drive
IF FM. EQ NULLF
OPT_ DEFL OPT_ OR 80H
ENDIF

; ;Get Drive and Format track density
MLAB2 LBL1.,DTY,%FT.,LBL2.,FTY,%FM.
IF (LBL1. EQ TP96) AND (LBL2. EQ TP48)
; ;Double step flag when using 48tpi format on 96tpi drives
OPT_ DEFL OPT_ OR 01
ENDIF

; ;Get data mark info
MLAB1 LBL1.,DDM,%FM.
IF LBL1. NE 0
; ;Data mark is deleted
OPT_ DEFL OPT_ OR 02
ENDIF

; ;Get sector offset
MLAB1 LBL1.,SOF,%FM.
IF LBL1.

; ;Sectors start at 1
OPT_ DEFL OPT_ OR 04
ENDIF

; ;Get side 2 flag info
MLAB1 LBL1.,BSF,%FM.
IF LBL1.

; ;ID header not set for side 2
OPT_ DEFL OPT_ OR 08
ENDIF

; ;Get side handling
MLAB1 LBL1.,CGS,%FM.
IF LBL1. EQ TRKREV
; ;Tracks are reversed on side 2
OPT_ DEFL OPT_ OR 10H
ENDIF
IF LBL1. EQ TRKCYL
; ;Tracks are cylindrical
OPT_ DEFL OPT_ OR 20H
ENDIF

; ;Get sectors on 2
MLAB1 LBL1.,SCY,%FM.
IF LBL1. NE 0
; ;Physical sectors on side 2 carry on from side 1
OPT_ DEFL OPT_ OR 40H
ENDIF

DEFLR OPT

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MLAB1 LBL1.,LBS,%FM.
IF LBL1. EQ TRKREV
;;Tracks are reversed on side 2
OPT_ DEFL OPT_ OR 10H
ENDIF
IF LBL1. EQ TRKCYL
;;Tracks are cylindrical
OPT_ DEFL OPT_ OR 20H
ENDIF

;;Get sectors on 2
MLAB1 LBL1.,SCY,%FM.
IF LBL1. NE 0
;;Physical sectors on side 2 carry on from side 1
OPT_ DEFL OPT_ OR 40H
ENDIF

DEFB OPT_
FN DEFL FN+1
ENDM

;Physical sectors per logical track
FN DEFL 1           ;Initialise floppy number
MAXSEC: REPT NDRV
MLAB1 FM.,FORM,%FN   ;Get Format type
MLAB1 LBL1.,MXS,%FM. ;Get sectors per track
DEFB LBL1.
FN DEFL FN+1
ENDM

;Sector to change sides
FN DEFL 1           ;Initialise floppy number
SIDSEC: REPT NDRV
;;Get Format type
MLAB1 FM.,FORM,%FN
;;Get sectors per track, and side handling
MLAB2 LBL1.,MXS,%FM.,LBL2.,CGS,%FM.

IF LBL2. EQ SECCYL
DEFB LBL1./2         ;Split track in 2 by sectors
ELSE
DEFB LBL1.           ;Don't change by sector
ENDIF

FN DEFL FN+1
ENDM

;Track to change sides
FN DEFL 1           ;Initialise floppy number
SIDTRK: REPT NDRV
;;Get Format type
MLAB1 FM.,FORM,%FN
;;Get tracks per format, side handling, and change side track
MLAB2 LBL1.,MXT,%FM.,LBL2.,CGS,%FM.
MLAB1 LBL3.,CGT,%FM.

IF (LBL2. EQ TRKCRY) OR (LBL2. EQ TRKREV)
IF LBL3.
DEFB LBL3. AND 7FH    ;Use parsed track for change
ELSE
DEFB LBL1./2         ;Split total tracks in 2
ENDIF
ELSE
DEFB LBL1.           ;Don't switch by track
ENDIF

FN DEFL FN+1
ENDM

IXPTR:             ;Driver data base pointer

;Format assignment
FN DEFL 1           ;Initialise floppy number
FORMS: REPT NDRV

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;fixed format type
    MLAB1 FM.,FORM,%FN
    DEFB FM.
FN    DEFL FN+1
ENDM

;Sector size 1=128, 2=256, 4=512, 8=1024
;Bit 7 set indicates data is inverse
FN    DEFL 1           ;Initialise floppy number
SCSZ: REPT NDRVF
;;Get Physical sector size
    MLAB1 FM.,FORM,%FN
    MLAB2 SEC.,SEC,%FM.,INV.,INV,%FM.
SZE_  DEFL HIGH (SEC.*2)
    IF INV. NE 0
SZE_  DEFL SZE_ OR 80H
ENDIF
    DEFB SZE_
FN    DEFL FN+1
ENDM

;Track skew
;FN    DEFL 1           ;Initialise floppy number
;TKSKEW: REPT NDRVF
;;Get Track skew
;    MLAB1 FM.,FORM,%FN
;    MLAB1 TSK.,TSK,%FM.
;    DEFB TSK.
;FN    DEFL FN+1
;ENDM

;First spare
SPARE1: REPT NDRVF
    DEFB 0
ENDM

;Second spare
SPARE2: REPT NDRVF
    DEFB 0
ENDM

;      * Drive parameters *

IYPTR:
;Drive type, not used internally
FN    DEFL 1           ;Initialise floppy number
FLPTYP: REPT 4
    MLAB1 FP.,FPPY,%FN   ;;Get Floppy type
    IF FP. EQ 0          ;;No drive
    DEFB 0FFH
    ELSE
    MLAB1 LBL1.,DTY,%FP.  ;;Get step rate
    DEFB LBL1.
ENDIF
FN    DEFL FN+1
ENDM

;Step speed in millisecs
FN    DEFL 1           ;Initialise floppy number
STPSPD: REPT 4
    MLAB1 FP.,FPPY,%FN   ;;Get Floppy type
    MLAB1 LBL1.,STP,%FP.  ;;Get step rate
    DEFB LBL1.
FN    DEFL FN+1
ENDM

;Track settling time
FN    DEFL 1           ;Initialise floppy number
SETTL: REPT 4
    MLAB1 FP.,FPPY,%FN   ;;Get Floppy type
    MLAB1 LBL1.,STL,%FP.  ;;Get settling time
    DEFB LBL1.
FN    DEFL FN+1
ENDM

;Headload delay in ms.
FN    DEFL 1           ;Initialise floppy number

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        ENDM

;Track settling time
FN      DEFL 1                      ;Initialise floppy number
SETTL:  REPT 4
        MLABI FP.,FPPY,%FN          ;Get Floppy type
        MLABI LBL1.,STL,%FP.        ;Get settling time
        DEFB LBL1.

FN      DEFL FN+1
ENDM

;Headload delay in ms.
FN      DEFL 1                      ;Initialise floppy number
HDDEL:  REPT 4
        MLABI FP.,FPPY,%FN          ;Get Floppy type
        MLABI LBL1.,HDL,%FP.        ;Get head load delay
        DEFB LBL1.

FN      DEFL FN+1
ENDM

LINK:   DEFB MPILNK                 ;Say if MPILNK is made

SUBTTL WD2797 Diskette Routines
PAGE

;Read/Write to sector in LSECT
;At track in LTRAK
;On drive in LDRIV,PDRIV
;To/From address in DMAADR
RWFLPY: PUSH IX                    ;Save users IX
        PUSH IY                    ;Save users IY
        CALL SETFLP                ;Set up for floppy read/write
        JR NZ,FLPERR              ;Error during set up, take it back
        CALL SETCNT                ;Set multi IO counters
        LD B,A                    ;Sector count to B
        JR GETSEC

GETALL: LD  (DMAADR),HL            ;Increment DMA address
        LD HL,LSECT
        INC (HL)
        LD A,(IX+MAXSEC-IXPTR)    ;Bump logical sector (assume 8 bit)
        TSTSEC: CP (HL)           ;Check for sector overflow
        JR NZ,GETSEC              ;Test for sector overflow
        LD (HL),0                  ;No overflow
        DEC HL
        DEC HL
        INC (HL)                  ;Reset sector
                                ;Back to logical track
                                ;(Assume 8 bit)

GETSEC: PUSH BC                    ;Save sector count
        CALL FRDWR                ;Transfer a sector
        POP BC
        JR NZ,FLPERR              ;Error
        DJNZ GETALL               ;Transfer all sectors

XYOUT:  POP IY
        POP IX
        LD HL,0
        LD (SKTAB),HL             ;Cancel skew table
        RET

;Floppy error, reset counters
FLPERR: LD HL,0
        LD (MULCNT),HL            ;Clear count down
        JR XYOUT                 ;Recover IX

;Set counters for multi IO
;Returns sector count in A
SETCNT: LD HL,MULCNT              ;Sectors to read
        LD A,(HL)

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LD (HL),0           ;Clear multi-count
OR A
JR NZ,MULTI
INC A              ;Multi count uninitialized so make it 1
MULTI: INC HL      ;Point to countdown
LD (HL),A          ;Set up countdown
DEC (HL)           ;Deduct first sector
RET

;Error during set-up, display and prompt retry
SETERR: CALL SHWERR           ;Show the error
        RET NZ                ;Don't retry

;Set up for floppy read/write
;Log in drive and start motor
;Initialise IX index to data base
;Load the heads
;Head load delay if new drive
;Set current track
;Initialise Sec size and command
;Called only at beginning of a multi read/write
SETFLP: LD HL,LDRIV           ;Logical drive
        LD A,(HL)             ;Get logical drive
        LD B,A                ;Keep it
        INC HL                ;FMTLOG
        CP (HL)               ;Compare against last call
        LD (HL),A              ;and update
        PUSH AF               ;Save flag
        INC HL                ;PDRV
        LD A,(HL)             ;Physical drive code
        OUT (DRVPRTR),A       ;Kick motor
        INC HL                ;DRVLOG
        RES 7,(HL)            ;Reset side flag
        CP (HL)               ;New drive ?
        LD (HL),A              ;Save physical code
        PUSH AF               ;Save flag
        LD IY,IYPTR-1         ;Point IY at drive data

GOIY:  INC IY
        RRCA
        JR NC,GOIY
        LD A,B                ;Recover logical drive
        SUB NDRVW              ;Take off Winis
        LD B,A
        INC B
        LD IX,IXPTR-1         ;Point to track map
GOTRAK: INC IX
        DJNZ GOTRAK
        LD A,(IX+FORMS-IXPTR)
        CP NULLF
        JR Z,QUITSU
        CALL LOADH
        POP AF
        JR Z,FNDTRK
        LD A,(IY+HDDEL-IYPTR)
        CALL DELAY

;Find out where head is on first using a drive
FNDTRK: POP AF
        LD A,(IX+TRKA-IXPTR)
        JR NZ,NEWFMT
        CP OFFH
        JR NZ,ONTRAK
NEWFMT: CALL WAITON
        JR C,SETERR
        LD A,READID
        OUT (FDCCOM),A
RDHEAD: IN A,(STAPRT)
        AND 00000001B
        JR Z,RDHEAD
        IN A,(FDCDAT)
        IN A,(FDCSTA)
        AND 11111011B
        JR Z,HDROK
        AND 10000000B
        JR NZ,SETERR
        ;Read address header
        ;Wait for IRQ or READY off
        ;Clear DRQ and BUSY
        ;We know we've lost data
        ;Ready?
        ;No

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JR C,SETERR
LD A,READID
OUT (FDCCOM),A
RDHEAD: IN A,(STAPRT)
AND 00000001B
JR Z,RDHEAD
IN A,(FDCDAT)
IN A,(FDCSTA)
AND 11111011B
JR Z,HDROK
AND 10000000B
JR NZ,SETERR
CALL TRACK0
XOR A
JR ONTRAK
HDROK: IN A,(FDCSEC)
LD (IX+TRKA-IXPTR),A
ONTRAK: OUT (FDCTRK),A
LD A,(RWFLAG)
OR A
LD A,RDSEC
JR NZ,GOTCOM
LD A,WRSEC
BIT 1,(IX+YNOPT-IXPTR)
JR Z,GOTCOM
SET 0,A
GOTCOM: LD (COMM),A
LD A,(IX+SCSZ-IXPTR)
LD (SECSZE),A
XOR A
RET

;Can't run unassinged NULLF
QUITSU: POP HL
POP HL
XOR A
DEC A
RET

;Convert logical track to physical
CONVT: LD A,(LTRAK)           ;Logical track
       LD (PTRAK),A          ;Default is logical
       BIT 5,(IX+YNOPT-IXPTR) ;Test for track cylindrical
       JR NZ,TCYL
       CP (IX+SIDTRK-IXPTR) ;Test for side change
       RET C                 ;Side 0 or not TRKCRY or TRKREV
;Track carry
       SUB (IX+SIDTRK-IXPTR) ;Take off side 0
       BIT 4,(IX+YNOPT-IXPTR) ;Test for TRKREV
       JR Z,T2
;Track reversed
       CPL
       ADD A,(IX+SIDTRK-IXPTR)
T2:   CALL SET2
T1:   LD (PTRAK),A
RET
;Track cylindrical
TCYL: SRL A                  ;Logical to physical, side in C
      JR C,T2               ;Side 1
      JR T1                 ;...else side 0

;Convert logical sector to physical
CONVS: LD A,(LSECT)
       CP (IX+SIDSEC-IXPTR) ;Test for change with sector
       JR C,CS1              ;Side 0 or not SECCYL
       SUB (IX+SIDSEC-IXPTR) ;Take off side 0
       CALL SET2              ;And set up for side 1
CS1:  LD E,A
       LD HL,(SKTAB)
       LD A,H
       OR L
       ;Test for skew table

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    LD H,E
    JR Z,CS2 ;No logical skew
;HL points at skew table

; LD A,(IX+TKSKEW-IXPTR)
; OR A ;Test for track skew
; JR Z,CS0 ;No track skew
;Track skew, convert sector to track skewed, before applying sector skew
; LD A,(PTRAK)
; OR A
; JR Z,CS0 ;Track 0
; LD B,A ;Track count
;NXTT: LD A,E
; ADD A,(IX+TKSKEW-IXPTR)
; LD E,A
; SUB (IX+SIDSEC-IXPTR) ;Test for overflow
; JR C,NOVF ;No overflow
; LD E,A ;Update E
;NOVF: DJNZ NXTT

CS0: LD D,0
      ADD HL,DE ;Add in to table
      LD A,(HL) ;Skewed sector
CS2: BIT 2,(IX+YNOPT-IXPTR) ;Test for sector offset
      JR Z,CS3
      INC A
CS3: LD (PSECT),A
      BIT 6,(IX+YNOPT-IXPTR) ;Test for sector carry on side 1
      RET Z

;Physical sector numbers on side 1 carry on from side 0
LD HL,DRVLOG
BIT 7,(HL) ;Test hard side flag
RET Z ;Side 0 anyway
ADD A,(IX+SIDSEC-IXPTR) ;Add in side 0
LD (PSECT),A
RET

;Set side 1 flags
SET2: LD HL,DRVLOG
      SET 7,(HL) ;Hard side select
      IF MPILNK ;If MPI is linked for hard side select
      BIT 3,(IX+YNOPT-IXPTR) ;..don't set soft side select
      RET NZ ;..if bad side 2 flag
      ENDIF
      LD HL,COMM
      SET 1,(HL)
      RET

;Floppy read/write
FRDWR: LD HL,DRVLOG
        RES 7,(HL) ;Reset hard side flag
        LD HL,COMM
        RES 1,(HL) ;Reset soft side flag
;First convert Logical to Physical
        CALL CONVT ;Convert logical track to physical
        CALL CONVS ;Convert logical sector to physical
;Read/write physical with retries
        LD B,8 ;Up to 8 retries
NEXTTRY: LD A,(DRVLOG) ;Physical drive code
        OUT (DRVPRT),A ;Keep them motors running, select side
        PUSH BC ;Save retry counter
        LD A,(PTRAK)
        CALL SEEK ;Get to required track
;Head is at the right place
        LD A,(PSECT)
        OUT (FDCSEC),A ;Select sector
        CALL WAITON ;Wait until drives ready
        JR C,DERR1 ;Drives have timed out
        LD C,STAPRT
        LD HL,(DMAADDR) ;Sector load address
        CALL RWFLOP ;All ready so do read/write in common memory
        POP BC ;Retry counter
        IN A,(FDCSTA)
        AND 11011111B ;Record type flag is not an error during read
        RETZ ;All OK

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;Head is at the right place
LD A,(PSECT)
OUT (FDCSEC),A
CALL WAIT0N
JR C,DERR1
LD C,STAPRT
LD HL,(DMAADDR)
CALL RWFLOP
POP BC
IN A,(FDCSTA)
AND 11011111B
RET Z
JP M,DERR
PUSH AF
LD A,B
CP 4
CALL Z,TRACK0
POP AF
DJNZ NEXTRY
JR DERR
                ;Tries exhausted

;Handle floppy error
DERR1: POP BC
DERR:  CALL SETERR
RET NZ
JR FRDWR
                ;Clear retry count
                ;Display error to user and re-set up
                ;Return error to BDOS
                ;Full retry

;Display error and prompt for retry
SHWERR: LD HL,DNR
         RLCA
         JR C,FFOUND
         LD HL,DWP
         RLCA
         JR C,FFOUND
         LD HL,WF
         RLCA
         JR C,FFOUND
         LD HL,RNF
         RLCA
         JR C,FFOUND
         LD HL,CRC
         RLCA
         JR C,FFOUND
         RLCA
         JR NC,NDEFF
         XOR A
         RET
NDEFF: LD HL,NDWF
FFOUND: CALL GOTMSG
        CALL CLEARF
;Prompt for retry
        LD HL,RETRY
        CALL PMSG
        ;Prompt reply
        CALL ?CONIN
        LD C,A
        AND SFH
        CP "Y"
        JR Z,RIGHT
        CP "N"
        JR NZ,WRONG
        DEC A
RIGHT: PUSH AF
        CALL ?CONOT
        LD HL,CRLF
        CALL PMSG
        POP AF
        LD A,1
        RET
                ;Auto retry lost data
                ;Shouldn't happen
                ;Print error and track/sector message
                ;Clear the 2797, drive and track logs

;Ensure u/c
;Save flag Z=="Y" NZ=="N"
;CRLF
;Z means retry
;In case error to be returned to BDOS

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;PRINT error, drive, track and sector
GOTMSG: PUSH HL           ;Save error type
        LD HL,ERMSG
        CALL PMSG          ;Error header
        POP HL
        CALL PMSG          ;Print message
TSMMSG: LD HL,ONMSG
        CALL PMSG
        LD A,(LDRIV)       ;Get drive
        ADD A,"A"
        LD C,A
DRVINC: CALL ?CONOT      ;Drive code
        LD HL,TRKMSG
        CALL PMSG          ;Track header
        LD HL,(LTRAK)
        CALL PDEC          ;Track number
        LD HL,SECMSG
        CALL PMSG          ;Sector header
        LD HL,(LSECT)
        CALL PDEC          ;Sector number
        RET

;Return NC if ready
WAITON: LD DE,2500         ;Motor start up allowance (2.5secs)
WAIT1: IN A,(FDCSTA)
        RLCA              ;Look at ready bit
        RET NC             ;Ready
        LD A,1              ;1 ms delay
        CALL DELAY
        DEC DE              ;Decrement time out
        LD A,D
        OR E
        JR NZ,WAIT1        ;Keep waiting
        LD A,80H             ;Not ready error
        SCF                ;Signal error
        RET

;Restore the head and initialise track map
TRACK0: XOR A
;Seek track in A
SEEK:  PUSH BC
        LD (IX+TRKA-IXPTR),A ;Update track map
        OR A                ;Test for track 0
        LD A,(DRVLOG)
        PUSH AF             ;Save drive for exit
        SET S,A              ;2 Mhz for step
        OUT (DRVPRT),A
;Ensure we get to track 0 from possible unknown or error
        LD BC,STPOUT         ;B = 256, C = step out command
        JR Z,STEP0            ;Track 0
;Get to required track using STEP IN or STEP OUT commands from known start
        IN A,(FDCTRK)        ;Current track
        SUB (IX+TRKA-IXPTR)   ;Subtract required
        JR Z,THERE
        LD C,STPOUT
        JP P,GOSTEP          ;Step out if required track is less
        LD C,STEPIN
        NEG                 ;Twos complement
GOSTEP: LD B,A              ;Number of steps
        BIT 0,(IX+YNOPT-IXPTR)
        JR Z,DOSTEP
        SLA B                ;Double steps
        JR DOSTEP

;Get a type I status
STEP0: CALL LOADH
STEP1: IN A,(FDCSTA)
        AND 000000100B        ;Test for track 0
        JR NZ,STPDON
DOSTEP: LD A,C              ;Get command
        OUT (FDCCOM),A        ;Issue it

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SLA B ;Double steps
JR DOSTEP

;Get a type I status
STEP0: CALL LOADH
STEP1: IN A, (FDCSTA)
AND 00000100B ;Test for track 0
JR NZ,STPDON
DOSTEP: LD A,C ;Get command
OUT (FDCCOM),A ;Issue it
LD A,(IY+STPSPD-IYPTR) ;Step delay
CALL DELAY
CALL WAITNB ;Make sure 2797 has finished
DJNZ STEP1

STPDON: LD A,(IX+TRKA-IXPTR)
OUT (FDCTRK),A ;Update track register
LD A,(IY+SETTL-IYPTR)
CALL DELAY ;Settling time
THERE: POP AF
OUT (DRVPRTR),A ;Back to original clock
POP BC
RET

;Load heads
LOADH: OUT (FDCTRK),A ;Track and
OUT (FDCCDAT),A ;...Data regs to any value
LD A,SEKTRK ;Seek to current track
JR SENCOM ;Do command

;Clear the track map and 2797
CLEARF: LD HL,FMTLOG
LD (HL),0FFH
INC HL
INC HL ;On to DRVLOG
LD B,NDRVRF+1
INITRK: LD (HL),0FFH ;Initialise track map and drive log
INC HL
DJNZ INITRK
;Clear the 2797
CLEAR: LD A,CLRFDC ;Load the command

;Send command to 2797 and wait til it's done
SENCOM: OUT (FDCCOM),A ;Send command
LD A,10
WAIT: DEC A
JR NZ,WAIT
WAITNB: IN A,(FDCSTA)
RRCA ;Wait til finished
JR C,WAITNB
RET

;Print AF
B2HEX: PUSH AF
RRCA ;Find error type
RRCA
RRCA
RRCA
CALL B1HEX
POP AF
B1HEX: AND 0FH
ADD A,90H
DAA
ADC A,40H
DAA
LD C,A
JP ?CONOT

;Print binary number 0-65535 from <HL>
PDEC: XOR A
LD (LD60),A

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LD BC,10010
LD DE,-10000
NEXT: LD A,"0"-1
PDECL: PUSH HL
INC A
ADD HL,DE
JR NC,STOPL
INC SP
INC SP
JR PDECL
STOPL: PUSH DE
PUSH BC
LD C,A
CP "0"
JR NZ,NOLDG
LD A,(LDG0)
OR A
JR Z,ISLDG
NOLDG: LD (LDG0),A
CALL ?CONOT
ISLDG: POP BC
POP DE
NEXDIG: POP HL
LD A,(BC)
LD E,A
INC BC
LD A,(BC)
LD D,A
INC BC
LD A,E
OR D
JR NZ,NEXT
LD A,(LDG0)
OR A
RET NZ
LD C,"0"
JP ?CONOT

TBL10: DEFW -1000,-100,-10,-1,0
LDG0: DEFB 0

SUBTTL Common Floppy read/write
PAGE
CSEG ;Actual read/write must be common

COMM: DEFB 0 ;FDC command for read/write with flags

;Read/write floppy
RWFLOP: LD A,(DMABNK) ;Select DMA bank
CALL BANK
LD A,(RWFLAG)
OR A ;Read or write ?
JR Z,WRTOP
LD A,(COMM)
OUT (FDCCOM),A ;Get command
JR RDTEST ;Send command

DATIN: IN A,(FDCCDAT) ;Read byte
LD (HL),A ;Store buffer byte
INC HL

RDTEST: IN B,(C) ;Get flags
JR Z,RDTEST ;Nothings happening
JP M,DATIN ;DRQ
JR RWDONE ;Finished, either IRQ or motor off

WRTOP: CALL INVERT ;Invert the data if necessary
LD A,(COMM)
OUT (FDCCOM),A ;Get command
DATOUT: LD A,(HL) ;Send command
INC HL ;Get buffer byte

WRTEST: IN B,(C) ;Get flags
JR Z,WRTEST ;Nothings happening
OUT (FDCCDAT),A ;Send byte
JP M,DATOUT ;DRQ
DEC HL ;Back to end of buffer
LD A,2
CALL DELAY ;Delay 2 ms after write

;Reselect page 0

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JP M,DATIN
JR RWDONE

WRTOP: CALL INVERT
LD A,(COMM)
OUT (FDCCOM),A
DATOUT: LD A,(HL)
INC HL
WRTEST: IN B,(C)
JR Z,WRTEST
OUT (FDCCDAT),A
JP M,DATOUT
DEC HL
LD A,2
CALL DELAY
;Reselect page 0
RWDONE: CALL INVERT
XOR A
CALL BANK
RET

SECSIZE: DEFB 4

;Invert data if read
INVERT: LD A,(SECSIZE)
OR A
RET P
PUSH HL
PUSH BC
AND 7FH
LD B,A
LD C,0
SRL B
RR C
LD HL,(DMAADDR) ; DMA address
CPL
LD (HL),A
CPI
JP PE,CPLLOOP
POP BC
POP HL
RET

;Delay for (A) ms
DELAY: OR A
RET Z ;Return on 0 delay
PUSH BC
LD B,A ;Ms count to B
DLY1: LD A,MSCNT
WTLP: DEC A
JR NZ,WTLP ;Main count
DJNZ DLY1 ;Ms count
POP BC
RET

;Disk communication data items
LTRAK: DEFS 2 ;^ Logical track number
LSECT: DEFS 2 ;^ Logical sector number
PTRAK: DEFS 1 ;Physical track
PSECT: DEFS 1 ;Physical sector

DMAADR: DEFS 2 ;Current DMA address
MULCNT: DEFW 0 ;Count for multisector transfer and countdown
DMABNK: DEFB 0 ;Bank for DMA operations
RWFLAG: DEFB 0 ;0 = Write, 1 =Read
SKTAB: DEFW 0 ;Skew table address

```

DSEG

;Error message components

ERMSG: DEFB 7

CRLF: DEFB CR,LF,0

ONMSG: DEFB " on ",0

TRKMSG: DEFB " : Track-",0

SECMMSG: DEFB " Sector-",0

DNR: DEFB "Drive Not Ready",0

DWP: DEFB "Write Protect",0

WF: DEFB "Write Fault",0

RNF: DEFB "Record Not Found",0

CRC: DEFB "CRC Fault",0

NDWF: DEFB "????",0

RETRY: DEFB " - Retry (Y/N)? ",0

CLEAN: CALL CLEARF ;Clean up 2797 and track map

IF DRVW

CALL CLEANW ;Set up SHD controller

ENDIF

RET