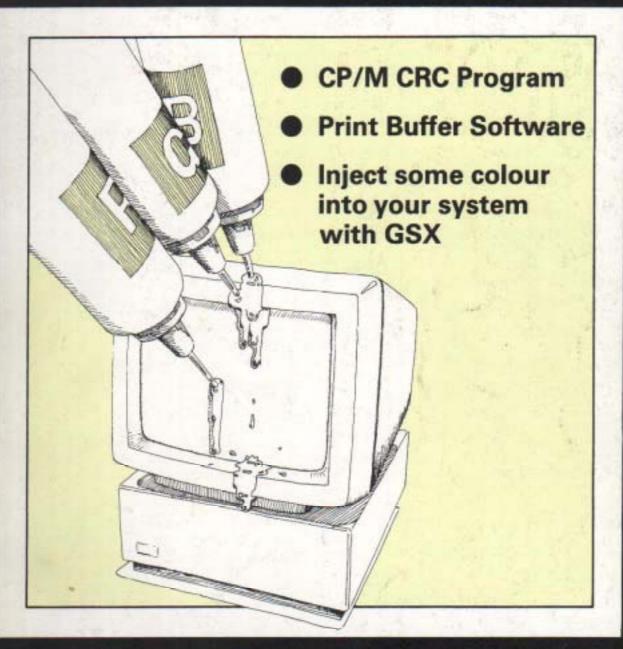
80-BUS NEWS

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The Magazine for GEMINI & NASCOM USERS

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EDITORIAL

Questionnaire

I was wrong (just). In my last Editorial I said that I did not expect us to receive more than 20 per cent of the Questionnaires back. Well, they are still coming in (very very slowly) and there is a miniscule danger that we may reach 30 per cent. As we have never sent out anything like this before, I am uncertain as to how good a response this is, but it has certainly been giving Steve, who was 'volunteered' to type the results into a database, plenty to be getting on with.

What has been fascinating is seeing the number of different ways people have been finding of filling them in. I thought, obviously somewhat foolishly, that there could only be one interpretation of how we expected the answers to be entered. Oh no, everybody has got their own idea, and some questionnaires have come back absolutely covered from wall to wall in writing. Steve has therefore had to spend a considerable amount of time working out how to extract useful information. We will be publishing the results soon, and I hope that all Steve's efforts have been worthwhile. A special thanks to all those who have responded. And if you are one of the 70 per cent (the silent majority) who has not returned the Questionnaire, then there may still be time, although you have not been included in the prize draw. On that subject, we will be publishing the names of the winners in the next issue.

Names and Addresses

Over the years various people have asked us to give them the names and addresses of other 80-BUS News subscribers in their areas. This seems like a very good way of getting people in touch with each other, as, for example, does either of two of the subscribers in Harrow, who live at numbers 70 and 77 of a particular road, know that there is another 80-BUS user so near? We have not previously released this sort of information as we respect that people may wish to maintain privacy. However what we have decided to do, with your permission, is publish your names and addresses in the magazine. When you receive your subscription reminder you will find that there is a `Yes' box and a 'No' box alongside a question as to whether or not you would like your name and address being published. When we have received a reasonable number of these back then we will publish those that have agreed. This system was only started recently, and at the time of typing this we have had 54 of these resubscriptions back. The response looks very favourable, with 42 people ticking `Yes' and only 9 ticking `No'. I really don't know what to do with the 3 that didn't tick either!

Advertising

In one communication received recently there was a comment along the lines of "If Gemini think that 80-BUS is so wonderful, why don't they support the magazine that is dedicated to 80-BUS by advertising in 80-BUS News?". This made me realise that you non-subscribers don't realise that you are missing out!! Several times in the past, and again with this issue, Gemini have included various catalogues and data sheets with the subscription mailings. In addition the recent Questionnaire only went in with the subscription issues, apart from a few that were left over and that were therefore sent to one dealer. So remember, if you're not a fully paid up subscriber you may be missing vital information and opportunities!

Front covers

I ought here to belatedly thank Alf for his work. Alf has been responsible for drawing the front covers on the last few mags, and in my opinion he has made an excellent job of them. In Vol.3 Issue 3 I asked if anyone could understand its front cover. Well ONE person responded, on his Questionnaire (and unfortunately I haven't been able to find it in the pile again), and got it right. Well done sir. The answer?? Well the SVC has the ability of supporting a serial (cereal) keyboard!! (Yes, Alf may get 10 for quality, but only 2 for content!!)

Letters to the Editor

Pertec Mods.

TYPE

I am a Nascom 2, Gemini 64K RAM card, GM829 FDC and Gemini IVC user. I am running CP/M 2.2 with Pertec FD250 drives.

I purchased surplus Pertec drives from the USA and had lots of problems. All the problems were a result of leaky decoupling capacitors. For those of you who intend to purchase surplus FD250, I suggest that all the decoupling capacitors should be replaced. Other than this, the drives are great.

There is a simple hardware and software modification to get these drives reading and writing 40 tracks instead of 35 tracks. Though I have done this independently, I understand "Henry's" is offering this modification. For those of you who are interested, please write and I will send full details.

Those of you who intend to make the printer-buffer as published in the June 1984 issue of BYTE, please note that the PIO output of the Nascom 2 or Gemini should be buffered and properly oriented with pull up resistors before it will work. I assume that the readers will have taken care of the other errors in the buffer hardware and software as published by BYTE

All correspondence on the above and other Nascom/Gemini subjects welcomed.

Yours truly, Hiten Patel, 4 Navyug Sagar, 183 Walkeshwar Road, BOMBAY 400 - 006. India.

Further thoughts on Hisoft Pascal

charl1 = ARRAY[1..11] OF CHAR;

HEADER: ARRAY[1..3] OF fcbtype;

Following on from the random musings of Dr. Dark in the issue of July-August 1984, I am writing to tell of some procedures developed for just this need. The CP/M manual describes a file control block (FCB) which is used for random access, but this is not allowed for in the standard Hisoft Pascal file handler - the Pascal one is three bytes too short. So we want a definition of a FCB which can be used for random access, like this -

```
{ charll is for the use of routines manipulating filenames
  unlike the Hisoft filename it coes not contain the : or .
   which are displayed in the directory listing so that
  a file will be
                   filenameext
                                  and not filename.ext }
   fcbtype.= RECORD
      DRIVE : CHAR;
      NAME : charll;
      EX: CHAR;
     S123 : ARRAY[1..3] OF CHAR;
      DIRECTY: ARRAY[1..16] OF CHAR;
     CREC : CHAR;
     RNDREC : INTEGER;
     RNDOVF : CHAR
   END;
VAR
```

{ whilst we are about it we can create three (or more) of the FCB's so that we can use more than one random file at a time }

The other definitions of types and variables must be placed in the program as needed. The most important of these is a buffer area to store the record read from the file or about to be written to it. The simplest of

definitions is simply an array of 128 bytes (or 64 integer numbers if you wish to use these or any other combination).

Having defined our FCB, there are a number of routines given in the CP/M manual which are of use apart from the obvious read random and write random. What of a function EXISTS which checks the existence of a file and returns a boolean value TRUE or FALSE? Here it is -

```
FUNCTION EXISTS(title:charl1):BOOLEAN;
VAR HEADER : fcbtype;
    i : INTEGER;
BEGIN
    i:=CPM(26,£80); (*reset DMA *)
    i:=fcbset(title,CHR(dkd),HEADER);
    i:=CPM(17,i); (* 255 if not found *)
    IF i = 255
        THEN EXISTS := FALSE
        ELSE EXISTS := TRUE
END; (* EXISTS *)
```

Note that we use the default DMA area (hex 80 to FF) to store the directory in as we read it, and the function used is number 17 which is search for first. Variable dkd is an integer giving the disk to be used 0=default 1=A 2=B etc. The function fcbset is used to copy the title and the disk number into the fcb area (called HEADER in this example), and also to set the extent, current record and overflow bytes to zero (ie CHR(0)). In my version of fcbset the function returns a value equal to the address of the fcb used but the value is ignored at present.

My versions of the random access routines (actually they are functions), use the fact that there is more than one fcb created, and calls this the channel number. The channel number is assigned by the programmer and all the function calls need this value (or else the wrong file would be used with unpredictable results). The routines are rather similar so I won't list them all (anyway I am not getting paid for this by the inch, and I earn my living as a professional programmer so I would be silly giving all my ideas away!!).

```
FUNCTION RDRAND
        (channel,bufad,recnum:INTEGER):INTEGER;
VAR i : INTEGER;
BEGIN
    i := CPM(26,bufad); (* set DMA *)
    HEADER[channel].RNDREC := recnum;
    i := ADDR(HEADER[channel]);
    RDRAND := CPM(33,i)
END; (* RDRAND read random *)
```

Call the function with the channel number, the ADDRess of the buffer area to be used and the number of the record. The numbers are both integers as you see so this limits you to records in the range of 0 to 32767. As the max number of a record in a CP/M file is 65535 (ie 8 Megabytes) I leave it as an exercise to any user to work out a way of getting the top half of such a large file (or for a small fee..?). Since I have mentioned fees, for a small fee I might supply all these routines (and the ones which are not given here) on a disk, but this letter should have given enough ideas to get anyone going in the fascinating jungle of random-access files.

Yours sincerely, Godfrey Nix, 11 Whitechapel Street, Nottingham.

BIOS in EPROM

In an issue some time ago Richard Beal was discussing the considerable hassle of patching a new BIOS into the MOVCPM.COM file for relocating CP/M.

I have just finished my own custom BIOS (for a Nascom 2 and two 8" Shugart drives) and have adopted another system for relocating CP/M which does not involve relocating the BIOS.

My BIOS sits in EPROMs; in a 56k system these start from E000 where it is entered by a reset jump. It contains its own routines for loading the CCP and BDOS from disk, to the desired place in RAM. A '56k' CCP & BDOS would be loaded near the top of RAM, whereas a `32k' CCP and BDOS would be loaded about half-way up. Having done this, the BIOS then initialises the jump table just after the BDOS and transfers control to the CCP. This has the advantages that the BIOS can be in EPROMs since it is always in the same place, one can use anybody's system disk to run in your system, regardless of which BIOS is on the disk, there is no restriction on the size of the bootstrap loader because there isn't one (normally it has to fit on the first sector of the disk and has to be short) and full disk read error recovery routines can be implemented in it. One therefore has a better chance of loading the system tracks off a slightly suspect disk; this is important since without disks one cannot do anything except some trivial debugging using SIMON, or reverting the whole Nascom back to NAS-SYS3 and ZEAP and writing some test routines to find what is wrong. I have found no need for SIMON and a 6k BIOS size limit is handy if you want to modify CONOUT to drive one of the flashy graphics/alphanumeric cards; some need a lot of software for writing text.

I have used the standard MOVCPM to produce a '56k' system (with the MDS-800 BIOS on it which is not used) and my BIOS starts at E000. It can extend up to F800, a total of 6k which is enough for most requirements. Even then there is a big gap for stack/data and with a slightly smaller BIOS (same size as Mr. Beal's I think) a 60k system can be configured. With non-Nascom hardware a 64k system is possible, using the IVC card.

The only disadvantages I can think of is that EPROMs are bit more difficult to patch that the MOVCPM is when in RAM.

I feel that people who implement CP/M on their own hardware will be interested in this approach, since they will simply buy a CP/M 2.2 disk in their chosen format, configure it for the biggest RAM they can get and are not trying to produce a licensed and commercially saleable system.

Incidentally, I have been told by Digital Research that the [v] option should be `avoided' when copying large files. It does indeed produce spurious `Verify Error' messages and aborts, but my disk read/write routines do not report any errors. The appearance of this problem is consistent for a given file. Does anyone have any clues why this should be?

Yours sincerely, P. Holy, Worthing.

Private Wants

WANTED: Processor, driver and power supply printed circuit boards for Epson MX80 or MX100 printer, either working or not working but must be mechanically undamaged to facilitate rebuild of damaged printer. Telephone (0742) 460609.

WANTED: Does anyone have any information on IBM 3270 interfacing/operation, i.e. manuals etc., OR would like said machine cheap with spares. Call Ian, Ipswich (0473) 831535.

When Nas-Sys scans the keyboard it stores the state of all the keys in 9 "KMAP" positions, known as KMAPO to KMAP8, at locations 0C01 to 0C09 hex, 3073 to 3081 decimal. These are updated every time the keyboard is scanned.

The chart shows the Nascom 2 keyboard as layed out. Beneath the legend for each key is the address and below that it's contents after a keyboard scan when that key is pressed. This is shown in both Hex and decimal notation. The contents remain the same on repeated scans until the key is released. Since each key is bit-mapped it can be detected irrespective of how many keys are simultaneously pressed. When several keys sharing the same map address are pressed, the content is the sum of the values for all the keys pressed.

Note that SHIFT does not change the contents for any key but only puts 10 (hex), 16 (dec) in KMAPO. Similarly GRAPH and CTRL are mapped as any other key.

When key presses are required to control features of programmes, the use of this table avoids involvement with repeat keyboard routines and their associated adjustable delays.

Example:

Assembly

SCANKB EQU 62H KMAPO EQU 0C01H

TESTKY LD HL, KMAP0+2

SCAL SCANKE

BIT 3,(HL) ; "D" pressed, other keys "don't care

JR Z,RTN1

INC HL

INC HL

LD A,4

CP (HL) : "8" pressed but no others using 0C05H

JR Z,RTN2 ; or CALL Z

JR TESTKY ; or RET

Basic

10 K=USR(0):REM Scan keyboard user routine

20 IF PEEK(3075) AND 8=8 THEN 100:REM Go to routine 1

30 IF PEEK(3077)=4 THEN 200:REM or GOSUB

40 GOTO 10:REM or RETURN

 $\,$ KMAP0 $\,$ is "duplicated" as KMAP8 at 0C09 hex (3081 decimal) and properly should be used instead. In practice I have never found any difficulty either way.

NASCOM KEYBOARD MAP CHART

1	2	3	4	5	6	7	8	9	0		r	3	
0C07 10	0C07 08	0C06	0C08 04	0C02	0C03		0C05 04	0C06 04	0C07 04	0C01 04	0C07 40	0C08 40	
30 <i>79</i> 16	3079 8		3080 4	3074 4	3075 4	3076 4	30 <i>77</i> 4	3078 4	3079 4	3073 4	3079 64		
GRA	Q	W	E	R	Т	Υ	u	I	0	P	6	BS	,
0C06 40	0C06 10	0C05 08	0C04 08		0C02 20		0C04 20	0C05 20	0C06 20	0C07 20	0C01 20	0C01 01	
3078 64	3078 16	30 <i>77</i> 8	3076 8		3074 32		3076 32		3078 32	3079 32	3073 32	3073 1	
CTL	A	s	D	F	G	Н	J	к	L	;	:	ENT	СН
0C01 08	0C05	0C04 10	08 0C03		0C08	0C02 01	0C03		0C05 01	0006 01	0C07 01	0C01 02	0C01 40
3073 8			3075 8	3074 8	3080 1	3074 1	3075 1	3076 1	3077 1	3078 1	3079 1	3073 2	3073 64
SHF	Z	×	С	V	В	N	М	,	+	/	SHF		
0C01 10	0C03 10	0C02 10	08 0C08		0C02 02		0C04 02		0C06 02	0C07 02	0C01 10		
	3075 16		8 3080				3076 2				3073 16		
CL.	CU	-	411 6	ing some range force being a	SI	PACE			*** **** ****	CD	CR		
0C03	0C02 40					C08 10				0C04 40	0C05 40		
3075 64	3074 64					08 0 16			,-		3077 64		

Add	iresse	s /	Hex Dec	0C01 3073		0C03 3075					0C08 3080
(Conten	ts									
bit	t Hex	Dec									
0	01	1		BS	Н	J	K	L	;	‡	G
1	02	2		ENT	В	N	M	•	+	/	V
2	04	4			5	6	7	8	9	0	4
3	08	8		CTL	F	D	E	W	3	2	C
4	10	16		SHF	Χ	Z	S	Α	Q	1	SPC
5	20	32		6	T	Y	U	I	O	F'	R
6	40	64		CH	CU	CL	CD	CR	GRA	E	3

GIANT INTELLIGENT PRINT BUFFER FOR GEMINI CPU CARDS By Richard Beal

This article gives you all the information and software which you need to set up a print buffer for a serial printer, using a Gemini GM813 CPU+RAM card with no other cards on its 80-BUS, or alternatively a Gemini GM811 CPU plus GM802 64K RAM combination. The print output, in a form suitable for a Centronics printer, is sent from the PIO of the host computer via the GIPB to the serial printer.

A large print buffer allows you to keep using your computer even when you have generated a very long printed report such as a program listing, without having to wait for the printer. This Giant Intelligent Print Buffer (GIPB) operates almost as fast as you can send data to it. For example when listing data to the screen using an SVC, which is very fast, there is no noticeable slowing up when sending the data to the GIPB at the same time.

You may like to develop the idea further, so here are some suggestions:-

- write a version which runs under a normal RP/M or CP/M;
- allow the display of characters in the buffer;
- allow buffering of the characters to an attached disk;
- develop a full automatic print spooling system;
- write a version with serial input and Centronics output.

The User Manual for the GIPB - Version 2.5

This program, called GIPB, is a special version of RP/M designed to perform the specific function of acting as a giant intelligent print buffer. Hardware requirements are:-

- (a) a GM813 CPU+RAM card or a GM811 CPU card with extra 64K RAM card.
- (b) a serial printer for output.
- (c) a cable connecting the PIO socket to the PIO socket of another computer which is set up to output data to a Centronics printer. If the other computer if a GM811 or GM813, or a Nascom I/O card, a 26 way ribbon cable with a connector at each end is all that is needed.
- (d) an optional serial keyboard on the printer, or a keyboard on the GM811.

No disk card or video card is required. Since there will normally be no video card the printer also acts as the console output device. See the RP/M documentation for details of operation without a video card. On the GM813 it is simply a matter of linking pin 1 to pin 14 on the link block labelled IC35. On the GM811 connect pin 6 to pin 7 on LKB1.

The card(s) may easily be added to an existing 80-BUS system by plugging it (them) in to the last connector(s) on the BUS. Since this would interfere with the BUS signals, cut all the lines on the motherboard except the power lines, which are 1 to 4 and 67 to 78.

As with RP/M, the UART speed for the printer may be changed by altering location F009 in the EPROM to hold the 2 byte UART divisor. This is normally 417 decimal, stored as 01 Al, giving 300 bps. Printer handshaking is supported in the normal way, if required. This is via pin 8 of the serial connector, which must be high to operate. Connect it to pin 2 if you have no handshake line.

The ports are used in a way compatible with the Gemini implementation of the Centronics interface, as follows:-

Port A is used in control mode.

Bit 0 is an output signal from the GIPB and is high when Busy and low when able to accept data.

Bit l is an input to the GIPB and is a strobe which goes low for a short

time when data has been sent to port B.

Port B is used in control mode, as the GIPB input port. Bit 7 of the input data is ignored, and the output to the printer has even parity added to follow the normal standards.

Operation of the system is completely automatic, and all data received is printed as soon as possible. The program uses a circular buffer and compresses consecutive spaces to save memory. Up to 128 consecutive spaces can be held in one byte. Most listings contain many spaces, so the buffer will often be able to hold well over 100K in the 60K available. If the buffer becomes full the Busy line remains high so that no data can be lost.

If a keyboard is attached the following single character commands are available:-

- Space Halt the printer, or if halted start printing again. This does not affect the input of data to the buffer.
 - D Delete the contents of the buffer and restart the program.
 - T Delete the contents of the buffer and restart the program with a minimal buffer of only two bytes.
 - CR Output CR/LF to the console device (normally the printer).
 - M Output a status message to the console device. This shows the number of characters waiting to be printed, the number of bytes spare in the buffer, and whether or not the printer has been halted.
 - N End the program and pass control to RP/M. RP/M operates as normal and can boot a disk system but does not have any cassette handling routines. The command G F000 will execute the program from RP/M.
 - ! Halt the processor.

Technical Notes

The GIPB operates on an interrupt driven basis, with an interrupt being generated when the input strobe goes high rather than low. It was necessary to do it this way because some host software does not initialise the ports correctly so that the first character is lost following a Reset. This method overcomes this problem and should not cause any problems. Some host software will send a null during initialisation. This is sent to the printer which is likely to ignore it.

The GIPB catches all characters transmitted by enabling interrupts and then setting the Busy line to 0. After about 8 instructions the Busy line is set back to 1. This should give the host machine plenty of time to notice that the line is not Busy, and decide to output the data. The GIPB waits for

about 40 instructions after it sets the Busy line back to 1 before it disables interrupts. This gives the host machine more than enough time to send the data and make the strobe go low and then high again. If an interrupt occurs the Busy line is at once set back to 1 to ensure that a second character is not sent. This system should work correctly, although it would in theory be possible for someone to write a Centronics output routine which is so slow to send the strobe after examining the Busy line that the data is held until the interrupts are next enabled. This could in theory cause loss of characters. All known versions of the BIOS for Gemini systems, as well as RP/M itself, work perfectly as host machines.

The GIPB accepts input both when it is inactive, and when it is waiting for the handshake signal or the UART status to become ready during printing.

The GIPB adjusts to the size of memory available, so that in fact only 2K of RAM at the start of memory is needed, although this would be of limited use. The reason that 64K is normally needed is that there must be 4K of RAM at the top of memory, occupying the same addresses as the EPROM. This is because the EPROM has to be paged out during use of the GIPB, and it copies itself to the same area in RAM. This is necessary because of a hardware feature of the card which prevents the PIO receiving the RETI instruction from code in the EPROM. Therefore the PIO can handle only one interrupt and then locks up.

An alternate version of the GIPB operates using only port B, with the Ready and Strobe lines for handshaking. This requires special interrupt handling software at the host end.

>.my~~Cm "L.:.p2B p";.M~~: &tMbz!.. .."y.msU ey".PK!> N..1 byt M for Ge 5 - GIPB mY.xV.GO .y.@..>C MizAaf.r ..psfrf" have given the complete code needed. It is necessary only to EPROM and plug this into a GM813. The listing is shown in two which does the GIPB ld easily be adapted two halves have been calculated √w/... S /S7S5/S4 CpvmSW.! .m{N.!tq : .q....ît ! Is.r6s>p ! \s[rbs}q i \q[rbs]q]qqs i }qqqsm{U.** /wE....<
....N..@.
2.....wE id.:.e"} A .KN!.. .[1M{}*. .~.D..M .~FO eE .LpM]ZM: .@.y!..! mini V2. CqpC\qC. C.pC. |C\$.}C.}!.. o>.s. you can see could easily ED 42 3A 000 00 11 11 45 45 B7 FF 55 8E F3 F3 F3 11 12 12 18 18 22 22 28 28 F0 C3 21 22 74 74 42 42 30 C3 32 932 08 00 00 32 D3 20 73 21 22 25 25 27 27 21 00 20 12 3E 3E 23 00 77 00 FC 17 21 79 47 47 47 FE 21 23 4E 4E 000 FF C4 ED 4B E9 E9 F3 F1 C9 C3 F6 18 08 62 20 49 36 BE AF 119 72 01 00 CB FF 00 00 00 EB D2 E9 FD FD 7E 0C CD FA 2A 41 3E 00 77 77 ED FF 23 23 38 8E 42 C3 FF 18 18 72 47 D6 the 00 22F 20 20 00 00 00 62 62 C1 86 01 FC 86 FF FF E9 67 78 for the as you system. FF 222 222 FE 57 57 57 69 60 00 01 DE 21 8F 119 66 20 110 ο£ 000 000 000 000 000 000 000 000 000 20 00 79 79 79 79 59 e poo A1 C3 1C 21 20 20 20 20 59 ren the source cod If contained, and as 3A 00 E5 56 39 E9 E9 the CRCs FF 21 33 23 23 21 00 00 00 00 00 00 34 36 86 B4 21 21 23 23 F0 F1 F1 F1 93 24 18 25 27 07 FE 00 2A 2A FF E5 CD 00 00 D3 00 F4 5E 3E FD E9 55 D3 B0 11A C0 77 77 83 01 F0 C3 C3 83 83 83 83 1E CRC 01 1D 1D 1D 5F 88 80 87 57 57 57 57 57 78 78 11 60 00 11 00 11 FO 0B checking F1 21 21 8C 8C 8C 52 52 56 FF B5 53 00 19 19 E2 E2 ED 77 CE 50 30 31 31 32 half: | half: 5C 09 21 22 20 20 20 20 FE given self c convenience, any D3 ED 4E 00 F2 F2 F3 GIPB. EF FF 111 222 4E 4E 22 22 00 C3 C3 13 12 21 21 21 20 01 first hase second also g It is s under a 20 01 23 22 22 24 28 18 39 F0 C3 C3 03 20 6E 6E to make 7E 20 0B 05 4C 4C FF 32 D3 70 ED 5F 5F F3 F1 15 00 77 77 77 77 00 00 11 18 71 03 03 FF 18 18 73 69 Below I hacreate a 2732 E halves, for conseparately, to AF C3 00 00 F1 B1 FD FD 04 00 00 00 00 00 00 F7 01 2F 18 32 46 1B FB C3 C3 OF 89 89 65 65 for for have Record (
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IF CD 10 FB F1 eM.{a}uM.{q} C3 05 00 2A 61 f.F.N@^c*a 62 F6 0E 09 CD .m[c.mKe .l.bvM 55 D3 E1 3D 20a+M.{CNCsa=F1 DE FB 20 }!{a"U IM:{.^*}C 01 3C D3 E4 3A	88 D3 EO 21 80	B7 20 12 3A ing boot B7 C8 32 40 @.7 .W 00 2B 5F 16 .>.1:E.7 0D C9 21 45/ CD CA FD CD .41:@.7 46 C8 21 4C/ A 2A 46 00 .>@M.TM 0B CD 55 FD 6.E6.(! 0D 20 D7 CD >.*F.w	0D 20 F7 CD {"m[F.MB 2B E 3E 20 -~5.2E.* 18 10 7E FE~4() 18 F3 79 E53[] 18 F3 79 E53[] 19 CD F3 !KFa() 79 21 03 00 >.C8 m.	CO CS 1 22 43 C.7 11 701 CO CS 0 CS 0 CS 0 CS 13 18 2 4 CS 13 2 4 1 00 CS 0 ED 10 7 m. IM. FE CD 78 FE {M.}MJ"! CD 96 FD 24 MB". (.M. SO 3E 7E CD ". e*H.	3E OA F5 C5 D5 .}.(>. M.)>.uEU DB BD CB 6F 28 eOM9paQA qIu[=Ko(C9 CD FB FD D8 gcSR[]X g(zIM{})X BC FD 30 FB C9 M.~XM~]X TIM{})V[1] 14 FE D8 DB DD MV}0[IM{})XX[-] 15 F5 D3 B5 00 .P[8Iu[4 .8{quS5.} 3A 03 00 E6 02 >}S4.>.S 4q1:f.
FB E1 7D F5 1F 1F 1F 17 00 ED 48 65 00 C9 11 00 ED F8 63 00 C9 11 00 E1 F5 5 C9 CD 34 10 FB 34 61 00 BF 20 35 56 CD 11 FB 34 61 00 BF 20 4F DB E0 07 07 2F A1	FA 3E 0B CD D1 FB 3E 80 ED 78 28 FC DB E3 F3 FA 9F FB DB E0 B7 47 47 ED 52 11 EF FB 00 00 21 80 00 01 80 00 A 3D 20 FD DB E0 0F 68 24 42 61 64 20 64 52 64 69 73 68 24 45	62 6F 6F 74 24 3A 45 0C CD BC FD 38 02 AF 3A 45 00 B7 28 19 2A 0D 20 07 AF 32 45 00 40 00 B7 20 09 CD 86 32 40 00 79 CD 86 80 C CD CA FD FE 03 00 18 28 21 4D 00 BE	46 00 CD 42 FE 12 13 32 45 00 2A 46 00 23 38 08 FE 58 30 04 3E FC F5 3E 1D CD 38 FE 6E 128 1D FE 0C 20 38 FE CD 19 FD CD 42 FD CD 85 FD 79 CD 50 FD 70 FD	DE FD 30 FB 29 7 7 FB 20 7 7 7 FB 20 7 8 42 00 32 00 42 00 32 00 60 60 60 60 60 60 60 60 60 60 60 60	18 00 3E 0D CD 9D FD FO ED B1 B1 C1 F1 C9 F5 C9 DB BE CB 67 28 FA CD CD FB FD DB C9 F5 DB B4 1F 38 FB 00 3E FF D3 B4 F1 C9
Record 6. 7C E5 CD 07 0410: 10: E6 OF C6 90 0420: 20: 00 ED 5B 63 0430: 30: 05 00 E1 2B 0450: 50: 31 3E D0 CD 0460: 60: 62 00 D3 E2 0470: 70: A8 B1 E6 02	Record 7. 0480: 00: 20 EC C3 5D 0490: 10: 00 0E E4 06 0440: 20: 78 28 FC D8 0460: 30: 2A 80 00 11 04C0: 40: CD 82 FB 11 04D0: 50: 00 D3 EO 3E 04F0: 70: 72 6F 6E 67	69 6E 67 40 00 B7 00 3E FF 00 19 7E 00 34 C9 AD FE 4F 00 BE 20 36 03 23	10: 78 FE ED 20: AD FE 3E 30: AD 7E 7E 30: AD 7E 7E 30: AD 7E 7E 30: AD 7E 3	20: 03 A 41 20: 03 A 41 40: 00 E9 79 50: B7 E8 EE 60: FB CD 96 70: CD 42 FE 4 11.	0690: 10: 9D FD 10 FB 06A0: 20: E5 4F CD 39 06B0: 30: FA F1 D3 B8 06CD: 50: CD 14 FD 30 06E0: 60: IF D0 DB B8 06F0: 70: 3E FD D3 B4
				5555585	
MYXQAE.z 1 rzQae 8.>. Miz az3Jbz .MQa7(zxemn	بالاستان استان	amcvc. "v!pvc!. M.{7mR.0 M.{7mR.0 M.{7mR.0 "X7Bcv.7m "x7Bcv.7m "t.'		1 (27BcvD 2 (27BcvD 2 (27 1) 2 (27 1) 3 (271.0M 4 (271.0M 7	
23 1B 7A EM.{MnzA 10 7E E5 3(hM CD 69 FA UE 8.". 67 62 FA MYXQE. E1 B7 28 .\embedre NMSGE. C5 CD 6EMCb	07 FB CD .eM.{Mnz 7A FA F1 nzq" 8." 72 FA CD MizMzzM CO 23 18 .zaMnz B7 20 3B =.eMBz"7 13 CD 4E +*aw£.^a 13 18 E3 zH.("". CD C2 PA+	E3 21 00 kx: ".".R C3 05 00 .7(.*a.e 52 19 30 : ".B^v 3A 60 00k.k.m 13 B7 ED ".B^vM.{ FE 04 D2 ".B^vM.{ FE 04 D2 ".B.vM.{ FE 04 D2 ".B.v	FA F6 10 ".Rcv> 60 00 FE Sm0> B7 C2 E3 .2.v.RO 01 C2 DE v]Mrz". 60 00 FE vC3".Bc F6 78 BA .HIS"NEF ED 43 3B .y;(.EE 28 06 2A .G=":."	E3 F6 44 "."b^vM IE FB 44 MmYI: " 20 06 21 MmXH: {C} I3 1A FE "."a.".R 00 09 CD (z^*, (v^*, 00) 1E 20Miz. EB FE 61 .wEMrzA.	0E 0A CD X*{PV I. 36 00 11Mbz! B C8 23 £! 1 \\ 5D 00 77 71Mbz! 7E FA D6 7H" 38 02 37 0X".8.V. 1B 37 C9 1.4£mo£m
1B 7A EM.{MnzA 7E E5 3(hM 69 FA UE" 8.". 62 PA MYXQZZ B7 28 .\eVMbz. CD 6EMCb 22 61 zAI:`.".	6E FA E1 7E 5 F5 CD 07 FB CD .eM.{Mnz} 10 FE 7F 30 0C F5 CD 7A FA F1 nzq" 8." FA CD 62 FA 06 0B CD 72 FA CD M1zMzzM 4E FA 20 07 78 B7 20 0C 23 18 .zaMnz 7E B7 28 0D 23 23 7E B7 20 3B =.eMBz"7 DE E1 1A FE 2E 20 07 13 CD 4E + "awe." a 20 08 13 1A B7 28 1F 13 18 E3 ZH.("". 18 BD FE 2F 2F 01 01 13 CD 22 FA " - + + - = 20 A FF A CD 12 AA F1 CD 57 EA +	2A 3E 00 18 AA E1 CD E3 F0 C3 8. /(.* 02 D2 DE F6 21 70 F6 E3 21 00 kx:\.".R 00 E5 11 80 00 0E 1A C3 05 00 .7(.*a.e DE F6 CD 1E FB B7 ED 52 19 30 :\.".B^\dagger 03 ED B8 C9 ED B0 C9 3A 60 00R.k.\.m 1E FB 78 B7 C2 E3 F6 13 B7 ED \.".B^\dagger 12 3 18 F4 3A 60 00 FE 04 DD \.".R.k.\.m 03 28 03 01 00 60 FE 02 30 03 \.\dagger 04 25 26 78 F6 78 \.\dagger 05 21 00 C0 7A B7 C2 E3 F6 78 \.\dagger 06 17 20 18 18 18 18 18 18 18 18 18 18 18 18 18	01 1C 1D 28 03 87 18 FA F6 10 ".Rcv> B0 3E 11 D3 FF C9 3A 60 00 FE SmO> D2 DE F6 CD 1E FB 7C B7 C2 E3 .2.v.RO C2 C0 0B 3A 60 00 FE 01 C2 DE v3M"z". C2 E3 F6 CD BD FE 3A 60 00 FE vC3"".Bc 23 46 23 78 B1 CA E3 F6 78 BA .H1S"NEF 23 23 18 EB 4E 23 46 ED 43 3B .y;(.EE 00 FE 02 D2 DE F6 B7 28 05 2A .C=":." 30 00 CD 00 FB C7 52 EA CG 2A	70	C9 11 7F 00 3E 7E 12 0E 0A CD X*{PV I. 80 00 5E 16 00 19 23 36 00 11Mozi AF 02 CD C2 FA D8 7E BC 08 38 E8t
C5 CD 07 FB CD 6E FA C1 CD 59 F8 D1 E1 23 1B 7A EM.{MnzA B3 28 02 10 E8 06 02 CD 72 FA D1 E1 06 10 7E E5 3(hM D5 C5 FE 20 38 04 FE 7F 38 02 3E 2F 5P CD 69 FA UE 8 C1 CD 59 F8 D1 E1 23 22 61 00 1B 7A B3 C4 62 FA AMYAGAE. 10 DC E5 D5 CD 62 FA 0E 0B CD 05 00 D1 E1 B7 28 .\empharton \text{vembaz}. 9C OE 01 CD 05 00 C3 62 FA 78 FE 09 C0 C5 CD 6EMCb FA C1 C9 3A 60 00 FE 01 C2 DE F6 ZA 61 00 22 61 ZAI:\\\\\\	00 E5 CD 00 FB CD 6E FA E1 7E E5 F5 CD 07 FB CD .eM.{Mnz} 6E FA F1 FE 20 38 10 FE 7F 30 0C F5 CD 7A FA F1 nzq~8.~ 5F CD 69 FA CD 7A FA CD 62 FA 06 0B CD 72 FA CD MizMzzM 87 FA E1 06 00 CD 4E FA 20 07 78 B7 20 CO 23 18 .zaMnz BD 04 E5 CD C2 FA 7E B7 28 0D 23 23 7E B7 20 3B =.eMBz~7 2B 7E E1 77 23 18 DE E1 1A FE 2E 20 07 13 CD 4E + "awe."a FA C8 18 28 FE 22 20 08 13 1A B7 28 1F 13 18 E3 ZH.("". 78 CA 7F B7 28 50 75 75 75 75 75 75 75 75 75 75 75 75 75	F8 3A 60 00 FE 02 D2 DE F6 21 70 F6 E3 21 00 kx:\.\tilde{\cdots}.RR 3A 60 00 FE 02 D2 DE F6 21 70 F6 E3 21 00 kx:\.\tilde{\cdots}.RR B7 28 03 2A 61 00 E5 11 80 00 0E 1A C3 05 00 \cdots\(\cdo	FE 04 D2 E3 F6 3E 01 1C 1D 28 03 87 18 FA F6 10 ".Rcv> D3 FF 11 00 01 ED B0 3E 11 D3 FF C9 3A 60 00 FE Sm0> 01 DA DE F6 FE 03 D2 DE F6 CD 1E F8 7C B7 C2 E3m0> 01 DA DE F6 FE 00 C2 C0 0B 3A 60 00 FE 01 C2 DE v}M"z". F6 C3 B3 FE FE 0D C2 E3 F6 CD BD FE 3A 60 00 FE vC3".Bc 01 C8 21 D3 FE 4E 23 46 23 78 B1 CA E3 F6 78 BA .HIS"NEF 20 04 79 BB 28 04 23 23 18 EB 4E 23 46 ED 43 3B .y;(.EE 00 C3 BD FE 3A 60 00 FE 02 D2 DE F6 B7 28 05 2A .C=":."	40 OF E C O C C D O FE O D C C D O C F O C C C C C C C C C C C C C C C C	D8 FE 78 D0 D6 20 C9 11 7F 00 3E 7E 12 OE 0A CD X*{PV I. 05 00 CD 62 FA 21 80 00 5E 16 00 19 23 36 00 11Mozi 81 00 C9 01 60 00 7E 02 21 03 7E 02 21 03 7E 02 21 00 00 0 22 5E 00 AF EF 08 38 E8*

```
The source code of the GIPB, as a routine to be linked into a program,
                                                             Written by Richard Beal in 1982, originally using only port B with
                                                                  strobe and ready lines, using interrupt handling on both machines.
                                              Provision is made for the output to be controlled from a keyboard
                                                                                                                     for a short time to show when a character is ready to be printed.
                                                                             This version developed in 1985 to add the option to emulate a parallel printer using a Centronics interface, so that interrupt
                                                                                                                                                    ; Set true to emulate Centronics interface
                                                                                                      For the printer emulator version Port A is the control port.
                           as
                          Written to enable a Gemini running under RP/M to operate
               Glant Intelligent Print Buffer (GIPB)
                                                                                                                                                         Set false for original version
                                                                                                  Port B is the input data port in both versions.
                                                                                       handling is no longer required on the host.
                                          Output is to a printer on the serial port.
                                     Input is via a PIO, interrupt driven.
                                                                                                                                                ; Select type of parallel interface
                               glant intelligent print buffer.
                                                                                                                                     equ not false
                                                     on the serial port.
                title CPMGG
                                                                                                                                                                         global gipb
                                                                                                                                                                                   ; Dummy routines
                                                                                                                                                     equ true
                                                                                                                                                                    ; GIPB routine
                                                                                                                                edn 0
                      .280
      follows.
                                                                                                                                 false
                                                                                                                                      true
                                                    >ZM8"IU. .H..U..@
u:.f.(. >.M8">YM
8"zM8"{M 8"qQIU..
      @U:?._[0 +(.+2?..
7.QI:..f .H>.M8">
kM8"M8"7 H>.M8">K
                                                                             4.d....
                                    ->=M8-|F
                                                                   h.cD~>.s
                                                                        89}88>
                                                                                  .!.P.A .. .! ... P..
                                                                                       ? . . $4..
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                     M8"MB"7I u[2.8{qS
                          11>.M8~>
                               I.MB~2H.
                                          M8"}F M 8"I>.M8"
                          11[2.8{[ 1
?M8~MB~2 1
MB~I>.M8
                                                                                        .h...E.
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# # # # # #
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111
07
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                                                    D5 C9 C9 D3 D3 O6 24
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38
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      118
CD CD
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C8
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33
CD
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      5F
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42
FE
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1B
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42
17
17
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4F
                                                                                                            FE CD B2 B2 FE C9 FE C9
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# # # # # # #
      3A
D1.
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FE
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                                                    CD 03 7A 7B 00 03 00 00 00 00
                                                                                                  88
      D5
1F
CD
C9
C9
C9
C9
                                                                                                  23
07
FF
FF
FF
                                                                                                                                               5A
3A
FE
18
2A
2A
A1
A1
                                                                                                  36
119
FF
FF
FF
                                                                                                                                               *************
                 6B
CD
3F
CD
20
                                                    3E
F5
38
08
08
01
01
                                                                                                                                               0880: 00: 0890: 10: 08A0: 20: 08B0: 30: 08C0: 40: 08D0: 50:
                                                                                                      0810: 10: 0820: 20: 0830: 30: 0840: 40: 0850: 50: 0850: 0850: 0870: 70:
                                                    0780: 00: 0790: 10: 07A0: 20:
                                                                  07B0: 30:
07C0: 40:
07D0: 50:
07E0: 60:
                                                                                                                                          Record 15,
          0710: 10: 0720: 20: 0730: 30: 0740: 40: 0750: 50: 0750: 60:
                                                                                             Record 14
  decord 12.
                                               Record 13.
                                                                                       07F0: 70:
                                                                                                  0800: 00:
                                          0770: 70:
```

```
From the point of view of the host, bit 0 is an input printer Busy line with 1=Busy, 0=Ready. Bit 1 is an output strobe which is made low
                                                                                                                                                                                                                                                                                                                                                                                                                      Address of top of memory+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                       Interrupt address table
                                                                                                                                                                                                                                                                                                                                                                                                                                        Start of work area
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               Direct console I/O
                                                                                                                                                                                                                                                                                                                                                                                                     RP/M entry point
                                                                                                                                                                                                                                                                          global reset,open,close,read,write
                                                                                                                                                                                                                                                                                                                               ; Set returned value (for dummy routines)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                        Start of buffer
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               Print string
                                                                                                                                                                                                                                                                                          global make, setdma
                                                                                                                                                                                                                                                                                                                                                   external bret
                                                                                                                                                                                                                                                                                                                                                                                                                                                       equ 0160h
equ 0200h
                                                                                                                                                                                                                                                                                                                                                                                                    equ 0005h
                                                                                                                                                                                                                                                                                                                                                                                                                      ed u 0006h
                                                                                                                                                                                                                                                                                                                                                                                                                                        equ 0100h
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              ; RP/M routines
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 9
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               6
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  edn
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   edn
                                                                                                                                                                                                                                                                                                                                                                                      ; Addresses
                                                                                                                                                                                                                                                                                                                                                                                                                                                         inttab
                                                                                                                                                                                                                                                                                                                                                                                                       pqos
                                                                                                                                                                                                                                                                                                                                                                                                                      abdos
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 conio
                                                                                                                                                                                                                                                                                                                                                                                                                                         work
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   prts
                                                                                                                                                                                                                                                                                                                                                                                                                                                                             stk
```

```
; Interrupt control word - enable interrupts for low to high, mask follows
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     out (pcl),a ; Direction control word - define bit \boldsymbol{\theta} as output and rest as input
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ; Interrupt mask - only interrupt on input bit 1 (when it goes low)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           out (pcl),a
; Output value to data port A to show printer busy
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     out (pc2),a
; Direction control word - ail bits are input
                                                                                ; Port B disabled
                                                                                                                  ; Port A disabled
                                                                                                                                                                                                                                                                                                                                                                                                                        ; Port A
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          out (pc2),a
; PIO port A to mode 3, control
ld a,Ocfh
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         if cent
; PIO port B to mode 3, control
1d a,Ocfh
                                                                                                                                                                                                                                                       ; Load I register eph: ld a,high(inttab) ld i,a
                                                                                                                                                                                                                                                                                                                                                                                       1d a, low(inttab)
; Disable CPU interrupts
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            ld a, Ofdh
out (pcl),a
                                                                                                                   out (pcl),a
end1f
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            out (pd1),a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               out (pcl),a
                                                                                                                                                                                                                                                                                                                                                                                                                       out (pcl),a
                                                                                                                                                                                                                                                                                                                                                                                                                                                        out (pc2),a endif
                                                                                     out (pc2),a
                                                                                                                                                                                                                                                                                                                                                                       ; Interrupt vector
                                                                                                                                                                    ; Ensure PIO happy
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           ld a, Ofeh
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               1d a,0b7h
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            ld a, Offh
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              1d a,01h
                                                                                                                                                                                      ld hl,eph
                                                                                                                                                                                                                                                                                                                        ; Interrupt mode
                                                                  1d a,03h
                                                                                                                                                                                                                                                                                                                                                                                                         if cent
                                                                                                      1f cent
                                                                                                                                                                                                       push hl
                                                                                                                                                                                                                                                                                                                                                                                                                                           else
                                                    ; Disable PIO
                                                                                                                                                                                                                                                                                                                                       fm 2
                                                                                                                                                                                                                        reti
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              ; Copy RP/M to RAM at same address to avoid hardware problem ; (RET1 from ROM not received by PIO)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ; Length of message
                                                                                                                                                         PIO data port A
PIO control port A
PIO data port B
PIO control port B
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ; Write interrupt address table to work area
                          ; Line feed
; Carriage return
                                                                                           UART data port
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ; RS232, no ROM
                                                                                                          Modem control
                                                                                                                                            Modem status
                                                                                                                            Line status
                                                                                                                                                                                                                                                                                                                                                                                                                             ; Heading message and work area head: defb cr,lf,"* GIPB * hw: defb "O waiting "hs: defb "O spare "hh: defb "
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                equ work+hw-head
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                equ work+hs-head
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                equ work+hh-head
defb "Halted"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ld hl,procl
ld (inttab),hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              defb cr,1f,"$"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  out (uartm),a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  ; Switch out the ROM
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  1d hl,0f000h
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 1d d,h
1d e,1
1d bc,1000h
                                                                                                                                            equ uartd+6
equ 054h
                                                                                                         equ uartd+4
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  equ $-head
                                                                                                                             equ uartd+5
                                                                                                                                                                          equ pd1+2
equ Ob5h
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  ld a, Ofh
                                                                                                                                                                                                             equ pd2+2
                                                                                           edn Ob8h
                                                                                                                                                                                                                                              : Dummy routines
                          equ Oah
equ Odh
                                                                                                                                                                                                                                                                                                                                                                                                 jp bret
                                                                                                                                                                                                                                                                                                                                                                                 xor a
           ; Characters
```

setdma:

headl

hcr:

headh gipb:

spare

reset: close: write:

open: read:

; Ports

cr

uarts uarth

pc1 pd2 pc2 pd 1

uartm uartd

```
; Accept lower case
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ; Move to work area
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ; Space entered
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ; Move "Halted"
                                                                           if "I" restart with 2 byte buffer
cp "I"
fr nz,y4n2
fr nz,y4n2
fd bc,stk+2
fr st3
; If "N" return to RP/M
yin2: cp "N"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ; Move spaces
                                                                                                                                                                                                             ; RS232, ROM
                                                                                                                                                                                                                                                                                                                                      id de,hcr
jr yin5
; If "M" output message to printer
yin4: cp "M"
                                                    ; If "D" restart with empty buffer
                                                                                                                                                                                                                                                      ; If CR output CR/LF to printer
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ; If " "flip "Halted"
yin6: cp " "
                                                                                                                                                                                                                           out (uartm),a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 push de
push bc
1d de,headh
1d bc,6
cp " "
1d h1,haltm
jr z,yin7
1d h1,hh
                                                                                                                                                                                            jr nz,yin3
1d a,07h
                                                                                                                                                                                                                                                                                                                                                                                                                                                     id de,work
id c,prts
call jbdos
                                                                                                                                                                                                                                                                    cp cr
jr nz,yin4
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        jr nz,yin8
jr z,noin
cp "a"
                                                                                                                                                                                                                                                                                                                                                                                               jr nz,yin6
                          jr c,yinl
                                                                                                                                                                                                                                                                                                                                                                                                                          push de
push bc
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              r yine
                                                                                                                                                                                                                                                                                               push hl
                                                                                                                                                                                                                                                                                                              bush de
                                                                                                                                                                                                                                                                                                                           push bc
                                                                                                                                                                                                                                                                                                                                                                                                             push hl
                                          and 5fh
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      push hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ldir
                                                                                                                                                                                                                                        ret
                                                                      yin];
                                                                                                                                                                                                                                                                                                                                                                                                                                                                   yin5:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             yin7:
yine:
                                                                                                                                                                                                                                                                    vin3:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    tinx:
                         out (pc2),a ; Interrupt control word - enable interrupts for port B
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ; DE is used to point to position to store input ld d,b \, ; Set to end of buffer
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ; Reset bit 7 to not represent compressed blanks
                                                                                                                                                                 ; BC is used to point to the end of the buffer+1 st2: ld bc,(abdos)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                              ; Set to start of buffer
                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ; HL is used to point to character to be output
                                                                                                                                                                                                                                                                                                                                      ; Start of buffer
                                                                                ; Dummy read to start handshake in a,(pd2) endif
; PIO port B to mode 1, input 1d a,4fh
                                                                                                                                                                                                                                                                                                                       ; Set number of bytes spare
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     for keyboard input
push hl
push de
push bc
                                                                                                                                                                                                                                                                                                                                                                 call sparep
inc hl
                                                                                                                                                                                                                                      ld hl,head
ld de,work
ld bc,headl
ldir
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            îd e,0ffh
1d c,conio
call jbdos
                                                                    out (pc2),a
                                                                                                                                                                                                                                                                                                                                       ld hl,stk
push hl
                                                                                                                                       ; PIO is now ready
```

sbc hl,bc add hl,bc jr nz,siz

or a

siz:

pop hl

ld e,c dec de 1d (de),a

; Scan tin:

pop de pop hl or a

pop bc

; Set up work area st3: push bc

push bc

pop bc

1d a,87h

```
; Set port A bit 0 to show busy
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           ; Test for too many blanks
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ; Character in buffer ; Test for first blank
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ; **** INTERRUPT HANDLING INPUT ROUTINE ****
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       ; Get data
; Strip parity
; Check for blank
                                                                                                                                                                                                                                                                                                                                                                                                                                             ; Includes EI/DI
; Some waiting, so test if halted n6: ld a,(headh) cp "".
                                                                                                                                                                                                                                                                                       inc

or a

sbc hl,bc

add hl,bc

jr nz,p6

ld hl,stk

; Subtract l from Walting

>6: push af

call waltm
                                                              ; Chars are waiting for output ld a,(h1)
                                                                                                                                                                                                                                                             pop af; Test for end of buffer inc hl
                                                                                                                                                                                                                                                                                                                                                                                                                                 ; Call output routine call out jr tinx
                                                                                        ; Decompress spaces
bit 7,a
jr z,p4
cp 80h
jr z,p3
dec (h1)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    if cent
ld a,0lh
out (pdl),a
endif
                                                                                                                                                                                                                                   push af
call sparep
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     in a, (pd2)
and 07fh
cp " "
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            jr nz,pr5; Compress blanks
ld a,(de)
                                                                                                                                                                                jr p6
p3: 1d a," "
1d (h1),a
; Add 1 to Spare
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 bit 7,a
jr z,pr4
cp Offh
jr z,pr4
                                       jr nz,n2
                                                                                                                                                                   1d a," "
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        procl: push af push hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                       ; Allow time for host to realise (assume tight loop)
                                                                              ; Does not apply unless host uses interrupt lines
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ; Allow plenty of time for host to send data
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ; Show printer busy again
                                                                                                                                                                                                                                                                                                                                                                                                                                  ; Enable interrupts
; Show printer not busy
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ; Disable interrupts
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            ; Disable interrupts
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     ; Enable interrupts
                                                    ; If "R" handshake (not usually needed)
                                                                                                                                                                                                                                                                                                                                                                                                   ; Still some spare so allow PIO input n4: if cent
                                                                                                                                                                                                                                                                                        ; None waiting, so test if some spare
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ; Loop
                                                                                                                                                                                 No input from keyboard; Test if chars are waiting noin: 1d a,(wait-1) cp ""
  ; If "!" halt for debug
yin8: cp "!"
                                                                                                                                                                                                                                                                                                                              ld a,(spare-1)
                                                                                                                                                                                                                                                                                                                                                     jr nz,n4
ld a,(spare)
cp "0"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                jr nz,n4a
1d a,01h
out (pd1),a
1d a,20
dec a
jr nz,n4b
                                                                                                                                                                                                                                      jr nz,n6
ld a,(wait)
cp "0"
                                                                                                                                                                                                                                                                                                                                                                                                                                                           out (pd1),a
1d a,3
                                                                                          else
cp "R"
jr nz,tin
in a,(pd2)
jr tinx
endif
                            jr nz,yin9
halt
                                                                                                                                                                                                                                                                           r nz,n6
                                                                  if cent
jr tin
                                                                                                                                                                                                                                                                                                                push af
                                                                                                                                                                                                                                                                                                                                                                                            jr z,n5
                                                                                                                                                                                                                                                                                                                                                                                                                       if cent
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    pop af
                                                                                                                                                                                                                                                                                                                                                                                                                                                 xor a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      dec a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       endif
                                                                                                                                                                                                                                                                                                    xor a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           else
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     n4a:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   :4b
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    n5:
```

```
; Allow plenty of time for host to send data
                      ; Show printer busy again
                                                                                                                             ; Disable interrupts
                                                                                                                                                                            ; Wait until free
                                                                                                                                                     in a, (uarts); See if free yet
                                                                                                 ; Test handshake
; Test CTS
                                                                                                                                                                                                         out (uartd),a; Output data
          jr nz,out2a
1d a,01h
out (pd1),a
1d a,20
                                                                                                    in a, (uarth)
                                                          dec a
jr nz,out2b
endif
                                                                                                                                                                               r z,outl
                                                                                                                                            ir z,outl
                                                                                                                  bit 4,a
                                                                                                                                                                   bit 5,a
                                                                                                                                                                                                                       pop af
                                                                                                                                                                                                pop af
 dec a
                                                                                                                                                                                                                                                               end
 out2a:
                                                               out2b:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ; Allow time for host to realise
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      (assume tight loop)
                                                                                                     out4:
                                                                                                                                                                                                                                                                                                                                                                                                                                                         pusn a.

jp pe,out0 ; Make processor 80h
; Decide if interrupts enabled while waiting
out0: push af ; Low level I/O routine
'onare-1) ; Test if any spare
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ; Show printer not busy
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ; Enable interrupts
                                                                                                                                                                                                                                                                                                                                          ; Subtract 1 from chars spare
                                                                                                                                                                                                                                                                                                                                                                                                                    ; **** OUTPUT ROUTINE ****; Output character to printer
                         jr nz,ntzero
ld (hl),"9"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ld a,(spare)
                                                                                                                                                                                                                                                                                                                                                                  ld hl,spare
jr ascsub
                                                                                                                                                                                                                      cp (hl)
jr nz,wfin2
ld (hl),"0"
                                                                            ntzero: dec a
ld (hl),a
cp "0"
jr nz,wfin
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             out (pdl),a
                                                                                                                              dec hl
ld a," "
cp (hl)
jr nz,wfin
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    jr nz,out2
                                                                                                                                                                               inc hl
ld (hl),a
pop hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ir z,out4
ascdec: 1d a,(h1)
                                                                r ascdec
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   if cent
                                                                                                                                                                                                                                                                                      pop hl
pop hl
                                                                                                                                                                                                                                                                                                                                                      sparem: push hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           1d a,3
                                                 dec hl
                                                                                                                                                                                                                                                             pop hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    xor a
                                                                                                                                                                                                                                                                            ret
                                                                                                                                                                                                                                                                                                    wfin2:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       out2:
 ; Not too many so increment
                                                                                                                                                                                                                                                                          ; Do not enable interrupts
                                                                                                                                                                                  jr nz,pr6
1d de,stk ; Set to start
                                                                                                                                                                                                                                                                                                                          ; **** ARITHMETIC ROUTINES ****
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ; Subtract 1 from chars waiting
                                                                                                                                                                                                           ; Store character in buffer
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ; Add 1 to chars waiting waitp: push hl
                                                                                                     ; Test for end of buffer
                                     pr4: 1d a,80h; Subtract 1 from Spare pr5: push af
                                                                                                                                                                                                                                                                                                                                       ; Add 1 to chars spare
              jr pr6; Compress first blank
                                                                                                                                                                                                                                                                                                                                                                                                        jr nz,gotdig
ld (hl),"l"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                       jr nz,not9
1d (h1),"0"
dec hl
                                                                                                                                                                                                                                                                                                                                                                  ld hl,spare ascinc: ld a,(hl)
                                                                             call sparem
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ld hl, wait
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    ld hl,wait
ascsub: push hl
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           inc a
ld (hl),a
                                                                                                                                                                                                                                    ; Add 1 to Waiting pr8: call waitp
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               jr ascinc
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 jr ascinc
                                                                                                                                                                   sbc hl,bc
                                                                                                                                                                                                                        1d (de),a
                                                                                                                                                                                                                                                                                                                                                                                                                                                        gotdig: cp "9"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       waitm: push hl
                                                                                                                                                                                                                                                                                                                                                      sparep: push hl
                                                                                                                                                                                                                                                             pop hl
pop af
reti
                                                                                                                              1d h,d
1d 1,e
                                                                                                                                                                                                                                                                                                                                                                                                                                   pop hl
                                                                                          pop af
                                                                                                                   inc de
   inc a
                                                                                                                                                       or a
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         not9:
```

GSX THE GRAPHICS INTERFACE

By Dave Russ

Who among you has suffered the trauma of having purchased at great expense a wonderful new colour card and then realised that you have weeks of work ahead of you creating some sort of software interface to your favourite language? As is often the case you will have to create a library of low level primitives armed only with boundless enthusiasm and a manual whose flashy cover does not reflect its contents. Fear not, for the cavalry is on its way. The Digital Research GSX graphics system will relieve you of this tiresome task, leaving you time to get on with what you originally had in mind (Maybe seeing your family and friends once in a while.)

GSX (Graphics System Extension) allows you to write application programs in any language that supports BDOS calls, and provides you with an environment that is independent of the device(s) that will eventually display the end product. Along with 2 other DR products, GSS Kernel and GSS Plot, you are able to program graphics applications that conform to the emerging Graphical Kernel system (GKS), a draft international graphics standard. Kernel and Plot are not essential to you, and you do not have to use them in order to produce graphical routines, but they do provide a friendlier interface to GSX giving you access to a standard library accessable from popular high level languages. DR have specified Pascal, Fortran and PL/I so far. The whole thing is similar in concept to the relationship between the BDOS and BIOS in that you have a standard interface to custom built device drivers.

Having decided that this is for you, off you trot to your friendly software dealer with your loot in your hand and swop it for the GSX80 (or GSX86) disk. For your money you will have received the following:

- GSX.SYS This is the actual GDOS that will load itself into memory and process all your graphics calls.
- GENGRAF.COM A utility program which is run against a graphics program once it has reached the .COM stage. GENGRAF attaches a GSX loader to your program. The GSX loader receives control as soon as the program is run, its purpose is to handle the loading of GSX.SYS, the rearrangement of the BDOS pointers, and then the loading of the assignment table ASSIGN.SYS (see below) along with the first device driver that is specified, which must also be the biggest. Once it has finished its work the loader brings down the application program from its position above the loader to the start of the TPA at 100H and executes it.
- ASSIGN.SYS This is an ASCII file containing the device driver numbers and names that you want to use in your particular system. As it is possible to have only one device driver in memory at any one time GSX has to refer to the table contained in ASSIGN.SYS in order to select new drivers when they are required by the system. The copy that you find on your master disk will contain the names of a few of the sample device drivers supplied to you on the disk, and as such will have to be altered to suite the drivers that you will be using.
- A number of ready to go device drivers This sounds good doesn't it? We have just bought the disk and we are off already. However, the bottom line here is that unless you own a Hewlett Packard 7220 plotter, a Digital Engineering Retro-Graphics colour monitor, or an Epson MX-80 with Graftrax plus, these drivers are not going to be of much use to you.

Referring to the last item, it seems that most members of the 80-BUS fraternity will have to stop and think at this point. "I now have GSX, but what about the device drivers for MY system?" Well you have a choice of two options, the first being to write your own device driver or secondly, wait for the one you want to be released.

Writing your own does seem to defeat the object of the exercise, doesn't it? However it is possible, you are able to implement as much or as little of the standard as you wish depending on the capabilities of the device. A word of warning here. The device driver specifications supplied with the GSX disk are attractivly bound and the contents well laid out, but trying to write a GSX device driver from the knowledge contained therein should not be attempted unless you are sure of your sanity and/or you have a hot line to Digital Research in Newbury. During the creation of the Gemini device driver for the Pluto board I have needed to refer to both the GSX80 and GSX86 manuals for information, the GSX86 one being by far the better of the two. Test software is yet another problem, as writing your own will not confirm that you have got it right. All testing for the Pluto driver so far has been done using the DR DRAW drawing package, a fine piece of software, but it will cost you £232 at current retail prices. The DR compiled BASIC, CBASIC, will also help you as it contains inbuilt commands that allow you access to GSX, and you will find yourself £393 the poorer for this experience. So in a nutshell, unless you are sure that you are committed to the subject it might be better if you waited for your device driver to appear on the scene.

But will they arrive? Well Gemini will soon be releasing a device driver for the popular Pluto board that will be configurable for the 640 and 768 versions in both high and low res. Input routines have been written for keyboard, digitiser and mouse, and seperate drivers may be supplied for each of these devices. They also have a driver for their GM837 colour card under development though no release date can be forecast for this just yet. As far as other devices are concerned, who knows, but I suppose if the demand is there others will appear.

So how does it work? Lets first take the source program that you yourself will write. You forget all about the target display machine and its limitations, frame size, aspect ratio etc, as GSX will sort all that out for you. This concept means that your program is capable of being displayed on any graphics device for which you have a GSX device driver. You have at your disposal up to 33 graphical routines depending on the particular device driver you have installed, these include old favourites such as line drawing and text display, along with extra goodies like complex polygon fills that will cater for a number of fill patterns. Each GSX function is invoked by a special call to the BDOS (115 in C register). All the data associated with a function call will have been stored in special arrays of your own creation and their start addresses passed across using the GDOS parameter passing conventions. (This is where GSX starts getting a little complex — but more on this later.)

The 32767 X 32767 virtual frame size means that you can afford to be lavish with your coordinates and even include some form of zoom feature in your emerging bijou of a program, providing that those around you don't take offence at the constant stream of expletives and apparent recurrence of brain death associated with such activities.

So you have typed in the source, and as usual it has compiled first time (bliss), you link and load it to produce the .COM version, nothing new so far. Now you enter GENGRAF <filename><RET> and the utility will attach the GSX loader to your program. Your graphics program is now ready to run.

When run, the GSX loader gets the first look in as previously mentioned, loads GSX.SYS to create the GDOS interface, loads the assignment table and the first named device driver contained therein. The space now occupied by the device driver is now referred to as the GIOS, which lives just below the BDOS and its workmate the BIOS. Refer to Nuts and bolts for more detail. The application program is moved down to 100h and executed.

The first command of any program will be GSX opcode l open workstation. This will inform the GDOS which of the available device drivers is to be used. If it is already in memory, entry one of the GIOS is called. If another driver is specified, it will be loaded into the GIOS area from disk. It can now be seen why the first entry in ASSIGN.SYS must be the name of the biggest driver available to the system, as GSX determines the amount of memory to allocate the GIOS solely from inquiring the size of this first named driver. If a subsequently loaded driver is bigger than the allocated GIOS size confusion will follow.

Open workstation calls the first entry in to the GIOS, and firstly informs the GIOS of any defaults that the application requires, such as line colour, marker type etc. More importantly though, this function returns to the GDOS information concerning the device that it is currently working with. On exit from open workstation the GDOS will have details contained in it on the exact capabilities of the device. These details include X and Y axis resolution, aspect ratios, no. of colours, available fonts, and more. In fact 57 16 bit values are returned to reflect the device specifications. Not only does the GDOS use this to prepare itself for the following commands, but this information is also available to the calling pogram if it needs it.

So they're off!! Your much awaited graphics program will now spring into colourful life, and all the lines and circles etc whos coordinates you programmed inside the GSX 32k X 32k virtual frame size now appear on your screen or whatever, which may only be 640 X 288 for example. Whats more your circles are circular, because the GDOS has received information on the aspect ratio of your screen.

In the time taken for your display to plot, the GDOS has intercepted all calls to the BDOS in which the C register contains the value 115, any others it passes on to the BDOS as normal. The control array is interrogated to see if you wanted to open a new workstation, if so another device driver is loaded, if not all coordinates contained in the array PTSIN are scaled to device size and control is passed to the GIOS. So as you can see the job of the GIOS has been simplified as the device has been passed coordinates that it can understand.

If information has to be returned to the calling program, such as in the case of `Inquire input locator', i.e. where is the graphics cursor now, device coordinates are returned to the GDOS and are likewise scaled before control is passed back.

NUTS & BOLTS.

I will now try and explain the technicalities of working with GSX. These will be clarified with the aid of diagrams (a picture's worth etc), as is does seem rather complex at first. It is worth mentioning that once you, as the applications or device driver writer, have created a routine that allows you to easily reference the data arrays concerned, the task is not quite so daunting as it first seems.

As calls to the BDOS involve the use of the BC and DE register to inform of your intentions, the problem is how do you manage to pass sometimes large amounts of data over using only one 16 bit value. Of course the answer is with the use of pointers as usual. Don't forget that the C register contains 115 on all calls to GDOS regardless and therefore cannot be used for pointer work.

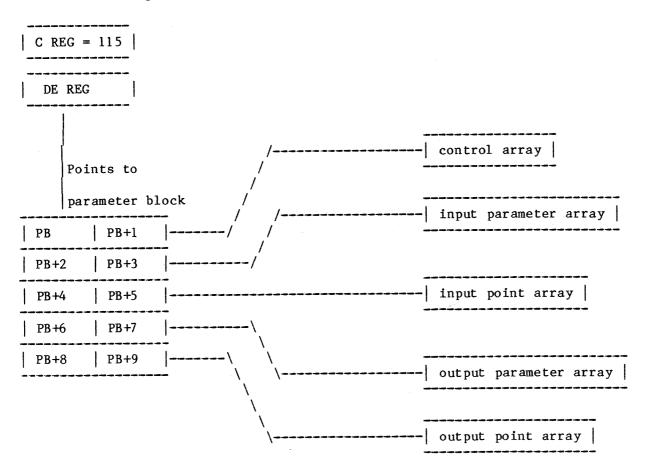
The GSX standard expects the application programmer to have set up 5 arrays, and to give them their proper names, these are:

PARAMETER BLOCK

This 5x16 bit array contains the start addresses of the other data arrays described below. On a call to GDOS the DE register pair must contain the start address of this array.

PB pointed to by DE.

Fig 1. On call to BDOS requiring a GSX function.



Parameter block contents.

PB	Address of control array
PB+2	Address of input parameter array
PB+4	Address of input point coordinate array
PB+6	Address of output parameter array.
PB+8	Address of output point coordinate array

CONTROL ARRAY

This area is used by the GDOS and GIOS for control of the selected function. For example control(1) will contain the number of the required graphics routine (Remember open workstation - opcode 1). The remaining fields are used to contain values representing the amount of valid data contained in the other arrays on both entry to, and return from, the GIOS. These are usually extracted by the GIOS and used as loop counters.

INTIN ARRAY

Contains information to be used by the GIOS in a called function. These are not usually point coordinates but colour change values, text strings, input device modes and the suchlike.

PTSIN ARRAY

Contains point coordinates passed to the GIOS from the calling program. Used to contain line coordinates for example. This array is scaled to device coordinates by the GDOS before control is passed to the GIOS.

INTOUT ARRAY

Similar to intin but used by the GIOS to return data to the calling program. Typical entries are text rotation achieved as opposed to rotation asked for, input samples flagged as successful or not, linestyle selected etc.

PTSOUT ARRAY

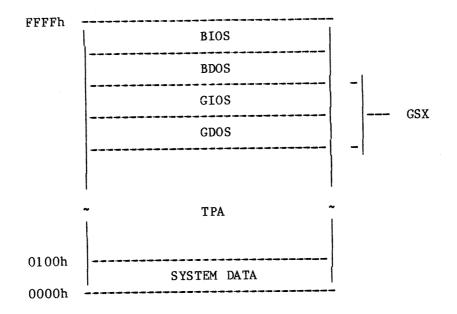
Similar to ptsin but used by the GIOS to pass coordinates back to the calling program. This array is also scaled by the GDOS before control is passed back to the caller, but this time to the 32k X 32k virtual frame size.

The GSX standard dictates that all array elements are 16 bit values, even ASCII text strings. All array references in the documentation are 1 based, which can be a source of bugs if you forget that array(1) is really array(0).

Memory arrangement.

Those of you au fait with the CP/M map may care to take a look at fig 2, which shows where the GDOS and GIOS live when at home. All calls to BDOS are rerouted through the GDOS first (via a modified 0005h) and passed on to the BDOS if it is not a graphics request.

Fig 2. GSX memory arrangement for CP/M 80.



The final washup

Well, what do you think? Is GSX the answer to the maidens prayer or is it too cumbersome in its constructs to be of any use to you. One thing is for sure, you will never be able to achieve the same fast animated graphics capability that you can get by `Bareback' colour board programming, due to the number of processes that have to be gone through before an element is displayed. But it is a much needed standard that will allow Joe Public to tap a variety of software sources, without ever having to find out which ports his cards are mapped to.

However if you were to give GSX the push you would never be able to use the new generation of graphical software that will shortly become available; I'm referring in particular to the desktop emulator that DR were showing off at COMPEC this year, it is similar to the things that we have seen from Macintosh so I'll say no more except that as a CP/M user you know it will run on more than one type of machine.

Before I return to "The happy hackers' holiday home" (Quote P.Greenhalgh, Gemini), I would like to mention the hours of innocent fun that I have had using DR DRAW to test out the Pluto GIOS. I would thouroughly recommend it to anyone who is passing through their second childhood, (and serious business users of course). The latest fun activity being the creation of a picture of a door, which to all intents and purposes is quite harmless, but if you zoom in on the keyhole and have a peep through you will find out whatever it was that made the butler blush......

SUMMARY OF GSX OPCODES

Opcode

Description.

- Initialise Workstation. Loads the device driver if necessary and sets default values.
- Close Workstation. Halts graphics output to this workstation. ~
- Ŧŧ Clear Workstation. This clears the device and is equivalent to CLS used on a CRT device. 3
- Update Workstation. Display all pending graphics. 4
- character output if the device has an alpha mode with addressable character cells. Function 5 is called and an escape sequence ID is passed Enable device dependent operation. These deal mainly with to GSX in control(6). 5

Description

- Inquire addressable character cells.

- 2 Enter graphics mode.
 3 Exit graphics mode.
 4 Alpha cursor up.
 5 Alpha cursor down.
 6 Alpha cursor right.
 7 Alpha cursor left.
 8 Home alpha cursor.
 9 Erase to end of screen.
 10 Erase to end of line.
 11 Direct cursor address (Move to row and column).
 12 Output cursor addressable text.
 13 Reverse video off.
 14 Reverse video off.
 15 Inquire current cursor address.
 16 Inquire tablet status. (Is a digitiser connected?)
 17 Hard copy. e.g. Dump a graphics screen to printer.
 18 Place graphics cursor at location.
 19 Remove graphics cursor. This turns the cursor off.
- 20 5051 100
- Reserved for future expansion.
- Unused and available.
- Polyline. Output lines from data in PTSIN array. 9
- Polymarker. Output markers at positions given in PTSIN. These markers are typically (. * 0 X +). 7
- Text. Output text from machine font at specified position. 00
- Filled area. Display and fill a polygon. 6
- Cell array. Create a pixel array from colour data given in the INTIN array and at a position given in PTSIN. 10

continued ŧ SUMMARY OF GSX OPCODES

Opcode

Description.

- Generalised drawing primitive. These routines give you an easy way to display bars, arcs, pie slices and circles. These are not always fully Π
- Set character height. Not possible of course if the Pluto font is used but should be implemented for plotter device drivers and the suchlike. 12
- Set character up vector. This allows you to rotate character strings if the device will allow it. 13
 - and for Set colour representation. Will allow you to specify the red, green blue intensity associated with a colour index. (Presumably this is use in palette systems.) 14
- or Set linetype. You should be able to choose from solid, dashed, dotted dashed-dotted. 15
- Set line width. 16
- Set line colour. 17
- Set marker type. 18
- Set marker scale. 19
- Set marker colour. 20
- Set hardware text font. (Only one to choose from in Pluto.) 2.1
- Set text colour. 22
- Set fill interior style. You should be able to choose from outline only, solid fill, pattern fill or hatch pattern fill. 23
- or Set fill style index. This allows you to specify the type of pattern hatch fill you require from the selection available. 24
- Set fill colour index. Having chosen the type of fill you require you can now say what colour you want it done in. 25
- Inquire colour representation. Returns the RGB intensities of a requested colour index. 26
- Inquire cell array. Returns the pixel colour values of the requested 27

reas

SUMM	SUMMARY OF GSX OPCODES - continued
Opcode	de Description.
28	Input locator. This function serves as the interface between GSX and outside world. Typically this will call the digitiser or mouse to relinformation on its whereabouts. When in request mode a cursor will appoin the screen and move according to the action of the specified in device. When in sample mode the current locator position is immedia returned and is often used in conjunction with ESCAPE 18 to plographics cursor while at the same time displaying a rubber banding li
29	Inquire input valuator. If some sort of analogue device is connected the workstation, then the current value of its status is returned. Go be of use if graphical displays of external monitoring devices required.
30	Inquire choice device. The choice device may be something like a ke or function keys. For use in menu driven applications I would imagine
31	String device. This returns a string from an input device which wil course be the keyboard in most cases.
32	Set writing mode. This affects the way in which lines or filled a etc. are placed on the screen. The modes available are reploverstrike, complement (XOR) and erase.
33	Set input mode. This lets you specify the type of input device you require next i.e. locator, valuator, choice or string. You have als specify whether this device is to operate in request or sample mode request mode the device waits until an event occurs such as the digit pen being pressed down to terminate input, in sample mode the cur status or location of the device is returned without waiting.

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Private Sales

Micropolis 400K Single/Sided Disk Drive, £85.00. Climax Colour Card and Software, £75.00. Mr Ward, Macclesfield (0625) 610678.

Large 19" rack case suitable for Gemini/Nascom with 8-slot card rack, floppy disk mounts, power supply. £85.00 ono. Plus Nascom 2, 64K RAM A card (4 MHz), cased with power supply and earom programmer. Software includes Nas-Sys 3, Zeap, Nas-Pen, Nas-Debug, Tool Kit, Documentation, etc, £220.00 ono and Gemini GM812 IVC card, £120.00 ono. Please make me an offer, I may not be able to refuse it. St Albans (0727) 73057.

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IBM Selectric KBD Printer, ex 2741, with Hardware/Software interface for Nascom 2/Nas-Sys or any system with 8-bit port/Z80. In excellent working order, with Service Manuals. Haggle around £115.00, Ian on Ipswich (0473) 831353. (PS: Faster than some cheap daisy wheels, and better print Q.)

There is a program in the CP/M user group library which is so useful that if you don't have it and you don't have access to the library then it is worth typing it all in - so it is listed in full below.

This program is called CRC.COM and its function is to calculate CRCs, which are a special almost infallible type of checksum, on files. The program can be used to confirm that a file has not been corrupted, even if it has passed through many different computer systems and communications links.

In its simplest form, you enter the command:CRC filename

and the CRC for the file is displayed, as two hex numbers.

The filename can be ambiguous, so if for example you type:- CRC B:*.*

all the files on drive B are read and the CRCs displayed.

Now the shortcoming of this is that if you received a file you would need to know what its original CRC was in order to be sure that the current CRC was correct. But this is where CRC is so useful, because if you enter an F after the command as in:-

CRC B:*.* F

then the resulting CRCs are not only displayed on the screen but are also written to a file called CRCKLIST.CRC. So when a disk of software is prepared, just before it is issued the CRC program is run, and the CRCKLIST.CRC file is added to the disk. The user of the disk has only to run the CRC program to the screen, or using Control-P to a printer, and compare the results with the values in the CRCKLIST.CRC file, which can be seen or printed using the TYPE command. Wouldn't it be a good idea if Gemini did this!

When version 5 of CRC.COM appeared, it had grown in size by more than 1K, and it didn't seem any different to the earlier versions. But it turned out to have a quite amazing feature which is well worth the extra space (and your time to type it in). If a disk has a CRCKLIST.CRC file on it and you just type the command CRC with nothing after it, then it will read in the file and then calculate the CRCs of all the files on the disk, reporting on whether they are different to those in the CRC file. It also reports on missing files. This means that with a single command you can verify the entire contents of a disk and be certain that it is the same as when it left the supplier. And if the software doesn't work, the supplier can't get away with the old excuse "It must be a bad disk - send it back and we'll replace it". And suppliers can save the trouble of replacing disks which are in fact correct.

For those who are curious to know how the CRCs are calculated, here is the equation, which is a CCITT standard polynomial:

 $X^16 + X^15 + X^13 + X^7 + X^4 + X^2 + X + 1$

I don't have a copy of the source code unfortunately, and I haven't found it in the CP/M library, so perhaps an enterprising person will disassemble it and comment it nicely. If they do, 80-BUS NEWS would like to print it, as it is quite short and must be a fine example of compact software.

Since you have to type in the code of CRC.COM, the first thing you should then do is type the command:-

CRC CRC.COM

which should give the answer B2 07, proving both that it works and that you haven't made any mistakes.

Perhaps when I tell you that hundreds of disks full of free software like this are available from the CP/M user group, and that it produces an interesting journal several times a year, you will send a cheque for £7.50 for your individual annual subscription to:-

CP/M Users Group (UK)
72 Mill Road
Hawley
Dartford
Kent DA2 7RZ

This is also the address of Derek Fordred, the software librarian, who can give you information about the amazing service which he offers.

The object code for CRCK V5.0 is given elsewhere.

LOST CHARACTERS IN CP/M

By Richard Beal

One of the advantages of having a buffered keyboard like that provided by the Gemini GM812 and GM832 video boards is that you can key ahead. However when using CP/M these characters can sometimes get lost. There are several reasons for this. One is that some programs check the keyboard and "gobble" any characters they find. Some programs, like MBASIC and WORDSTAR can gobble one character while they are starting up, and then it can reappear when the program is exited. But the most common problem and the one which is most annoying is that one character gets lost when a warm boot occurs, for example at the end of a PIP command. This is because the one character workspace in the BDOS is overwritten during a warm boot, and its contents are lost.

This article describes how to cure the problem of characters getting lost during a warm boot. I have used this patch for a long time, and have found it a useful improvement. It is very dangerous to make any alteration to the BDOS, since this leads to a non standard system, but this small change is harmless. I do not recommend any other changes to the BDOS. The SYS BIOS has implemented this alteration by patching the BDOS after each warm boot, but this article shows how to make the same change to the standard Gemini versions of CP/M, including the excellent new version called BIOS 3.

The solution is to move the location of the one byte workspace out of the BDOS into a spare location in the BIOS, by altering all references in the BDOS to this location. The location in the BIOS must be zero initially, otherwise a spurious character will appear on the screen after a cold boot. The method of installing the patch is to change the CP/M system which is generated by running either MOVCPM in the case of older versions of the BIOS, or GENSYS in the case of BIOS 3.

Having generated the system, use your debugging program to load the CP/M image, and examine location 13FC. This will contain OE. (If it doesn't - STOP!) Change this to 8D. Now examine the next location, 13FD. Take the value in this byte, add OB to it, and replace it. Now repeat the above for 1424-1425 and 1443-1444. Then SAVE the CP/M image to disk and use SYSGEN to write it to the system tracks. Use CONFIG as usual. This will place the workspace in the 32 byte patch area provided in the Gemini BIOS. No other changes are needed.

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THE DH BITS

By David Hunt

A few days ago a customer of ours popped into the shop and gave us a long tale about having exported some gear in 1982 and because of some customs problem could they have a copy receipt for the goodies? Well much as we try to please, trying to find a receipt for goods supplied on an unknown date in 1982 is a little too much. So, as we remembered the customer, and were therefore pretty certain that he had bought something a long time ago, we suggested that we could give him a new receipt, but dated 1982, altogether a lot less hassle. This was fine, but could we make sure that the receipt was not dated on a Sunday or Bank Holiday or some other such suspicious date? I wonder how many of you have a 1982 diary or calendar to hand? We certainly didn't. This whole thing was taking on the proportions of a farce, as trying to find a 1982 calendar looked like turning into as much fun as trying to find the original receipt. Then a thought occurred. A very long time ago, when Nascom first grew BASIC, I cobbled together a Calendar program. Did it still exist, had it been converted to run under CP/M, etc? A quick consult of the CAT program revealed CALENDAR.BAS, which when fired up, worked.

This turned out to be an interesting program, short and to the point, so for my sins, it's offered here. It may even have been published before, although a quick look through the old back issues of INMC didn't reveal it. Looking closely at the program, I'm pretty sure it's not all original DH, so I must have pinched it, or bits of it, from somewhere. The basic algorithm for working out the start date seems to be attributable to David Ahl's `101 Basic Programs', but different, and I'm pretty sure, left to my own devices I wouldn't have calculated the screen TAB positions the way it's done here, but 1979 was a long time ago and premature senility is creeping over me, so I don't remember. One thing I do remember was the trap for the 1752 start for the Gregorian calendar, and the nasty business of the (MOD 4000)+2000 over the fact that the year 2000 won't be a leap year when it should be. This doesn't happen again until the year 6000, so it looks like I `short cut' the procedure and checked at the 1000 year boundary. This makes the effective operating range the 1250 odd year span from the year 1753 to the year 2999. The 1752 trap won't allow earlier dates, and the 2999 trap stops the program thinking that the year 3000 is not a leap year when it should be. All pretty esoteric really as I doubt that anyone will be interested in dates outside a century span anyway. See Listing One.

Another thing which has been causing problems lately (and still on the subject of dates and times) is using machine code routines under dBASE II. Now versions 2.4 or later allow machine code calls, and the favourite for these is making the cursor display different on the SVC/IVC card, or reading either of the Gemini clocks (the clock on the GM816 I/O card or the GM822 RTC) in as dBASE data.

The cursor first. The various permutations of Gemini CP/M do different things with the cursor when either waiting for a CP/M command, or actually executing a program (it's documented in the manual, so I won't repeat it here). Normally the cursor blinks whilst in the CP/M command mode, and becomes a non-blinking cursor when executing a program. I say normally, as the Winchester based Quantum machine I borrow when I visit Gemini, turns the cursor off completely when executing a program. I find this infuriating if the program I'm using doesn't turn it back on again, and of course, dBASE is one program that doesn't. I know this is very easily patchable with the program

CONFIG, but I can't be bothered to do it to a machine which doesn't belong to me in the first place.

All that apart, I use dBASE with the reply prompts highlighted, that is, in inverse video, and I like to see a flashing cursor under these circumstances anyway. Further, the prompt is usually on the eighth line which looks untidy as it overlays the underhangs on characters like lower case g, j, y, etc. So I like to see a flashing cursor on the 9th line when using dBASE. How can this be achieved?

Well the obvious is to send a command string to the SVC/IVC to turn the cursor on, blinking, and move it down one line. It appears nice and easy, just work out the control words and then print them:

STORE CHR(27)+"Y"+CHR(72)+CHR(8) TO curs? curs

Not so, dBASE says different. The main problem in this instance is the CHR(8), instantly recognisable as 08h, or backspace. Now dBASE does not send a backspace, it translates the 08h into a cursor left movement. To achieve a back space you use the DEL key and the 7fh code is translated into a three byte string: 08h (to move the cursor back one), 20h (to delete the character at the cursor, which also advances the cursor) and 08h to move the cursor back again. (This problem is not uncommon, several control codes are converted either by the BDOS or the application program, so dBASE is not an isolated instance. The characters usually affected would be 08h, backspace, 09h, tab, 0ah, line feed and 0dh carriage return.) The answer is to use the PUTVID program in the SVC/IVC manual, but this means making a machine code patch for dBASE (or whatever).

According to the manual dBASE uses memory up to about a000H, so any address above this could be used for the patch, so I chose C000H for convenience. The machine code listing is given in Listing Two.

All it has is a data table at collh and the PUTVID routine enclosed in a loop to shove the four characters in turn at the video card. dBASE uses POKES like Basic, but the `call' procedure is slightly different, you use the SET function to set the call address, then use CALL to call it. So the (decimalized) dBASE version of the above is as follows, (49152 is the decimal equivalent of coooh):

* Enable cursor type SET CALL TO 49152 POKE 49152,6,4,33,17,192,219,178,15,56,251,126,35,211,177,16,245 POKE 49168,201,27,89,72,8 CALL

You could do the same with MBASIC, using the same POKE addresses and data, thus:

```
10 CURS=49152
20 FOR A=49152 TO 49172: READ B: POKE A,B: NEXT
30 DATA 6,4,33,17,192,219,178,15,56,251,126,35,211,177,16,245
40 DATA 201,27,89,72,8
```

50 CALL CURS

But be warned, MBASIC starts it's workspace and stack from the top of the TPA. The stack and/or workspace could come crashing through the program if you're not careful. So POKEing this into RAM in MBASIC is not a clever idea. Far sneakier is a method suggested by Carl Lloyd-Parker, and that makes use of the fact that although MBASIC knows where strings are in a program, it doesn't pull then out into the workspace area until some additive or subtractive manuipulation is carried out on it. In other words, the string stays where it was originally written in the program unless you do something nasty to it. Carl's method is to define a string of asterisks somewhere in the program, the string being as long as the machine code to be POKEd into it. Then calculate the position of the string using VARPTR, then POKE the code into the string as the example above. The address calculated from VARPTR is also the call address for the program. There are two examples of this, one by Carl in his IVC HIRES programs and another by me in the BASIC demo CLOCK program in the Gemini GM816 manual. The fun part of this method is if the string is placed as the first line of the program, then, when the program has been run, the string is full of lots of interesting junk, making the program unlistable (if you start the list at the first line).

Perhaps you're wondering how I decimalize the HEX code from the programs as assembled into a form useable by dBASE or MBASIC, or whatever, at least without making too many mistakes. Simple I use BASIC to do it! First I assemble the program using M80 and L80 as usual, then I load it up under ZSID, DDT, GEM-DEBUG or some other debugger. I then clear out the memory around the location where the program is to reside and move the program to the working address. This gives me the program in memory at its correct place with some ODh's before and after it. (Nice and identifiable that way.)

Next into MBASIC, work out the start and end addresses using the &H function in BASIC, note that this gives negative answers if not treated right. Take the instance above:

```
? 65536 + &Hc000
49152
Ok
? 65536 + &Hc011
49169
Ok
```

Now for the crafty bit, open a sequential file and write the code to it, I do this in the command mode like so:

OPEN "O",£1,"CODE": FOR A=49152 TO 49169: PRINT£1, PEEK(A);: NEXT: CLOSE

Surpise surpise, this gives me an ASCII file that I can bash into a text editor and edit to suit. My favourite address for machine code to be used in this way is c000h, as for some reason I can always remember 49152. When it comes to the end addresses of these programs, having calculated it I usually have a quick PEEK around the calculated address to see if I got right, hence the nulls either end of the program. The whole process takes about as long as it took to write up and being done by machine is not susceptible to human error.

Listing One	240 **	<pre>** What's the first day</pre>	t day?			
10 *** CALENDAR ***		K=2+J1+INT(J1/4)-INT(J1/100)	(31/100)			
20 . 30	5/0 K=K+1 580 C=K-I	K=K+INI(J1/400)-INI(J1/1000) C=K-INI(K/7)*7	01/1000)			
		×				
50 'Suitable for Nascom 1/2 fitted with NASBUG 'T' or NAS-SYS monitors.	600 ° 610 END					
90 Modified for Gemini CP/M and MBASIC. 22 March 1981	44	ć.				
100 110 CLS\$=CHR\$(26): PRINT CLS\$;"Calendar"	Listing 1	0.84				
120 ** 0.4 ** 0.4 ** 0.4 **	To Load t	the IVC/SVC Cursor register	or register	MACRO-80 3.44	3.44 PAGE	~ -1
150 GeL 11purs 140 RESTORE: F=0: F1=0			.z80			
150 PRINT: PRINT "Do you require continuous output?";	,0000		a s eg	40010		
			org •phase			
80 IF I\$="Y" THEN FI=1: GOTO 190 ELSE IF I\$<\"" THEN 140	0018	9	500	15h		
200 PRINT "Out of range!": GOTO 190		í				
210	0000	90 90	14	p,4	; Four chars to send	send
220 ** Calculate starting day	C002	21 C011	ld fa	hl,chars a (0h2h)	. See if IVC/SVC	readv
250 J=1:6050B 490 240 J	C002	70		a, (0.02)) }	
250 ** Print calendar	8000	38 FB	jr	c,rdy		
FOR M=1 TO 12	C00A	7E	14	a,(hl)	; Get the character	ter
270 READ M\$: PRINT CLS\$: PRINT TAB(34-INT((LEN(M\$)+6)/2));M\$;" ";Y	2000	23 D3 R1	inc	hl (0blh).a		
PRINT" SUN MON TUE WED THU FRI	COOE	10 F5	djnz	rdy		
300 PRINT" statement the part of May with it may with invaria	0100	60	ret			
	C011	1B 59 48 08 cl	08 chars: defb	esc,"Y",48h,8h	,8h ; Cursor control	
			7 !!			
340 PKINI D;: C=C+1: IF C=7 THEN PRINT: PRINT: PRINT: C=0 350 NRXT D			eug			
3/0 PKINI memanananananananananananananananananana	Listing Three	Three				
390 PRINT: PRINT TAB(42) "Hit any key to continue." 400 I\$=INKEY\$: IF I\$="" THEN 400 ELSE IF I\$=CHR\$(3) THEN END	GM816 CL	GM816 Clock Read Routine		MACRO-80 3.44	09-Dec-82 PAGE	1
410 NEXT M: GOTO 140 420 ´			.z80			
430 *** Data for months and days 440 DATA "JANUARY", 31, "FEBRUARY", 28, "MARCH", 31, "APRIL", 30, "MAY", 31, "JUNE", 30	,0000			100h		
450 DATA "JULY", 31, "AUGUST", 31, "SEPTEMBER", 30, "OCTOBER", 31, "NOVEMBER", 30 460 DATA "DECEMBER", 31			•phase	0c000h		
470 $^{\prime}$ ** Is it a leap year? F=1 says yes.	0020 000B	clock nmrreg	ck equ reg equ	20h 11	; Base port of clock ; Number of regs to	of clock regs to read
490 IF J/1000=INI(J/1000) THEN 550 500 IF J/400=INI(J/400) THEN F=1: GOTO 550	0000	C3 C014	đ	start		
510 IF J/100=INT(J/100) THEN 550 520 IF J/4=INT(J/4) THEN F=I ELSE GOTO 550	6003 C009	regs:		6 11	; Workspace for results ; Workspace for registers	for results for registers
530						

Listing Four	* Get the time and display on SVC	CALL	IF FEEK(S:C1me+b)<=9 STARE "O"+STR(PREK(s:time+6).1) TO hr	ELSE	STORE STR(PEEK(s:time+6),2) TO hr	ENDIF	IF PEEK(s:time+/)< 9	STORE "0"+STR(PEEK(s:time+7),1) TO min	ELSE	STORE STR(PEEK(s:time+7),2) TO min	ENDIF	IF PEEK(s:time+8)<=9	STORE '0'+STR(PEEK(s:time+8),1) TO sec	TSTE	STORE SIR(PEEK(S:time+8),2) TO sec	ENDIF STODE CHR(22)+"+"+h++min+so++CHR(22)+"+F"" TO ++	? tt		STORE "JanFebMarAprMayJunJlyAugSepOctNovDec" TO m.		STORE hr+"."+min TO hm		\$(d,3*PEEK(s:time+4)-2	STORE "Log on time "+hm+" "+d+" "+STR(PEEK(s:time+5),2)+" "+m TO s:logon1				Listing Five.			POKE 49167, 62, 255, 211, 30, 62, 255, 211, 30, 201, 62, 255, 211, 31, 62, 255, 211, 31	POKE 49184, 201, 62, 255, 211, 29, 62, 255, 211, 31, 62, 16, 211, 31, 201, 237, 81	POKE 49200, 237, 89, 219, 28, 237, 81, 201, 205, 33, 192, 205, 15, 192, 33, 3, 192						POKE 49312, 201, 62, 255, 211, 29, 62, 255, 211, 31, 62, 16, 211, 31, 201, 237, 81	POKE 49328, 237, 89, 219, 28, 237, 81, 201, 205, 33, 192, 205, 15, 192, 33, 3, 192	POKE 49344,6,12,22,236,30,140,14,29,205,46,192,230,15,254,15,40		-	FOKE 49392,55,192,33,3,192,84,93,205,88,192,237,160,6
; Read into workspace					read and convert into HEX												; Exit, all done	HEX	: Do the low byte	226	Save it in C	: Do the high byte			; Multiply by 10	•					: Add low to high	Save it		; Clear any Z flag	•		ige flag	ı		: Mask off ton nibble				
Read into	hl,regs	υ	2001	IIZ) I eau	any changed during read and convert into	b,4	hl,regs	de,regs-1	scanl			test	z,start	(de),a	de	scanl	; Exit, all	ytes and convert to HEX	: Do the 1	2	•		2		a ; Multiply	•	œ	w	್ಯ ಹ	bc	: Add low	,a : Save it		: Clear any	•		e and test the change flag	a,(h1)	h		: Test for			
l ; Read into	1q		ini ir no read		convert into				_			-				call scanl	; Exit, all	; Take two bytes and convert to HEX	call test : Do the 1	ret 2	e c	T test :	•	push bc	a ; Multiply	สู้บ			add a,c		a,c ; Add low	(de),a : Save it	de	a ; Clear any	•	ret	e a byte and test	ld a,(h1)	inc hl	Ofh : Mask off	ofh : Test for			end
1d c,clock+1; Read into	PT 6000	read: inc		TD JI	any changed during read and convert into	Id	C009 14	C008 1d	CO3C scan: call	E7 jr	F9 djnz	C055 call	EO jr		dec		ret ', Stait ; Exit, all	two bytes and	call test : Do the 1	ret 2		CO55 call test	N	ysnd	27 sla a ; Multiply	1d c,a	27 sla		add	dod	add a,c ; Add low	ld (de),a : Save it	de	xor a ; Clear any	dec a		; Take a byte and test	test: ld a,(hl)	inc	OF and Ofh : Mask off	co Ofh : Test for	ret		end

Ok, now on to clocks and dBASE. The same process is used, just the programs are different. Firstly the clock call routine for the GM816, this is likely to be the more popular. I don't include any utilities for setting the clock as both the GM816 and the GM822 are sufficiently reliable to only require setting every now and then by separate utilities described in their respective manuals.

See Listing Three

This lot comes down to a neat and tidy little piece so:

STORE 49152 TO s:time SET CALL TO s:time

POKE 49152,195,20,192
POKE 49172,14,33,6,11,33,9,192,12,237,162,32,251,6,4,33,9
POKE 49188,192,17,8,192,205,60,192,40,231,16,249,205,85,192,40,224
POKE 49204,18,27,205,60,192,40,217,201,205,85,192,200,79,205,85,192
POKE 49220,200,197,203,39,79,203,39,203,39,129,193,129,18,27,175,61
POKE 49236,201,126,35,230,15,254,15,201

Note that in this routine the 11 registers are first read into an 11 byte workspace, the results are then converted from the decimal one byte per digit into HEX numbers in a second workspace, as dBASE requires the numbers stored in HEX. It is then a simple matter of PEEKing the workspace to extract the time and date. The order is thus:

s:time+3 = month
s:time+4 = day of week
s:time+5 = day of month
s:time+6 = hours
s:time+7 = minutes
s:time+8 = seconds

Listing Four is an extract from my radio logbook program which firstly shoves the correct time at the SVC and then picks up a logon string for later use. This is for the GM816. The same is true for the GM822 hung on a PIO device. The routine is quite a bit larger, but the output format is the same. In this instance the port decode was lch - lfh, if you want it any different, then you can unscramble it and disassemble it yourself. See Listing Five.

Naturally these routines could be used with any high level language which has the ability to PEEK and POKE and to CALL user subroutines. The principles are the same regardless, but care should be taken as to where they are put as some parts of programs could crash into them if they are located at c000h, or worse, they could be moved by the program itself.

The values of \$TAB, \$OUT, \$UOUT, \$IN, \$UIN and \$NMI are variously initialized by Nas-Sys 3, ROM BASIC, PolyDos and PolyDos Disc Basic. A table of these values might save some head scratching when incorporating user routines or patches.

Fetr	n Wkspc. Add.	Nas-Sys 3	ROM BASIC	FolyDos	Disc Basic
\$TAE	3 0071 (3185)	0700 (1792)	0700 (1792)	C07E (-16258)	C07E (-16258)
\$0U1 \$UOU		0779 (1913) 002F (47)		0779 (1913) C240 (-15808)	B138 (-20168) B13A (-20166)
*IN	0C75 (3189) 0C7B (3195)	077C (1916) 002F (47)		D416 (-11242) 002F (47)	D416 (-11242) 002F (47)
\$NM	C 0C7E (3198)	0475 (1141)	FEDE (-290)	unaltered	(see note *)

* The byte at OC7D is set to £C3 (jump) by Nas-Sys initialization. OC7E/F is set to 0475 by Nas-Sys PARSE calling INLS at 02E8 each time. Therefore if on power up neither Nas-Sys or ROM BASIC is implemented then state of OC7E/F is indeterminate. If Nas-Sys STMON is called (as by PolyDos) but Nas-Sys command input is not used then the byte £C3 is set but not the subsequent address.

Note that FolyDos copies out the routine table STABA to its workspace. The base is CO7E and the table actually begins at C100. Within this table the addresses of MRET, CRT, NNIM, and BLINK are altered. RKBD, SP2 and SCALI are altered as these routines are written into FolyDos so as to make it compatible with Nas-Sys 1.

Note further that PolyDos Disc Basic extension to ROM basic once again alters the address of MRET and also alters the address of INLIN.

These changes are tabulated below. (decimal values in brackets)

Routine	Nas-Sys	adon hadir brok danin dalah dalah papa sapa paka paka bada bagi para bidan kada saba bada kada paba k	tion times among their makes below below below below below below to be the below below to be the below to be t	
	address	address	table pos'n	Basic add.
MRET	03FE (1022)	D09D (-12140)	C134 (-16076)	B079 (-20359)
CRT	0190 (400)	D3C7 (-11321)	C148 (-16056)	
MINN	0742 (1858)	D410 (-11248)	C16E (-16018)	
BLINK	0078 (120)	D419 (-11239)	C174 (-16012)	
RKBD	0082 (142)	D481 (-11135)	C178 (-16008)	
SP2	0362 (866)	D504 (-11004)	C17A (-16006)	
SCALI	0585 (1461)	D509 (-10999)	C17C (-16004)	
INLIN	02F0 (752)	not altered	C144 (-16060)	BDEC (-16916)

Since PolyDos keeps STABA in RAM then a routine to trap carriage returns before the CRT routine, such as shown above, can be "patched in" rather than written as an user routine. It is only necessary to alter the address at C148 (-16056), normally D3C7 (-11321) to the "patch" address and end the "patch" routine with a jump to CRT (C3 C7 D3).

POLYDOS FILE NAME LISTING

This program is for making a listing on a printer of all the file names on a Gemini GM809/GM815 system with Polydos 2.0. The output consists of a listing of file names as they appear on the disks and a sorted list of file names, as well as a usage summary of all the user disks owned. For this to work properly the user must have some way of identifying his disks, I have decided to use the last two digits of the twenty digit disk name (see the FORMAT utility or the NAME command).

Another way of obtaining a list is to use the DIR; ELD command but this takes a long time for a large number of disks and does not give a sorted listing and a summary. With only a few disks it is quite easy to remember where things are kept, this becomes increasingly more difficult as the number of disks increases. (Also I have a remarkably bad memory).

The following is a sample output from running this program against two disks the first disk has an identifier D2 and the second B2:-

DISK INDEX LISTING

RHEX Z2 B2
PECOPY GO B2
PECOPY Z2 B2
DATE GO B2
DATE Z2 B2
1P Z2 B2
MP GO B2
8
E

DISK INDEX LISTING

ACCOU ACCTD CHECK CHECK CHECK	ATA SUM SUM	BS DT GO Z2 GO	D2 D2 B2 B2 B2	DIRFILE DUMP DUMP EDIT1 EDIT2	TX GO Z2 BS BS	D2 B2 B2 D2 D2	MEMTEST1 MEMTEST2 MEMTEST2 MEMTEST3 MEMTEST3	GO Z 2 GO	B2 B2 B2 B2 B2	SPAOLD TAPECOPY TAPECOPY TEST UPDATE		D2 B2 B2 D2 B2
CHRDI	S	Z_2	B2	EDITFILE	DT	D2	PAYROLL	BS	D2	UPDATE	Z2	В2
CHRHE	X	GO	B2	INDEX	BS	D2	SPA	BS	D2	VORTEX	GO	D2
CHRHE	X	Z_2	В2	INDXDATA	DT	D2	SPADATA	DT	D2			
DIRFI	LE	GO	D2	MEMTEST1	GO	B2	SPADATA	TX	D2			

DISK INDEX LISTING

DISK NAME		_		FILES		•	SE	CTORS	
			USED	DEL	FREE		USED	DEL	FREE
BASIC SYSTEMS	1	D2	32	0	18	•	646	0	614
UTILIES		B2	33	0	17	•	253	0	1007
			65	0	35	•	899	0	1621
			====	====	====	•	====	====	===

NUMBER OF DISKS ..:-

This program is split into two portions. Firstly there is a machine code program that gathers up the data and secondly a BASIC program that processes the output and sorts the data into order. The machine code section is loaded into RAM at f1000 and executed at f1000, the user is asked to load the disk into drive 0 and press enter, this reads into RAM the directory and is a very quick operation. When all the directorys of the disk have been read in then the user presses the `ESC' key, you are asked to insert into drive 0 the disk which will contain the completed directory file. This will be the disk containing the BASIC program. The BASIC program should then be run, it reads the file generated by the machine code programme, prints it out, sorts it into alphabetical order and prints it out again. After this it prints out the disk summary. The BASIC program is able to remove certain files name from the listings, for example all disks have the file `Exec' so there is no need to list it out. The user can change or add to lines 1260 - 1480 which is where all the unwanted file names are removed. The file produced by the machine code program can be viewed using the Polydos LIST option or changed using the EDIT facility (be careful as this may well stop the BASIC program operating correctly).

A word of warning; as the directory data is held in RAM then there must be a limit to how many disks can be used although I have used 40 disks without any trouble. Should this be a limitation then the job should be split up into sections and the BASIC program changed to merge several files together. Alternatively change the machine code program to remove unwanted files which would significantly reduce the ammount of data in RAM.

There follows a full Assembly listing including a sorted symbol table, also a dump of the program using a modified version of the disk dump published in Vol.1 issue 2 of 80-BUS NEWS (the numbers on the left hand side are the RAM locations). The BASIC program is also included.

PolyZap V2.2	ASSEMBLER	ER	PAGE 1	*
0000		*****	**************************************	* * *
0000				
0000				* .
0000		Σ * *.	M.J.K.GIBBS 11-12-82	* *
0000			*******************	***
0000			; NAS-SYS	COMMAND
0018	NASSYS	EQU	£18	
0900	ARGS	EQU	£60	
00/A	BIHEX	EQU	£/A	
0078	BZHEX RITNY	E C E	108 £78	
0070	CPOS	202	17 B	
006A	CRLF	EQU	£6A	
006B	ERRM	EQU	£6B	
005E	FFLP	EQU	£5E	
0063	INLIN	EQU	£63	
0062	KBD	EQU	£62	
005F	MFLP	5 E	1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1	
00.5R	MPFT	25	£3.F £5.B	
0064	MIN	EOU	1694	
6200	RLIN	EQU	£79	
0050	SCALJ	EQU	553	
0900	SOUT	EQU	£6D	
6900	SPACE	EQU	£69 	
0067	TBCD1	EQU	293	
9900	TBCD3	EQU	£66	
0020	TDEL	2	£ 5D	
0060	IXI	EQU	£6C	
00/1	E N	2 5	t/1	
2/00	NNOW	F 5	277	
0078	NNIM	EQU	£78	
0000			; OTHER	RESET COMMANDS
0020	BRKPT	EQU	£20	
0008	CHIN	EQU	£08	
0030	CRT	EQU	£30	
0078	PKS	2 6	£28	
0030	ROIT	ROII	£10 £30	
0008	RIN	EOU	£08	
0000			••	
0000			; CONTROL	CHARACTERS
0008	BACKSP	EOU	£08	
2000	CLEAR	EQU	500	
000D 001B	CRET	EQU	£0D £1B	

24

at i.

		; POLYDOS WORKAREA	•	DIRECTORY	:DRIVE CODE	; POWER UP FLAG			ΑK		; RAM BUFFER LENGTH	COMMAND FILE FLAG	COMMAND SECTOR ADDRESS	COMMAND SECTOR COUNTER	SECTOR	; KEYBOARD ROW	; KEYBOARD BIT MASK		-4	BLINK KUULINE FLAG		2	COMMAND LINE BUFFER	; OVERLAY FCB		; FILE NAME	Ž		æ			; EXEC ADDRESS		FLE NAME	Ä)R	S.		-	DISK I-O WORKSPACE		; DIRECTORY BUFFER	; :DIRECTORY BUFFER	; NEXT AVAILABLE SECTOR	;A(NEXT FCB)	
PAGE 3			£C000	,					7	2	···· .	⊣ -	ى 1	ı –	٠		-		7	٦,		1 6	78	10	\$>⊹	оо «	7 -		. 2	2	2	. 2	ςς · (æς.	7 -	•	5	2	2	2	6	1		£C400	£C414	£C416 £C418	
었			ORG	SO	8 8	S	DS	SC	DS	SO	DS	8 8	א מ מ	S 2	82	DS	DS	DS	So	So	S 5	3 2	SO	DS	EQU	S 2	S S	2 2	82	DS	DS	DS	EQU	2 2	מ צ	88	SO	DS	DS	SQ	8 8	3		EOII	EQU	EQU	
ASSEMBLER				MDRV	DDKV	FIRST	ERRFLG	ERRCOD	BREAK	BRAM	BNSC	CFFLG	CFUKV	CFNSC	CFSBP	RKROW	RKBIT	RKVAL	RKCNT	BLINKE	PLCE	CLIND	CLIN	OVFCB	SIFCB	FINAM	FIEXI	FIIIFI.	FISEC	FINSC	FILDA	FIEXA	SZFCB	FZNAM	FZEAL	FZUFL	F2SEC	F2NSC	F2LDA	F 2E XA	DSKWSP	70.00		DIRRIF	NXTSEC	NXTFCB FCBS	
PolyZap V2.2	0000	0000	0000	+	C001 + 0001	+	C004 + 0001	+	+	+	+	+ .	C000 + 0001	- +	+	+	+	+	+	+ -	COI/ + 000I	+ +	+	C04B + 000A		+ .	COSD + 0002	- +	+	+	+	c067 + 0002		8000 + 6900		+	+	+	+	+	CO7D + 0006	-	0000	C0C0	C414	C416 C418	
		••	; DISK COMMANDS	; DISK SIZE	; READ	WELTE PRAD DIRECTORY	AND TOWN THE WILL AND THE WORLD	CONVET FILE SPECIFIER	: UPDATE DIRECTORY	; CALL OVERLAY	; CALL OVERLAY RESTORE	; CHECK FOR ERRORS	; CHECK FOR BREAK	; ABORT COMMAND MODE	COOKUP FILE DIRECTORY		PRINTER OUTPUT									PAGE 02		u. 00 00 00 00 10 00	41 54 41 44 54 00												04		DISK INDEX LISTING"	兼部時間開發時報管理時間時期			
	ASSEMBLER PAGE 2			ZDSIZE EQU £80	EQU	ZBNT EQU 182	F 10	EOU	SR EQU	EQU		EQU	EQU	EQU	ZLUOK EQU E85	201	EOU	•								DIMP DIRECTORY PROGRAM		10 27 E8 03 64 00	00 00 49 4E 44 58	00 00 00 00 00 00 00											PAGE :-	s des sign ains deus dies dies des spec plas deus pas que pas pas pas pas des des des des des des des des des	PRINT CHR\$(4);" DISK				
	PolyZap V2.2	0000	0000	0080	0081	0082	0084	0085	0087	0088	6800	008A	0088	0080	0089	0000	008F									DIMP VI. O	•	C9 75	00 00	00											NASCOM BASIC LISTING		20000 PRINT (20010 PRINT (ZUUZU KEIUKN		

		٠			;FORCE DIR READ ;DIRECTORY DRIVE IN 0 ;READ DIRECTORY	;READ DIRECTORY ;ERROR TRY AGAIN ;CALC NUMBER OF SECTORS	;DE = A(STARI) ;HL = L(DATA)	; KEEP LENGTH ;HL = A(START) ;DE = A(NEXT FREE SECTR)	; LUALD FCB ; RECOVER NUMBER OF SECTS ; C = NUMBER OF SECTORS ; B = 0 ·1.0A D FCR	;B = NUMBER OF SECTORS ;C = DDRV ie 0 ;WRITE II OUT	;HL = A(FCB) ;TRY ENTER IN DIRECTORY		;FIND SAME NAME FCB ;IS IT LOCKED
PAGE 5		CRET		0 NASSYS BLINK	(DDRV),A A C,A	ZRDIR NZ, L20 IX	DE, OUTLOC A HL, DE	HL, OUTLOC DE, (NXTSEC)	(FXNSC), DE BC C, B B, O (FXNSC), BC	B, C C, O NASS YS ZDWR NASS YS	CCKEK HL, FXFCB NASSYS ZENTER Z.STOPIT	£31 Z,ENT20 NASSYS ZCKER HL HL,FXSFL-FXNAM	HL, DE 0, (HL)
ASSEMBLER		0.8 0.8	9.B	DB RST DB	XOR LD	DB DB JR PUSH	LD LD OR SBC	PUSH LD LD	20 P C C C C C C C C C C C C C C C C C C	LD LD RST DB RST	DB LD ENTER RST DB JR	CP JR ENT10 RST DB ENT20 PUSH LD	ADD BIT
PolyZap V2.2		10AC 682E 10AE 0D 10AF 20202020 10B3 20202020		100F 2D2D 10D1 00 10D2 DF 10D3 7B				10E8 E5 10E9 210018 10EC ED5B14C4			1102 0A 1103 219513 1106 DF 1107 87 1108 283F		1114 19 1115 CB46
	;LOAD OUTPUT AREA	; POINT TO USER OUTPUT	Load Disk in Drive O Press'	"Enter ".', CRET, CRET	To End the Programme Press'		0,	;IS ESCAPE	;NEXT DISK ;FLIP OUTPUT		; BACK TO NORMAL		Load File Insert disk.
PAGE 4	£1000 £1000,£1000 £1800 IX,OUTLOC	HL,OUTPUT (£OC78),HL PAGE PRS		"Enter",	To End		"Escape", ',	NASSYS BLINK ESC	2,ENDII DIR LOOP HL,OUTTAB NASSYS	NOM PRS CRET 'EOF', CRET, 0	NASSYS NNOM A, 0 B, 0 (1x+0) A	IX L10 PAGE PRS	Load File I
ASSEMBLER P	START ORG OUTLOC EQU	LD LD LOOP CALL RST	80	DB	DB		DB	RST DB CP	CALL JP JP ENDIT LD RST	DB RST DB DB	KSI DB LD LD		DB
PolyZap V2.2 AS	DD210018	1004 21/813 1007 22780C 100A CDDC12 LC 100D EF		1028 5020307.2 102A 657373 3 102D 2022456E 1031 74657220				1063 DF 1064 7B 1065 FEIB	2808 CD4B11 C30A10 218113 DF		1075 JF 107C J7 107D 3E00 107F 0600 1081 DD7700 L10	DD23 10F9 CDDC12 EF 20202020	1094 20202020 1098 4C6F6164

	;SETUP OUTPUT TABLES	CHANGE TO LOAD RAM	; SCAN																									vanta arra arrange	, booner Fire Denectors				: DELETED	;YES	; INCREMENT NUMBER FILES			; ADD NUMBER SECTORS				; INCREMENT NUMBER FILES	; DELETED	Sacardas ac daawiin day.		, Denetico			,			
PAGE 7	HL, OUTTAB	NOW NOW	B,£73	BC	PRS	Drive ,00	A, (DDRV)	Α, υ	CRT	PRS	١	0	B, 20	HL, DIRBUF	A, (HL)	CRT	H	DIR40	NASSYS	CRLF	HL, NUMFL	8,8	("H"), U	HL	D1K50	р, г./ 3 нт сожов	NASSVS	71 OOV	N7 DIBOO	טל אדט לאט	A (TOCT)	DE. (F2NSC)	1.A	NZ, DIR70	HL, NUMFL	(HL)	HL, (NUMSC)	HI, DE	(NUMSC), HL	DIR80	HL, NUMFLD	(HL)	UT (NIMECR)	ur ne	nr, ne	(NUMSCD), HL	E E	DIR60	HL, (NUMFL)	HL	PKTNUM	E NO
	LD	KS1	1	PUSH	RST	03	רם מיני	ADD	RST	RST	DB	DB	C	2	2	RST	INC	DJNZ	RST	80	2 :	3 5	ן דען	T INC	ZNCC	3 5	האם	Tey and	g e	DITCH	וייים ד	3 5	BIT	8	C C	INC	C	ADD	3	ĸ	r C	INC	6	מ ע	ADD	LD	POP	E E	C	PUSH	CALL	100
ASSEMBLER				DIR30											DIR40							0	ULKOO			חזמנת	DIKOD														DIR70						DIR80		DIR90	. **		
PolyZap V2.2	1	11.5 12.5 12.5 12.5 13.5 13.5 13.5 13.5 13.5 13.5 13.5 13								11AF EF							11BA 23	11BB 10FB					1104 3600		11C/ 10FB							,		11DC 200D	11DE 218D13	11E1 34						11EE 34	11EF 240212			11F3 229313		11F7 18D2		life E5	1200 FF	
		; YES STOP		<= OLD FILE DELETED =>									; DELETE OLD FILE	; RECOVER	;TRY AGAIN	; RECOVER HL		;CANNOT OVERWRITE LOCKED		; RET TO NASSYS	; JUMP OVER ERROR				Disk not Loaded Correctly											; GET REPLY	; KEEP REPLY			6	;IS ESC	ON:	antitudan.	induction of the contract of t	FORCE DIRECTORI READ			SAD	;C = DRIVE NUMBER		;READ DIRECTORY	,
PAGE 6	A, £33	NZ, ENT30	CRET									CRET, 0	1,(HL)	HL	ENTER	HL	NASSYS	ZCKER	NASSYS	MRET			PRS	CKET	Disk no							Try AGAIN'O			NASSYS			PRS	CREI, 0	AF	ESC	NZ, DIR20	FNDTT	TTONT V	(DORY) A	PRS PRS	CLEAR, 0	A,0	C,A	NASSYS	ZKDIR N7 DIB 10	Ma, Danio
ASSEMBLER	67	JR	DB	DB								DB	SET	POP		ENT30 POP	RST		STOPIT RST			DIRIO CALL	KSI	90	80							DB			RST	DB	PUSH	RST	DB	POP	d C	JR	10r	10 00 atu	UT OFWEG	RST	DB	CD LD	ľΩ	RST	25 E	3
PolyZap V2.2		1119 202B										113F 0D00											1150 EF		1152 20202020						_			1178 494E00								1184 2004								1195 DF	1196 83	

					1.00KHP FILE DIRECTORY						; PRINT 4 PARMS										!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!	; DELETED FILE			; LOCKED FILE								;NORMAL I-O		٠				
PAGE 9	i I I I I	CRET, 00	BC C,£OB	HL, SIFCB	NASSYS ZLOOK	NZ, DIR150	BC	HI. 12	HL, DE	B, 4	E, (HL)	D,(HL)	HL	DE, HL	TBCD3	DE, HL	DIR110	DE,-10	A.,, oc.	1, (HL)	Z, DIR120	A, D.	0,(HL)	Z, DIR130	A, L	NASSYS	SPACE	JE RC	LSTFIL	NASSYS	CRLF	NASSYS	NNOW						
ASSEMBLER		DB	ro. ro	DIR100 LD	RST DB	JR	PUSH	FUSH T.D	ADD		DIKIIO LD	CD CD	INC	EX	DB	EX	DJNZ	LD	TD	BIT	JR.	G ₩	DIR120 BIT	JR	LD DIP130 PST		DB	POP	CALL	DIR140 RST	0B 10	DIR150 RST		RET					
PolyZap V2.2	128D 61642045 1291 78656320 1295 46204E61				12A3 DF 12A4 86		12A7 C5		12AC 19		1280 23			1283 EB 1284 DF				1289 11F6FF 12RC 19			12C1 2804		12C7 CB46	1209 2802	12CB 3E4C		12CF 69	1200 01 1201 C1			1206 6A 1207 1867	1209 DF		12DB C9					
R PAGE 8	DB 'Files used,',00	LD HL, (NUMFLD)		RST PRS	'nereren	DAD UT	FOF NL POP DE		JH, HC	LD HL, 50 ;TOTAL NUMBER OF FILES		د	RST PRS				ADD HL, DE DIISH HI		r prs	DB Sectors used, ',00				FUSH HL				POP HL		_	LD A, (DDRV)		r NASSYS	DB ZDSIZE ;DISK SIZE OR A		CALL PRINUM RST PRS		DB 'Sect Nsec Load Exec F Name'	
ASSEMBLER	90	17	CA	78.0 10.0	ď	Od	PO	AI	EX	3 6	SE	CA	S S		רם	11	AL	CA	RS	DB			5 7	n d	RS RS	80		PO	PO	AD	LD	CD	RS	88 88	SB	SS SS	DB	DB	
PolyZap V2.2	1201 2046696C 1205 65732020 1209 20757365 1200 642C2000	1211 2A8F13		1218 EF 1219 20446560		1221 2C2000			1227 EB	1228 213200 1228 B7		122E CD4F13	1231 EF 1232 20467265			123D 110400				1246 20536563			1256 2A9313	1259 65 1254 CDAF13			1262 65746564 1266 202000			1268 19 1266 FB		1270 4F			1274 ED52	1279 EF	127A 20467265	1281 53656374 1285 204E7365	1289 63204C6F

j:

	; PRINT NO DECIMAL		;MUST PRINT LAST 0 ;REPLACE WITH BLANK ;PRINT IT ;NEXT DIGIT	; OUTPUT LOAD RAM	
PAGE 11	A, 0, CRT BC, £0400 DE, TENS+2 DE (SP), HL E, (HL) HL	HL (SP), HL A A HL, DE NC, PRTN20 HL, DE A NZ, PRTN30 C C	C NZ, PRTN30 A,B A Z, PRTN30 A,' CRT CRT	C A, O C C A, O C C C C C C C C C C C C C C C C C C	
ASSEMBLER	ADD RST RET PRTNUM LD LD PUSH PRTNIO EX LD LD LD LD	INC EX EX XOR XOR SBC JR ADD DEC JR INC		PRINGO DEC ADD AND RST POP RET OUTPUT LD INC RET OUTTAB DB	
PolyZap V2.2	134B C630 134B F7 134E C9 134F 010004 1352 118513 1355 B5 1356 E3 1358 23	135A 23 135B E3 135C AF 135D 3C 135E ED52 1362 19 1363 3D 1364 200D		13/3 UD 13/4 C630 1376 F7 1377 10DD 1379 D1 1378 DD7700 1378 DD23 1380 C9 1381 7500	
	; PAGE HEADINGS Disk Directory File Load		CRET, CRET, CRET, 0	; LIST FILE	; PRINT DRIVE NO
PAGE 10	PRS CLEAR	CRET	CRET, CRET	DE BC BC BC, EOAOO A, (DE) DE C C C A, B A, C C C C C C C C C C C C C C C C C C C	A,':' CRT A,(DDRV)
ASSEMBLER P	PAGE RST DB DB	80 80	DB	LSTFIL PUSH PUSH LD LD LST10 LD INC CP RST INC CP CP RST INC LD CP CP RST INC LD CP	PRIDRV LD RSI LD
PolyZap V2.2	12DC EF 12DD OC 12DE 20202020 12E2 20202020 12E6 20202044 12EA 69736B20 12E7 64697265 12F2 63746F72 12F3 65520469		1322 303030 1325 00000000 1329 00 132A C9	1328 D5 132C C5 132D 01000A 133D 1A 1331 13 1332 FE2O 1334 F7 1335 OC 1336 78 1339 7 1339 CO4 1339 F7 1330 F7 1341 79 1341 79 1342 C1 1344 C9	1345 3E3A 1347 F7 1348 3A01C0

PAGE 01	• 66 85 •	Load F11 Load F11	Loaded Correctly Try AGAIN	• • •	* * *	* -	# O P		.sect nsec Load Exec F Name	£V£f> . N(.>DF(.>Li	Disk Di	d. decoly file Load	# # · · · · · · · · · · · · · · · · · ·	
0 DUMP DIRECTORY PROGRAM	21 00 18 21 7B 13 22 78 20 20 4C 6F 61 64 20 44 72 69 76 65 20 30 20 50 74 65 72 20 22 2E 0D 0D 45 6E 64 20 74 68 65 20 65 20 50 72 65 73 73 20 65 20 0D 78 FE 18 28 06 13 DF 71 EF 0D 45 4F 46 DD 77 00 DD 23 10 F9 CD	20 20 20 20 20 20 20 20 4C 6F 61 64 20 46 69 20 49 6E 73 65 72 74 20 64 69 73 6B 2E 00 20 20 20 20 20 20 20 20 20 20 20 20	6F 61 64 65 64 20 43 6F 72 72 65 63 74 6C 54 72 79 20 41 47 41 49 4E 00 DF 78 F5 EF	F1 FE 1B 20 04 F1 C3 6F 10 3E FF 32 01 C0 00 3E 00 4F DF 83 20 B4 21 81 13 DF 71 06 EF 44 72 69 76 65 20 00 3A 01 C0 C6 30 F7 20 00 06 14 21 00 C4 7E F7 23 10 FB DF 6A	13 06 08 36 00 23 10 FB 06 73 21 69 C0 DF 27 D5 3A 73 C0 ED 5B 77 C0 CB 4F 20 0D 21 34 2A 91 13 19 22 91 13 18 0B 21 BF 13 34 13 19 22 93 13 19 19 22 93 13 19 19 22 93 13 19 19 25 93 19 19 19 19 19 19 19 19 19 19 19 19 19	20 46 95 50 51 10 22 28 30 13 53 50 47 20 46 87 13 E5 CD 4F 13 EF 20 44 55 66 55 74 20 20 20 00 E1 D1 19 EB 21 32 00 B7 ED 52 CD	EF 20 46 72 65 65 2E 0D 00 2A 91 13 11 04 E5 CD 4F 13 EF 20 53 65 63 74 6F 72 73 20 65 64 2C 20 00 2A 93 13 E5 CD 4F 13 EF 20	6C 65 74 65 64 2C 20 00 E1 D1 19 EB 3A 01 DF 80 B7 ED 52 CD 4F 13 EF 20 46 72 65 65 53 65 63 74 20 48 73 65 63 30 42 65 61	65 63 20 46 20 4E 61 6D 65 0D 00 C1 0E C0 DF 86 20 32 C5 D5 21 0C 00 19 06 04	56 23 EB DF 66 EB 10 F6 11 F6 FF 19 3E 20 28 04 3E 44 18 06 CB 46 28 02 3E 4C F7 DF	20 20 20 20 65 63 74 6F	00 20 20 20 20 20 20 20 20 20 20 20 20 30 30 30 30 30 30 30 30 30 30 30 30 30	30 30 30 30 00 00 00 00 00 09 05 C5 01 00 13 FE 20 F7 0C 78 FE 03 20 04 3E 2E F7 0C	79 C1 D1 C9 3E 3A F7 3A 01 C0 C6 30 F7 C9 01 O4 11 85 13 D5 E3 5E 23 56 23 E3 AF 3C ED 52 FB 19 3D 20 0D 0C 0D 20 09 78 3D 28 05 3E 20 18 04 0D C6 30 F7 10 DD D1 C9 DD 77 00 DD 23
DUMP VI.0	1000 DD 1010 20 1020 44 1030 6E 1040 20 1050 6D 1060 22 1060 22			1180 00 1190 0C 11A0 C5 11B0 3A	11C0 8D 11D0 20 11E0 13			1260 65 1270 4F 1280 OD		1280 23 12C0 4E		1300 64 1310 3D	1320 3D 1330 1A	1340 EF 1350 00 1360 30 1370 F7
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	·			BLINK 007B BRKPT 0020 CFSEC C00D	CPOS 007C DIR 114B DIR130 12CD DIR40 11R8	ניז	F1LDA C065 F1UFL C060 F2NSC C077	FFLP 005E FXLDA 13A5 FXHFT, 13A0		NNIM 0078 NUMFLD 138F	PLCT C017 PRINIO 1356		LJ WSP	IXI 006C ZCOV 0088 ZENTER 0087 ZWDIR 0084
en 1					C019 CPOS C001 DIR 12C7 DIR			C418 FF 1395 FX 139F FX		138D NU		134F RC, C011 RK		1383 TAL 008A ZCO 0082 ZEN 0083 ZWD
				<u>ρ</u> ,	CLINP C DDRV C DIR120 1	_	Eu . 3 54	FCBS C FXFCB 1 FXSFL 1		NIM NUMFL 1			SZFCB C STOPIT 1	~: ~:
	. 0	INDXDATA		0068 C008 C00F	COIB 0030 12AF 118A		C067 C061 C079	C074 139D 13A1		0064 0064 0416	C04B 0028	1377 C014	C055 1000	
PAGE 12	10000 10000 1000 100 10 0 0	\$ \IND \DT\ 0 0 0 0 0	PAGE 13	BZHEX BRAM CFNSC	CLIN CRT DIR110 DIR20	DIR70 DSKWSP ENTER	F1EXA F1SEC F2LDA	F2UFL FXEXT FXSEC	L10 LSTFIL	NASS IS NUM NXTFCR	OVFCB	PRTN40 RKCNT	SIFCB START TDEI	ZCFS ZDSIZE ZLOOK
				007A C00A C00B	000C 006A 12A0 12D9	11CB C002 11146	001B C063 C071	C073 13A7 13A3	0062 133F	0071 1393	1381 C018	1373 C012	0030	E 3. C
BLER		EQU DB DB DB DB DB DB DB DW DW DW DW DW	BLER	222			555	O	0 (75	0030	008C 008L 008L
ASSEMBLER	TENS DW DW NUMFL DW NUMFL DW NUMSC DW NUMSCD DW	FXFCB EQU FXNAM DB FXSFL DB FXSFL DB FXUFL DB FXNFC DW FXNSC DW FXLDA DW FXEDA DW	ASSEMBLER	1 1	CLEAR 00 CRLF 00 DIR100 12 DIR150 12	00.0		FZSFL C FXEDA 1 FXNSC 1	KBD CLST20 1	6		30	SPACE 000	

of disk directorys and remove the common file names and produce a		2100 PRINT RIGHT\$(" "+STR\$(DA(0, 4)),6);
Print out of all files in disk order and also print a sorted list.		PRINT RICHTS(" "FSIKS(DAC) PRINT RICHTS(" "+STRS(DAC) PRINT SPC(27); PRINT "==== "==== "; PRINT "==== "==== ";
M.J.R.GIBBS 2/11/82		PRINT SPC(5);"NUME PRINT RIGHTS("
DIM ND\$(50), DA(50,6) ND = 1:REM NUMBER OF DISKS	1630 DA(ND, 6) = VAL(MID\$(ZZ\$,34,4)) 1640 ND = ND + 1 1650 SETINP(1).ZZ\$	2180 CLS 2190 SETPROFF 2200 END
SCREEN 12,16 PRINT "DISK FILES INDEX PROGRAMME"		REM SHELL SORT REM
SCREEN 12,1 PRINT "memmanananananananananananananananananan	1680 REM NOW PRINT IT ALL OUT AND SORT 1690 SETCLS(1)	8020 SCREEN 7, 8:PRINT "SHELL - METZNER SORT " 8030 REM 8040 SN = T-1
DIM AS\$(1000)	CLS	SS
SETNEW (1), "INDXDATA" FOR I = 1 TO 1000	PRINTINPUT	SS = INI(SS/Z) IF SS = 0 THEN RETURN
SCREEN 12,8 PRINT "Reading record no:-":1	1740 GOSUB 9000 1750 GOSUB 8000	8080 SCREEN 28, 8:PRINT "BLKSIZE :=";5S;" "; 8090 SZ = SN-SS
	1760 GOSUB 9000	8100 FOR SM = 1 TO SZ 8110 ST = SM
If LEN(ASS(1)) = 0 GOLO 1210 IF "EOF" = MID\$(AS\$(I),1,3) THEN 1680		SST = SI+SS
IF "Drive" = MID\$(AS\$(I),1,5) THEN 1540 NNS = MID\$(AS\$(I),23, 8)	1790 PRINT 1800 PRINT SPC(5);"DISK NAME ";	8130 IF AS\$(SI) <= AS\$(SJ) THEN 81/U 8140 SH\$=AS\$(SI):AS\$(SI)=AS\$(SJ):AS\$(SJ)=SH\$
= "Exec " THEN 1210	1810 PRINT " FILES";	8150 SI = SI-SS 8160 IF SI > 0 THEN 8120
" THEN	PRINT SPC(25);	
"Ecmd " THEN 1210 "Edit " THEN 1210	1840 PRINT " USED DEL FREE . "; 1850 PRINT " USED DEL FREE"	8180 GOTO 8060 9000 SETPRON
" THEN	ND = ND-1	9010 AS\$(I) ="""
"BSfh " THEN 1210 "Bsfh " THEN 1210	1870 FOR I = 1 TO ND 1880 PRINT SPC(5):ND\$(1):	9020 IN = INT((I-1)/200)+1 9030 FOR II = 1 TO IN
THEN	PRINT RIGHTS(" "+STR\$(DA(I,	SI
"BSut " THEN 1210 "BSdr " THEN 1210	1900 PRINT RIGHT\$(" "+STR\$(DA(I, 2)),6); 1910 PRINT RIGHT\$(" "+STR\$(DA(I, 3)),6);	9050 IE = IS+200 9060 IF IE \rangle = I-1 THEN IE = I+4
" T	PRINT " ";	9070 GOSUB 10000
THEN	+SIR\$(DA(9090 SETPROFF
" THEN	PRINT RIGHT\$(" "+STR\$(DA(I,	9100 RETURN
"Init " THEN 1210 "PSfh " THEN 1210	1960 NEXT I 1970 PRINT SPC(27):	$10000 \ \text{GOSOB} \ 20000 \ 10010 \ \text{L} = \text{INT}((\text{IE-IS})/4)$
" THEN	PRINT "	IE = IS+4*L
"DPfh " THEN 1210 "Info " THEN 1210	1990 PRINT ""	10030 IF $K > 200$ THEN $K = 200$ 10040 PRINT
" THEN	FOR $J = 1$ TO	
= "DISKPEN " THEN 1210 = "MASPAS" " THEN 1210	2020 DA(0, 1) = DA(0, 1) + DA(1, 1)	10060 PRINT AS\$(J);" "; 10070 PRINT AS\$(J + L);" ";
= MD\$(AS\$(I),32,2) = MTD\$(AS\$(I),32,2)		PRINT
DOST (LIEDY, ROST L.) ***********************************	PRINT RIGHTS(" PRINT RIGHTS("	
GOTO 1670	D PRINT RIGHT\$(" "+STR\$(DA(10120 CLS

for

MONITORS

COTRON SWORD

Cotron's progressive development of high technology TV monitors for professional users, has enabled them to produce computer monitors that few other manufacturers can match in both quality and price.

With the introduction of the Sword range, a new dimension is offered to micro users. All the Sword monitors incorporate 14" 'Black Glass' tubes, producing very high contrast with bright clean colours.

All Sword monitors will accept both TTL and analog input signals, via a 25 pin 'D' type connector.

FEATURES INCLUDE:

14" RGB MONITOR * TTL & ANALOGUE *
18MHz BANDWIDTH * INFINITE COLOUR
PALETTE * PRESTIGE CASE * BRITISH
DESIGN & MANUFACTURE * OPTIONAL
TILT & SWIVEL BASE * STANDARD OR
LONG PERSISTANCE

SABRE
The Sabre is a medium resolution monitor that has a horizontal resolution of 650 pixels and a dot pitch of .40mm, bandwidth is 18MHz.

 SABRE-15
 £ 455,00

 SABRE-1L
 £ 480,00

 SABRE-2S
 £ 542,00

 SABRE-2L
 £ 570,00

RAPIER

The Rapier is a high resolution monitor with a horizontal resolution of 850 pixels and a dot pitch of .31mm, bandwidth is 18MHz.

 RAPIER-1S
 £ 550,00

 RAPIER-1L
 £ 575,00

 RAPIER-2S
 £ 628,00

 RAPIER-2L
 £ 650,00

CABLES

Cable - PLUTO mini pallette £ 32,00

K E Y

(1) No Stand (2) With tilt & swivel stand

We think you will agree that

Cotron's Sword range itechnology at it's best.

- is British (S) Standard persistance
- (L) Long persistance

MICROVITEC

C U B 4 5 2

The CUB-452 is the standard resolution option from the popular Microvitec CUB range of 14" colour monitors. This model offers 452 x 585 addressable pixels.

This monitor has an RGB type input avalable as either TTL or linear.
A special version (MZ) is also avalable for the Sinclair Spectrum computer.

The (AP) version of this monitor is designed for use with a video recorder as well as a computer and as such is equipped with both PAL and AUDIO inputs.

 1431MS
 (Metal Case)
 £ 199.00

 1431LS
 (Structural Foam)
 £ 249.00

 1431MZ
 (TTL + Spectrum)
 £ 225.00

 1431AP
 (TTL + PAL + Audio)
 £ 225.00

C U B 653

The CUB-653 is the medium resolution option in the Microvitec 14" range. This model offers 653 x 585 addressable pixels.

Avalable in both TTL and Linear versions the CUB-653 provides the ideal specification for the majority of micro's with high resolution colour and 80 column displays.

C U B 895

The CUB-895 is the high resolution option in the Microvitec 14" range.

This model offers 895 x 585 addressable pixels and is ideal for those applications requiring very high clarity and precise colour reproduction.

 1441MS
 (Metal Case)
 £ 440.00

 1441LS
 (Structural Foam)
 £ 450.00

 1446LI
 (IBM/Programmed Rom)
 £ 495.00

PHILIPS

CT2007 TV/MON

The Philips CT2007 is a 14" colour TV reciver/monitor with inputs for both R.G.B. and C.V.B.S. as well as audio. Bandwidth is 20MHz and the display is standard resolution.

CT2007

SANYO

C R T 7 0

The Sanyo CRT70 is a 14" high resolution monitor in an attractive silver alloy finish. Resolution is 800 pixels and the input is R.G.B.

0.664 3