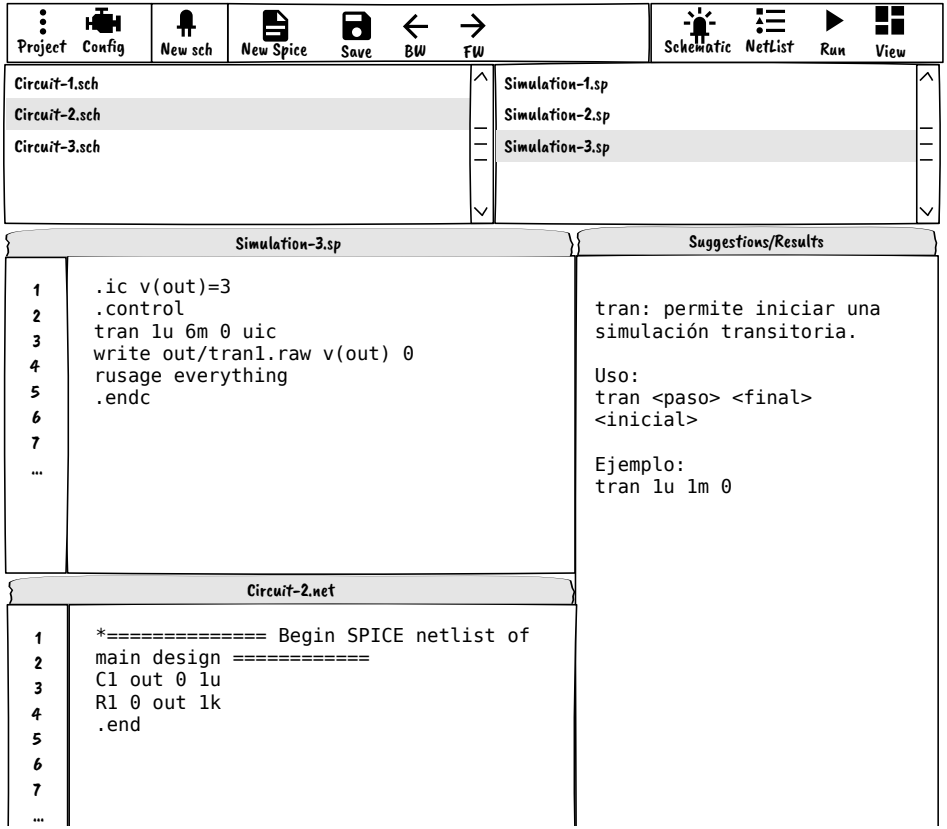
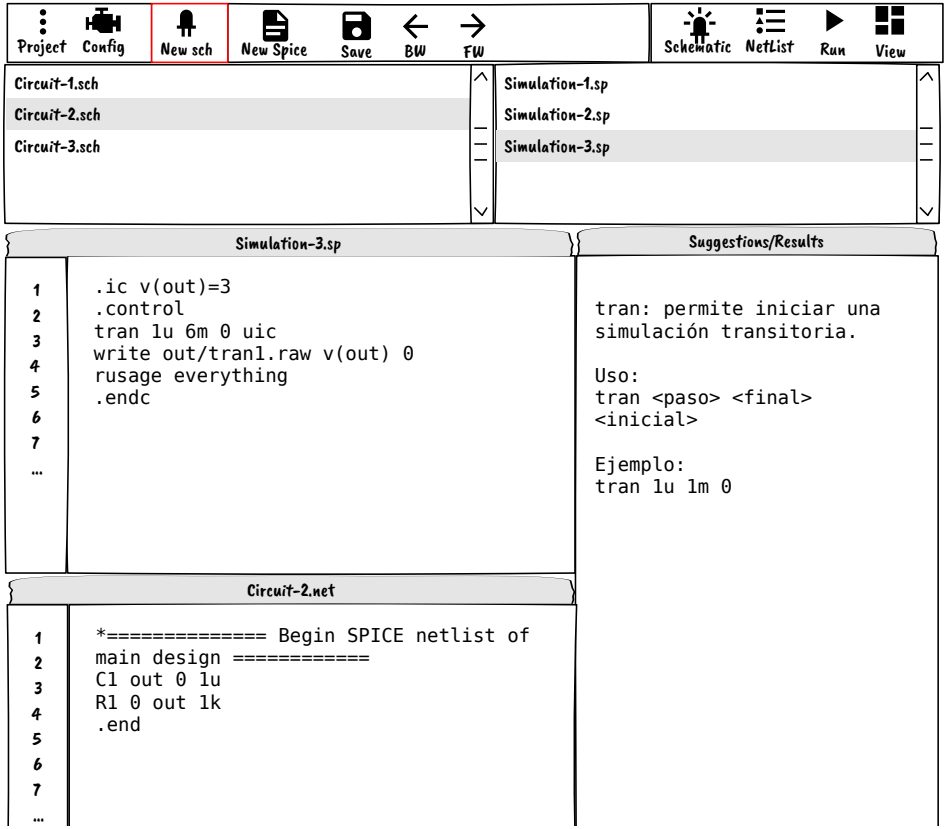
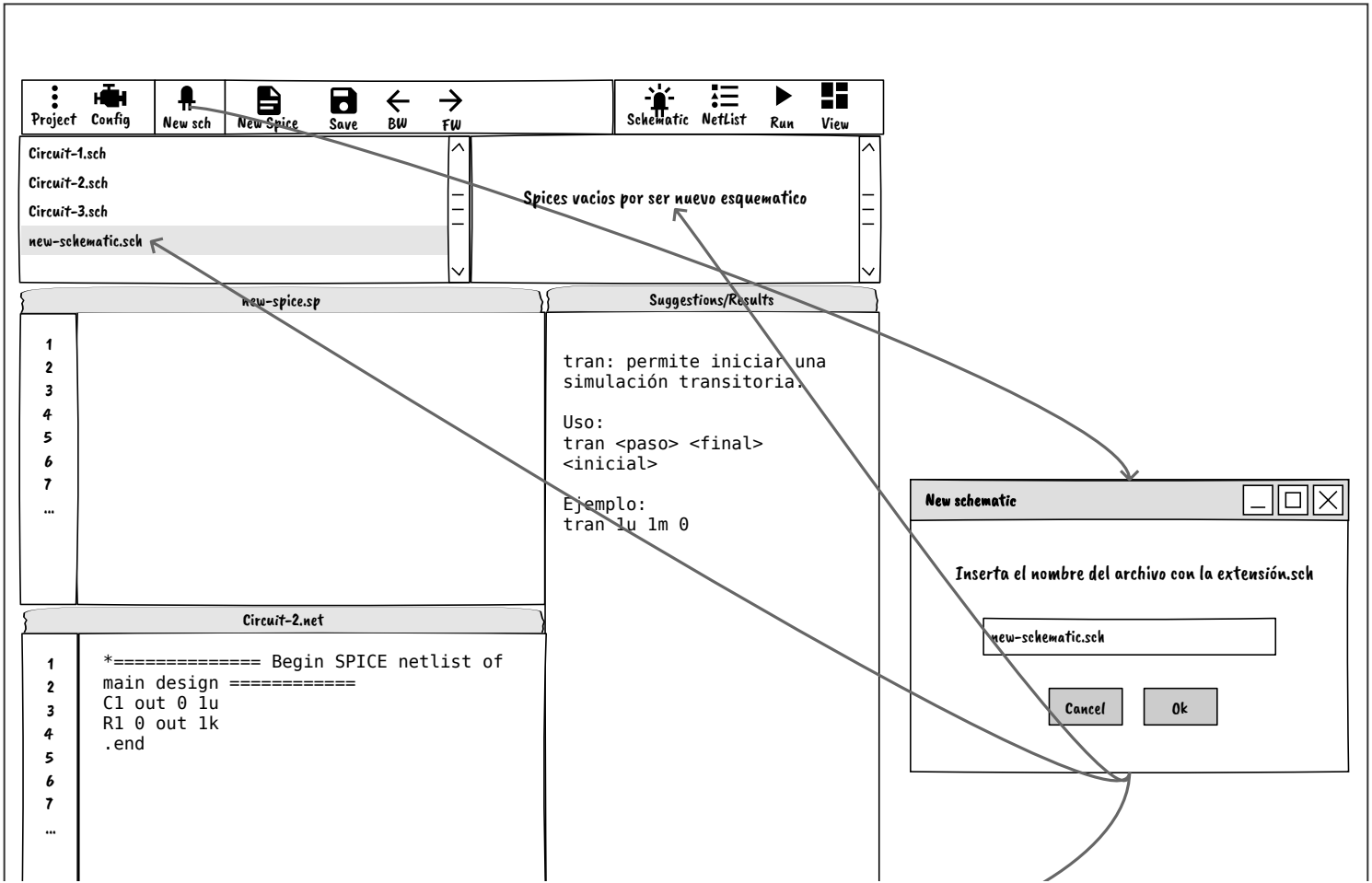
General


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una
simulación transitoria.
Uso:
tran <paso> <final>
<inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end

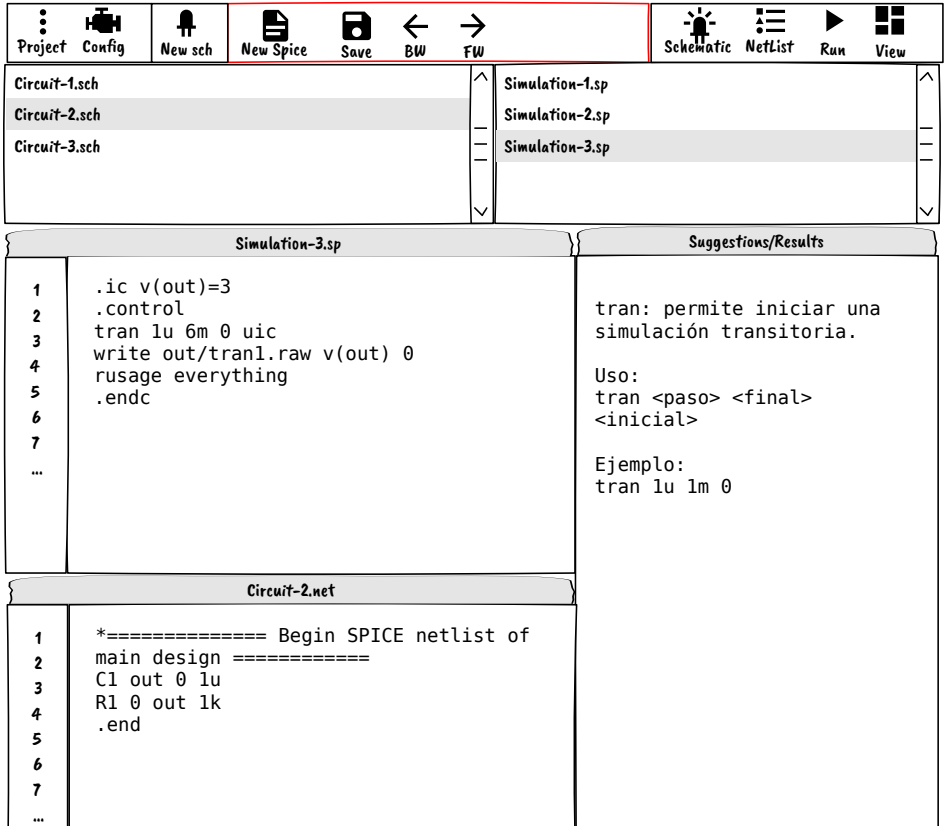
menu-2


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una
simulación transitoria.
Uso:
tran <paso> <final>
<inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end

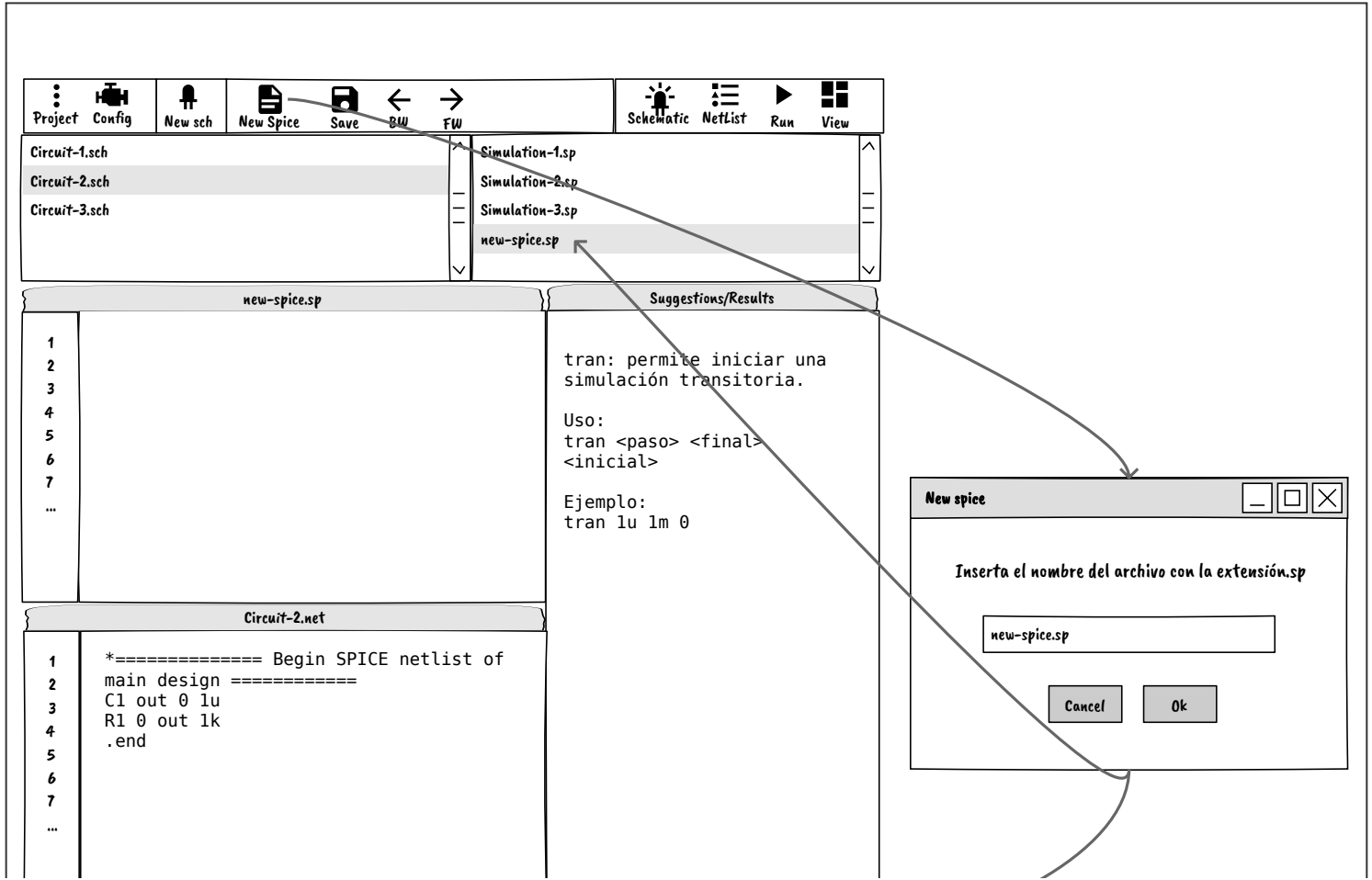
New sch


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
new-schematic.sch
Spices vacios por ser nuevo esquematico
new-spice.sp
Suggestions/Results
tran: permite iniciar una simulación transitoria.
Uso:
tran <paso> <final> <inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
*============== Begin SPICE netlist of main design ============
C1 out 0 1u
R1 0 out 1k
.end
New schematic
Inserta el nombre del archivo con la extensión.sch
new-schematic.sch
Cancel
Ok

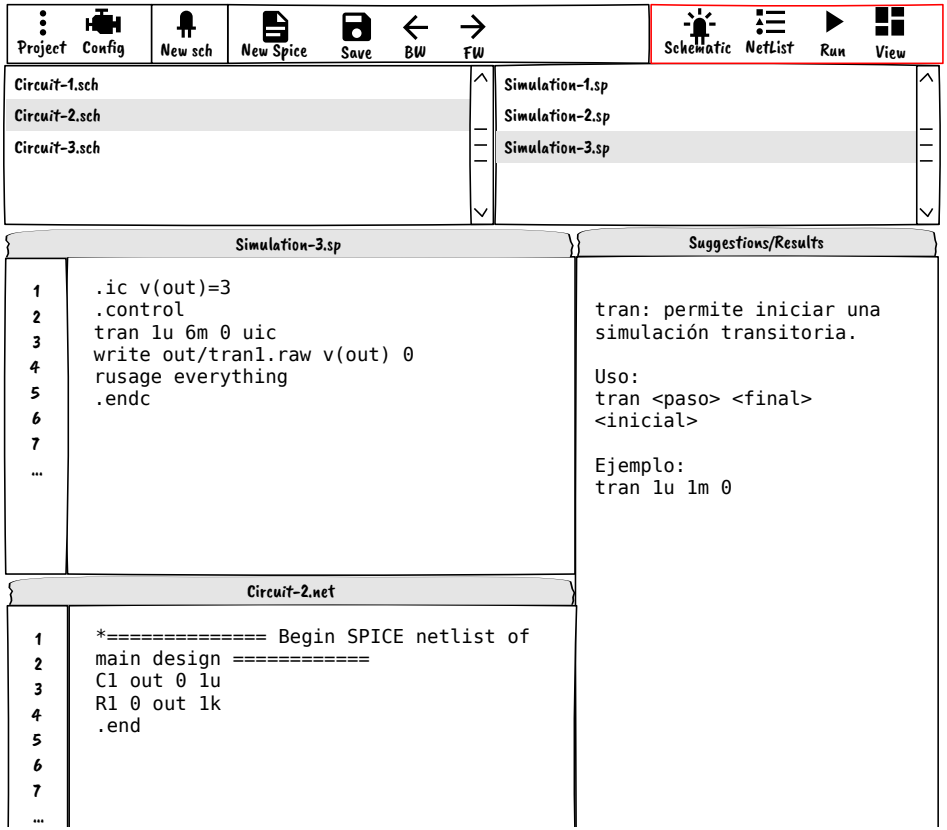
menu-3


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una
simulación transitoria.
Uso:
tran <paso> <final>
<inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end

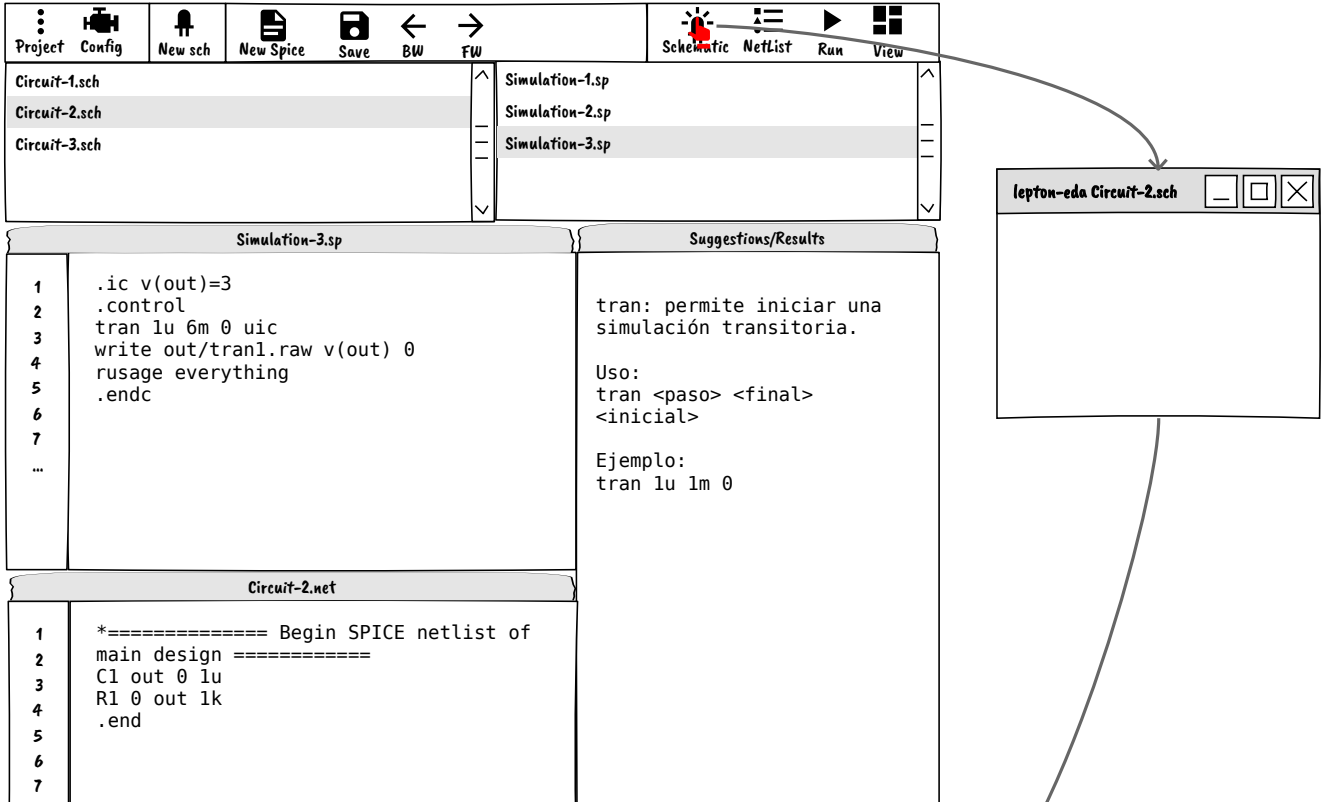
new-spice


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
new-spice.sp
new-spice.sp
Suggestions/Results
1
2
3
4
5
6
7
...
tran: permite iniciar una
simulación transitoria.
Uso:
tran <paso> <final>
<inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end
New spice
Inserta el nombre del archivo con la extensión.sp
new-spice.sp
Cancel
Ok

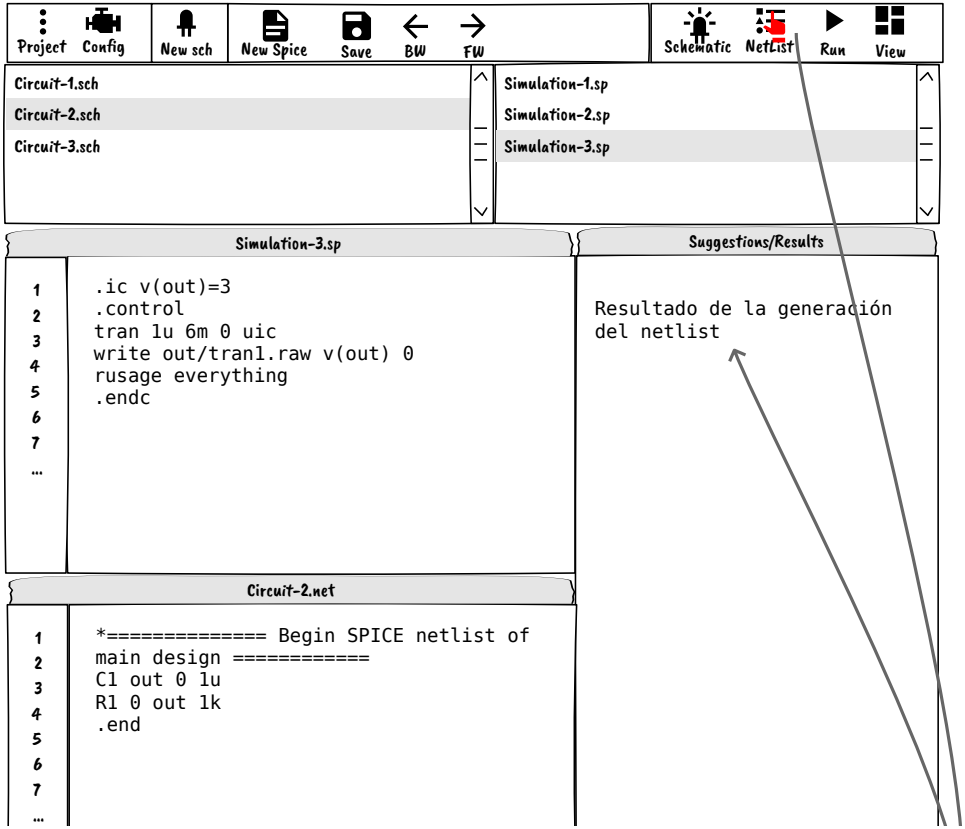
menu-4


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una simulación transitoria.
Uso:
tran <paso> <final> <inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of main design ============
C1 out 0 1u
R1 0 out 1k
.end

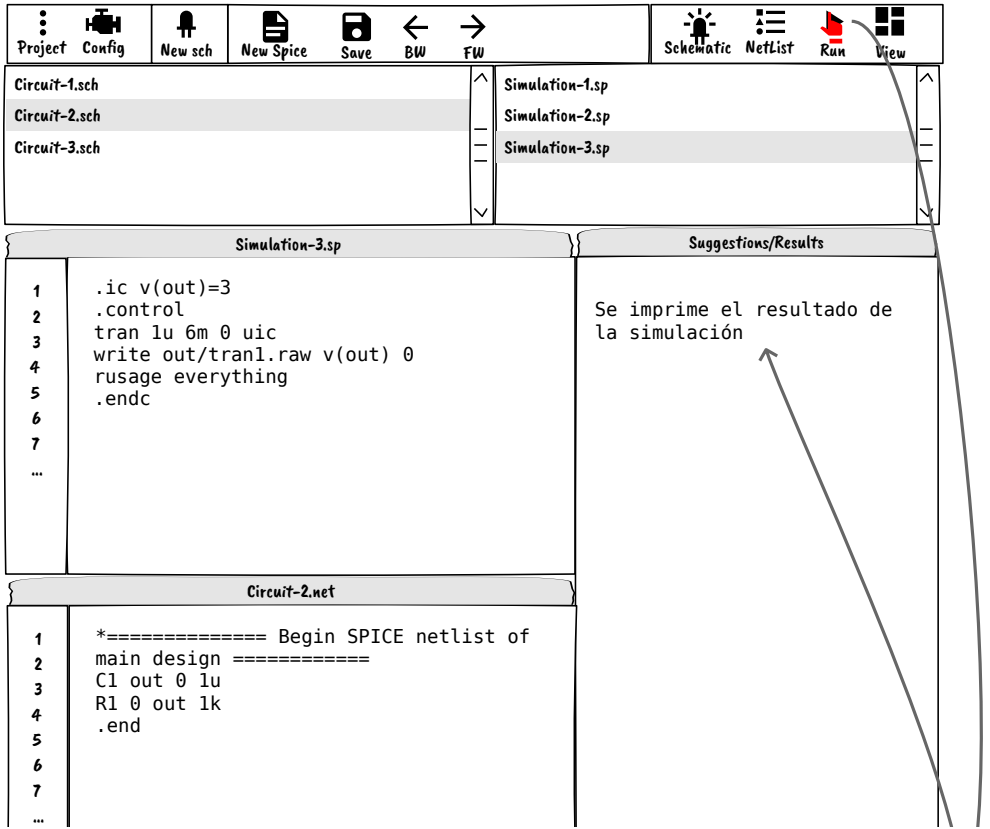
Schematic


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una
simulación transitoria.
Uso:
tran <paso> <final>
<inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end
lepton-eda Circuit-2.sch

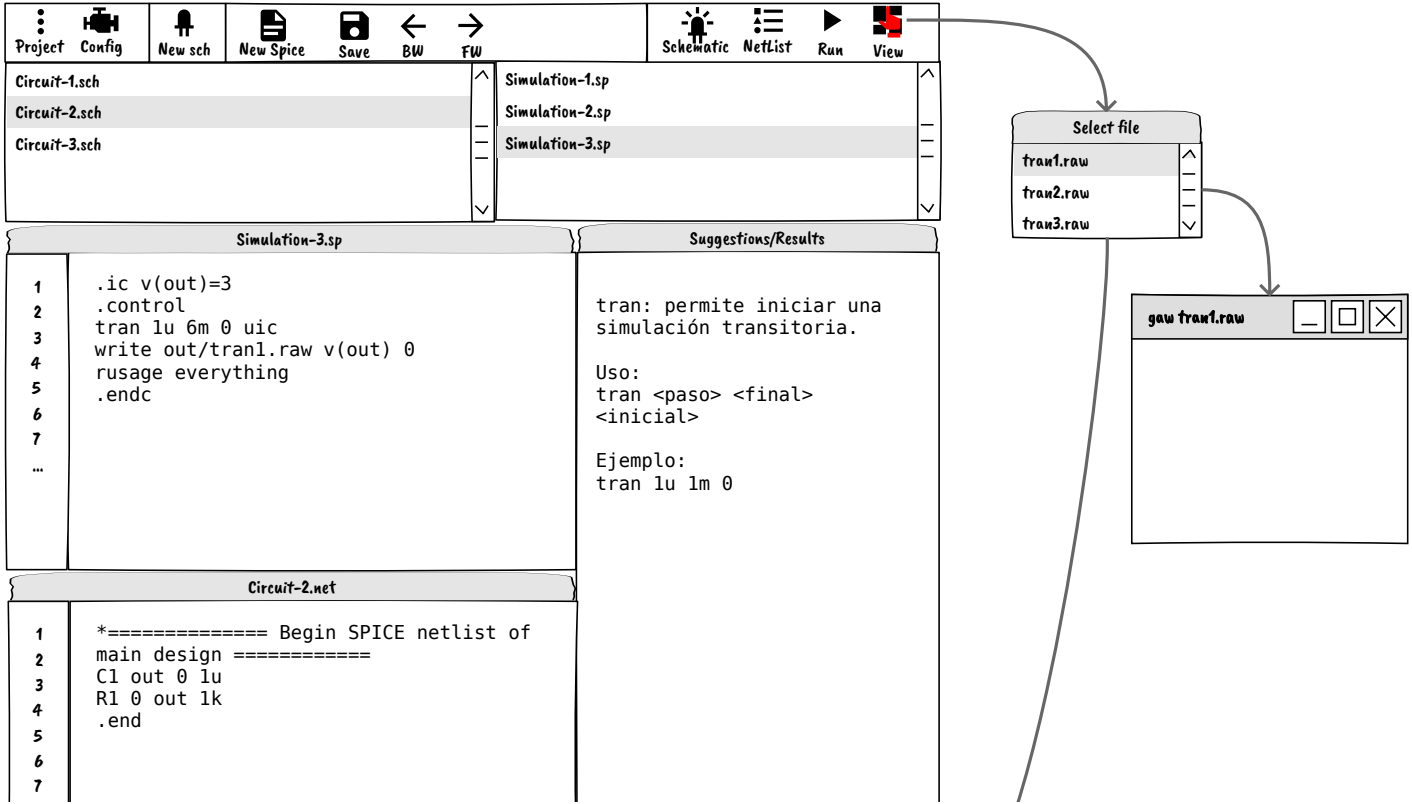
NetList


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
Resultado de la generación
del netlist
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end

Run


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
Se imprime el resultado de
la simulación
Circuit-2.net
1
2
3
4
5
6
7
...
*============== Begin SPICE netlist of
main design ============
C1 out 0 1u
R1 0 out 1k
.end

scope


Project
Config
New sch
New Spice
Save
BW
FW
Schematic
NetList
Run
View
Circuit-1.sch
Circuit-2.sch
Circuit-3.sch
Simulation-1.sp
Simulation-2.sp
Simulation-3.sp
Simulation-3.sp
1
2
3
4
5
6
7
...
.ic v(out)=3
.control
tran 1u 6m 0 uic
write out/tran1.raw v(out) 0
rusage everything
.endc
Suggestions/Results
tran: permite iniciar una simulación transitoria.
Uso:
tran <paso> <final> <inicial>
Ejemplo:
tran 1u 1m 0
Circuit-2.net
1
2
3
4
5
6
7
*============== Begin SPICE netlist of main design ============
C1 out 0 1u
R1 0 out 1k
.end
Select file
tran1.raw
tran2.raw
tran3.raw
gaw tran1.raw