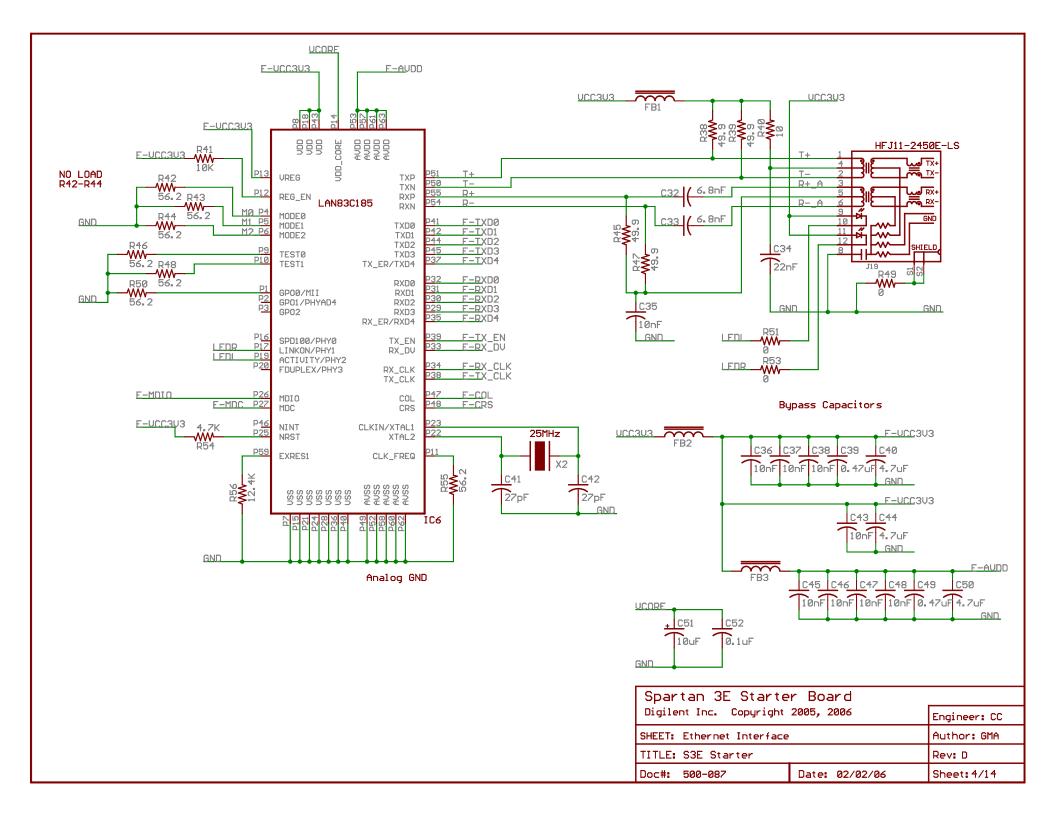
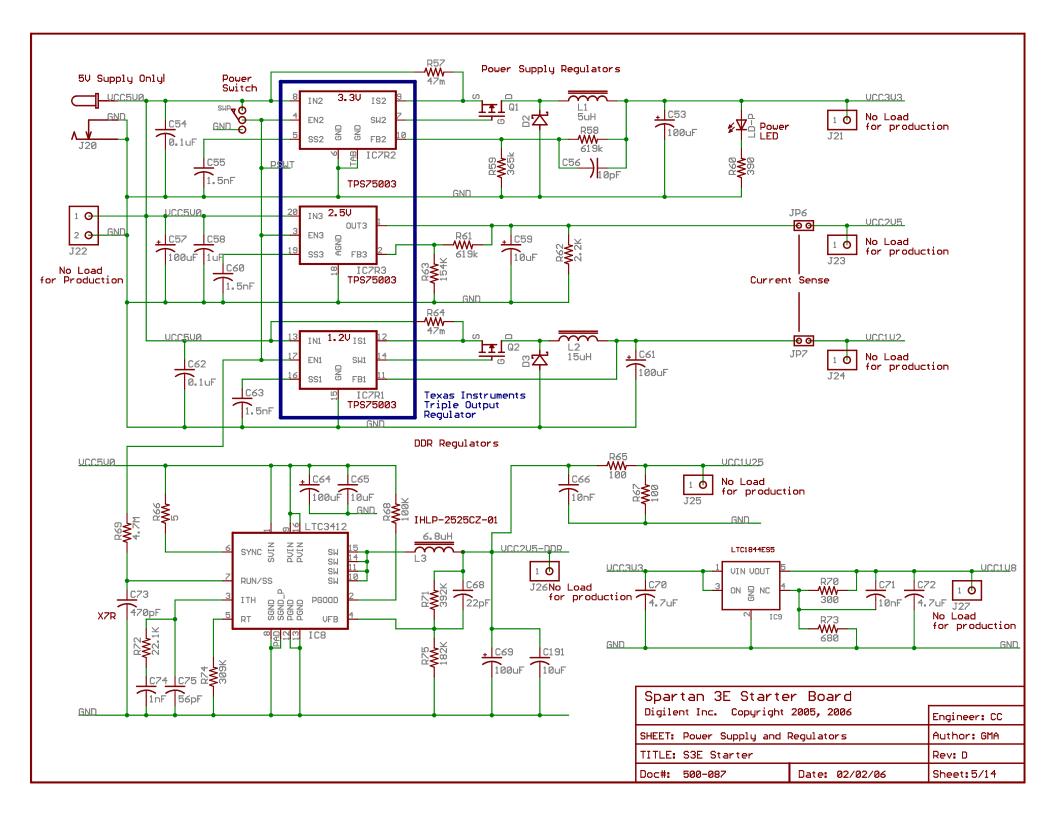
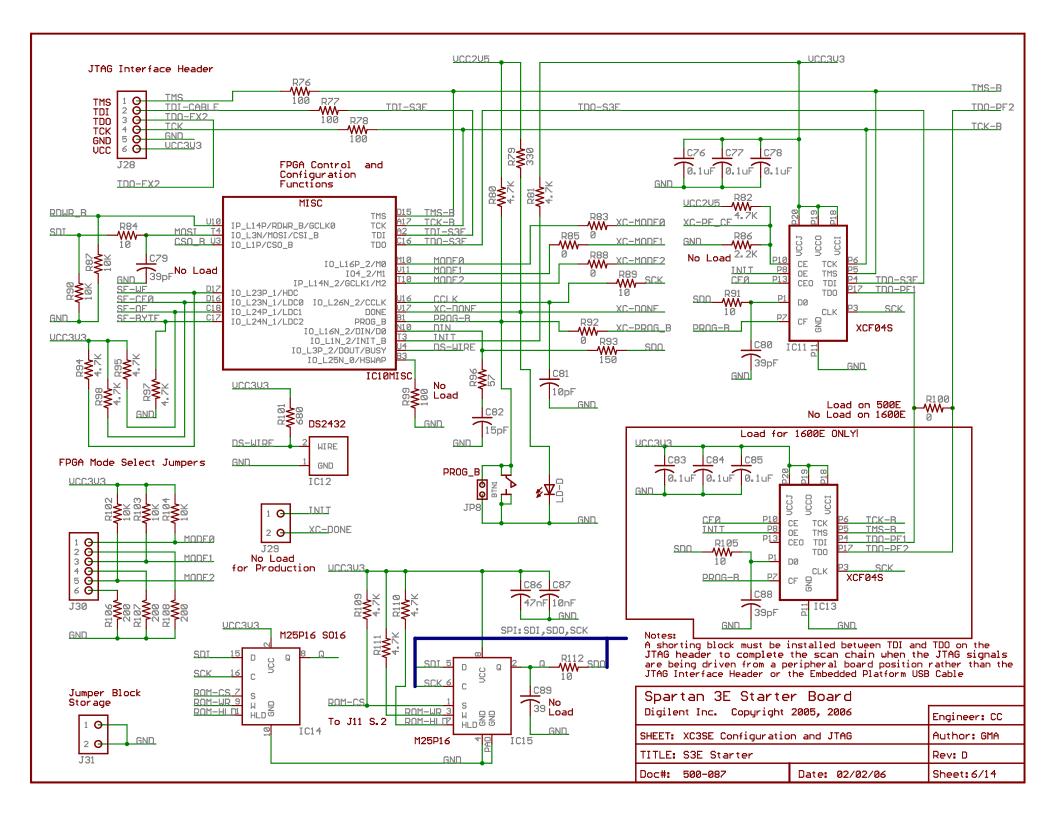


Page 3 contains proprietary information and has been intentionally left blank.





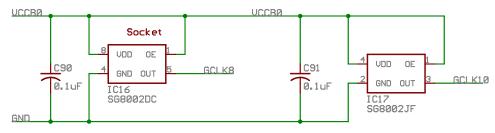




BANKØ FX2-T033 IP1 102 EX2-I040 FX2-I031 IP3 IO3/UREF A7 D13 IP4 ₩ FX2-I032 I04 IP5 * FX2-I030 105 FX2-I029 FX2-I038 B15 IP_L2P 107 EX2-I037 A15 FX2-I034 IP_L2N 108 FX2-I036 FX2-I028 IP_L7P IO_L1P FX2-I035 FX2-I027 IP_L7N IO_L1N FX2-I026 F10 IP_L10P IO_L3P G1 Ø C8 D8 A5 FX2-I025 IP_L10N IO_L3N/VREF FX2-I024 IP_L16P IO_L4P FX2-I023 IP_L16N IO_L4N FX2-I021 IP L22P IO L5P FX2-I022 IP_L22N IO_L5N/VREF FX2-I020 IO_L6P FX2-I019 IO_L6N IP2 * FX2-I018 I0_L8P FX2-I017 D6 IO_L21P * IO_L8N FX2-I016 IO_L21N * IO_L9P FX2-I015 IO_L9N FX2-I014 IO L15P FX2-I013 IO_L15N FX2-I012 IO_L17P FX2-I011 IO_L17N FX2-I010 I0_L18P FX2-I09 IO_L18N/VREF FX2-I08 IO_L19P EX2-CLKOUT FX2-I07 IO_L11P/GCLK4 IO_L19N/VREF EX2-CLKIN E10 FX2-I06 IO_L11N/GCLK5 IO_L20P FX2-T05 XC-CPLD FN B10 IO_L12P/GCLK6 IO_L20N

VCC for Bank 1 = 3.3V

SF-STS B18	BANK1	* IO_L22N	E16 E17	XC-CPLD EN
AMP-DO E18	IP1 IP3	* IO2 * IO1	P16	_
ROT-B G18 EAST H13	IP4	* 103	P15 U18	E-TX_EN
SOUTH K17	IP5	IO_L1P/A16	T17	SF-A16 SF-A15
WEST D18	IP6	IO_L1N/A15	R18	SF-A14
ROT-A K18		IO_L2P/A14 IO L2N/A13	T18	SF-A13
SWØ L13	IP9	IO_L3P	R15	SF-D8
SW1 L14		O L3N/VREF	R16	SF-D9
SU2 H18	IP11/VREF	IO_L5P	M1 4	TXD
SU3 N17		O_L5N/VREF	M13	TXD-A
XC-TRIG R17	IP14	IO_L6P	P1 <i>7</i> P18	SF-D10
		IO_L6N	M15	XC-CMD0 SF-D11
		IO_L7P	M16	SF-D12
		IO_L7N	N18	XC-CMD1
NC4 N15	TO 14D *	IO_L8P	M18	LCD F
NC5 N14	IU_LTP	IO_L8N IO_L9P/A12	L16	SF-A12
NC6 E15	IO_L3P *	IO_L9N/A11	L15	SF-A11
	10_L22F	IO_L10P	L18	LCD_RS
	īn	L10N/VREF	L17	LCD_RW
		IO_L15P/A2	J12	SF-A2
		IO_L15N/A1	J13	SF-A1
		IO_L16P	H16 H17	XC-GCKØ
		IO_L16N/A0	H1 4	SF-AA UGA-RFD
SF-A10 K13		IO_L17P	H15	UGA-GRN
SF-A9 K12	IO_L11P/A10/RHCLK0	IO_L17N	G15	UGA-BLUE
SF-A8 K15	IO_L11N/A9/RHCLK1	IO_L18P	G16	XC-DØ
SF-A7 K14	IO_L12P/A8/RHCLK2 IO_L12N/A7/RHCLK3/TRDY1	IO_L18N IO_L19P	F18	XC-D1
SF-A6 J17	IO_LI3P/A6/RHCLK4/IRDYI	IO_L19N	F17	XC-D2
SF-A5 J16	IO L13N/A5/RHCLK5	IO_L20P	G1 4	PS2C
SF-A4 J15	IO L14P/A4/RHCLK6	IO_L20N	G13	PS2D
SF-A3 J14	IO_L14N/A3/RHCLK7	IO_L21P	F15	UGA-HS
		IO_L21N	F14	UGA-US
		IC10B1	-	



I0_L23P

IO_L24P

IO_L24N

IO_L25P

IC10B0

IO_L23N/VREF

FX2-I04

FX2-I03

FX2-I02

FX2-I01

FX2-I039

AD - A/D Converter (11) AMP - Gain Amplifier (11) DAC - D/A converter (11) E- Ethernet (4)

SMA-CLK

GCL K10

SD-CK_P/F

EX2-CLKIO

SD-CK P

R113 GCL K8

₩

A10

B8

C9

D9

IO_L12N/GCLK7

IP_L13P/GCLK8

IP_L13N/GCLK9

IO_L14P/GCLK10

IO_L14N/GCLK11

FX2 - Hirose FX2 Connector (1) ROM - M25P16 (6)

SD- SD RAM (12)

SF - StrataFlash (12) ST - Soft Touch Connector(1)

U - USB (3) XC - XC9572 (10)

*	Notes	are	for	pins	that	veru	between	500/1200/1600 c	lies
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Spartan 3E Starter Board			
Digilent Inc. Copyright 2005, 2006		Engineer: CC	
SHEET: XC3SE Banks 0 and 1, Clock ICs		Author: GMA	
TITLE: S3E Starter		Rev: D	
Doc#: 500-087	Date: 02/02/06	Sheet:7/14	

Vcc for Bank 2 = 3.3V

VCC vor Bank 3 = 2.5V DDR

	DANIKO		1
F-RX DU V2	BANK2		U5 F-MNIN
E-RX_DU V2 CENTER V16	IP1	I01/UREF	
E-CUI NE	IP2	I02/D5	<u>R9 SF-D5</u> P9 E-MDC
E-CRS U13	IP3 💥	103	R11 E-TXDØ
	IP4 *	105	
1000	IP_L2P	I06/UREF	
	IP_L2N	IO_L4P	
	IP_L8P	IO_L4N	
	IP_L8N	IO_L5P	
RXD-A U8 F-RXDØ V8	IP_L11P	IO_L5N	P6 SF-D13 N7 AMP-CS
	IP_L11N/VREF	I0_L7P	
	IP_L17P	IO_L7N	
	IP_L17N	IO_L9P	
	IP_L23P	IO_L9N	N8 <u>DAC-C</u> S R8 SF-D14
<u>F-RXD4 U14</u>	IP_L23N	IO_L10P	
		IO_L10N	
NO3 113		IO_L18P	P11 AD-CONV N11 SF/XC-A23
NCZ VZ	IO4 *	IO_L18N	
	IO_L6P *	IO_L19P	
	IO_L6N/VREF *	IO_L19N/UREF	V13 SF/XC-A21 T12 SF/XC-A20
NC10 N12 NC11 P12	IO L21P *	IO_L20P	
NC11 P12	IO_L21N *	IO_L20N	R12 SFL
			D12
0E B3 N0		IO_L22P/A23	<u>P1</u> 3 <u>R1</u> 3
SF-DZ N9 SF-D6 M9	IO_L12P/D7/GCLK12	IO_L22N/A22	KI 3
	IO_L12N/D6/GCLK13	IO_L24P/A21	T14 R14 V15 US2 SF-A19
	IO_L13P/D4/GCLK14	IO_L24N/A20	KLT
	IO_L13N/D3/GCLK15	IO_L25P/VS2/A19	
SF-D2 R10 SF-D1 P10	IO_L15P/D2/GCLK2	IO_L25N/VS1/A18	
2F - III P10	IO_L15N/D1/GCLK3	IO_L26P/US0/A17	T16 USØ_SF-A17
•		IC10B2	_
		101002	

	BANK3	* 101	D4	NC15
D3	TDI	* IO2		SD-A4
F <u>5</u>	IDO	* IO L4P	E4	
<u>UCC1U25 J6</u>	TD4 /UDEE	* IO3/VREF		<u>UCC1U</u> 25
6 <u>1</u> J <i>7</i> K <u>2</u> K <u>7</u> M <u>1</u> N <u>1</u>	IP5	IO_L1P	C1	SD-RAS
J <u>7</u>	IP6	IO_L1N		SD-CAS
K <u>2</u>	IP7	IO_L2P	D1	SD-WE
K/	IP8	IO_L2N/VREF	D2 E2	<u>UCC1U</u> 25
1 <u>11</u>	IP9	IO_L3P		SD-DQ8
N3	IP10	IO_L3N	F1	SD-DQ9
R1	1511	IO_L5P		<u>SD-</u> DQ10 SD-DQ11
U1	IP12	IO_L5N		SD-UDQS
01	IP13	IO_L6P		UCC1U25
		IO_L6N/VREF		SD-DQ12
		IO_L7P		SD-DQ13
		IO_L7N		SD-DQ14
		IO_L8P	H5	SD-DQ15
		IO_L8N		SD-A5
NC12 E3	TO 1.4N *	IO_L9P		SD-A6
NC13 P3	IU_LTN	IO_L9N	H2	SD-A8
NC14 P4		IO_L10P		SD-AZ
	IU_LZZN	IO_L10N IO_L15P	L1	SD-DQ1
		IO_L15P		SD-DQ0
		IO_L16P	L3	SD-DQ2
		IO_L16P	L4	SD-DQ3
		IO_L17P	L6	SD-LDQS
		IO_L17N/VREF		<u>UCC1U</u> 25
		IO_L18P		<u>SD-</u> DQ5
		IO_L18N		SD-DQ4
		IO_L19P		SD-DQ6
SD-CK_P J5		IO_L19N	M6	SD-DQ7
SD-CK_N J4	IO_L11N/LHCLK1	IO_L20P	N4	SD-A9
SD-UDM J1 SD-LDM J2	IO_L12P/LHCLK2	IO_L20N	N5 P2	SD-A11
	IO_L12N/LHCLK3/IRDY2	IO_L21P		SD-A12
SD-CKF K3 SD-CS K4	IU_LI3P/LHCLK4/TRDYZ	IO_L21N		<u>SD-</u> A3 SD-A1
SD-BA1 K6	TO_LISN/LHCLKS	IO_L23P		SD-A2
SD-BAO K5	IO_L14P/LHCLK6	IO_L23N	T2	SD-A10/AP
au-pok Na	IO_L14N/LHCLK7	I0_L24P		SD-A0
		IO_L24N	1.	<u> </u>
		IC10B3	•	

AD - A/D Converter (11)
AMP - Gain Amplifier (11)
DAC - D/A converter (11)
E- Ethernet (4)
FX2 - Hirose FX2 Connector (1)
ROM - M25P16 (6)
SD- SD RAM (12)
SF - StrataFlash (12)
ST - Soft Touch Connector (1)
U - USB (3)
XC - XC9572 (10)

USC SF-A17

USC SF-A17

USC SF-A18

USC SF-A18

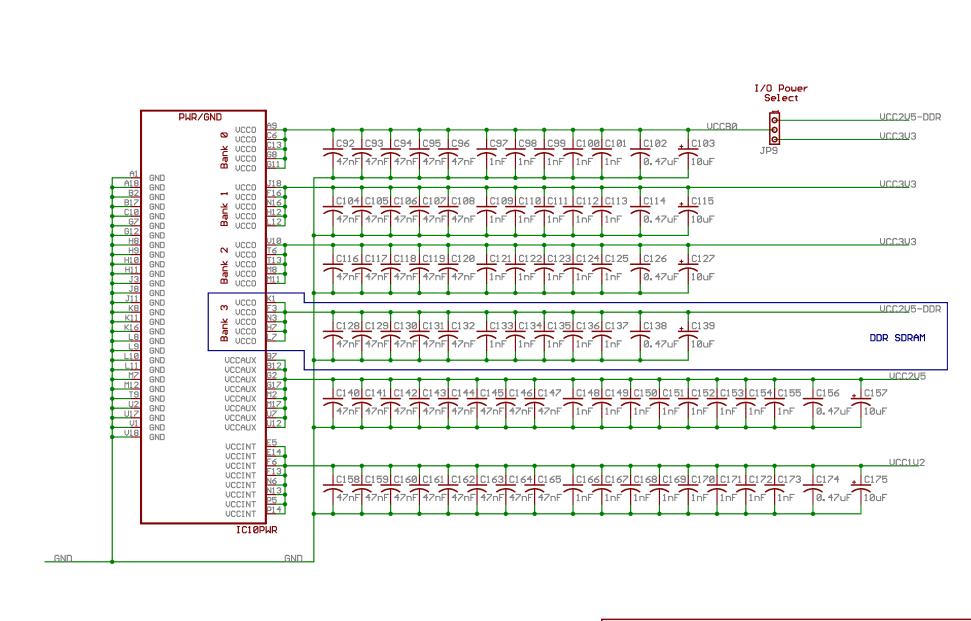
USC SF-A19

USC SF-A19

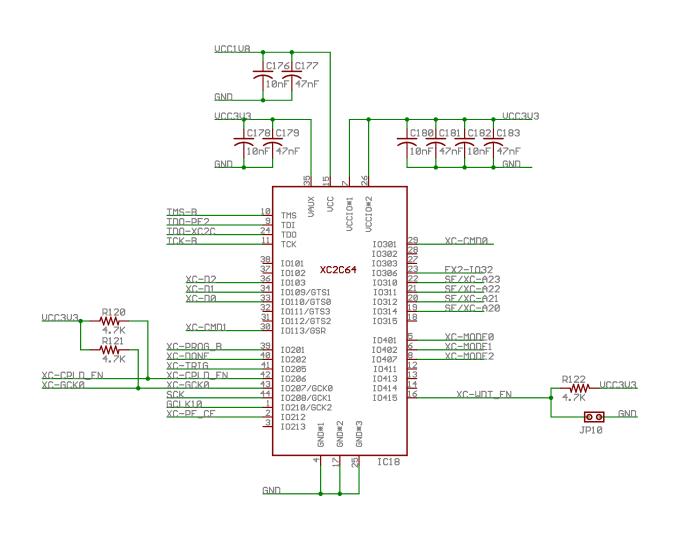
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* Notes are for pins that very between 500/1200/1600 dies

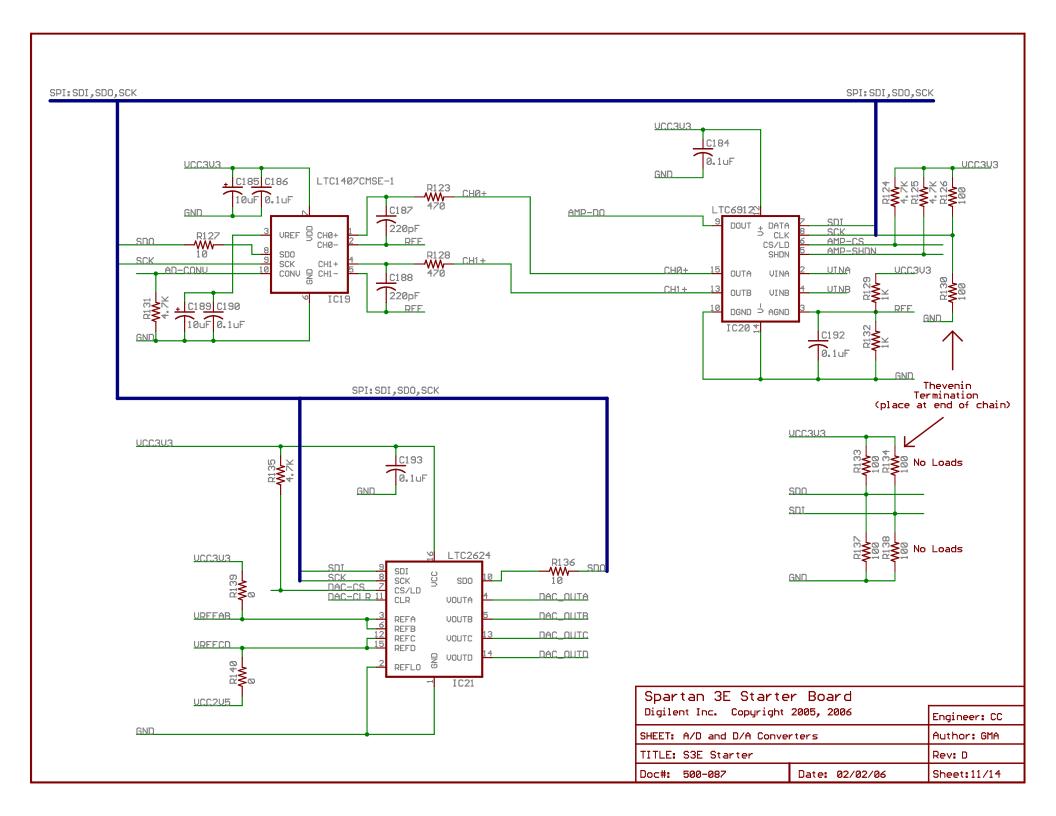
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Digilent Inc. Copyright 2005, 2006		Engineer: CC	
SHEET: XC3SE Banks 2 and 3		Author: GMA	
TITLE: S3E Starter		Rev: D	
Doc#: 500-087	Date: 02/02/06	Sheet: 8/14	

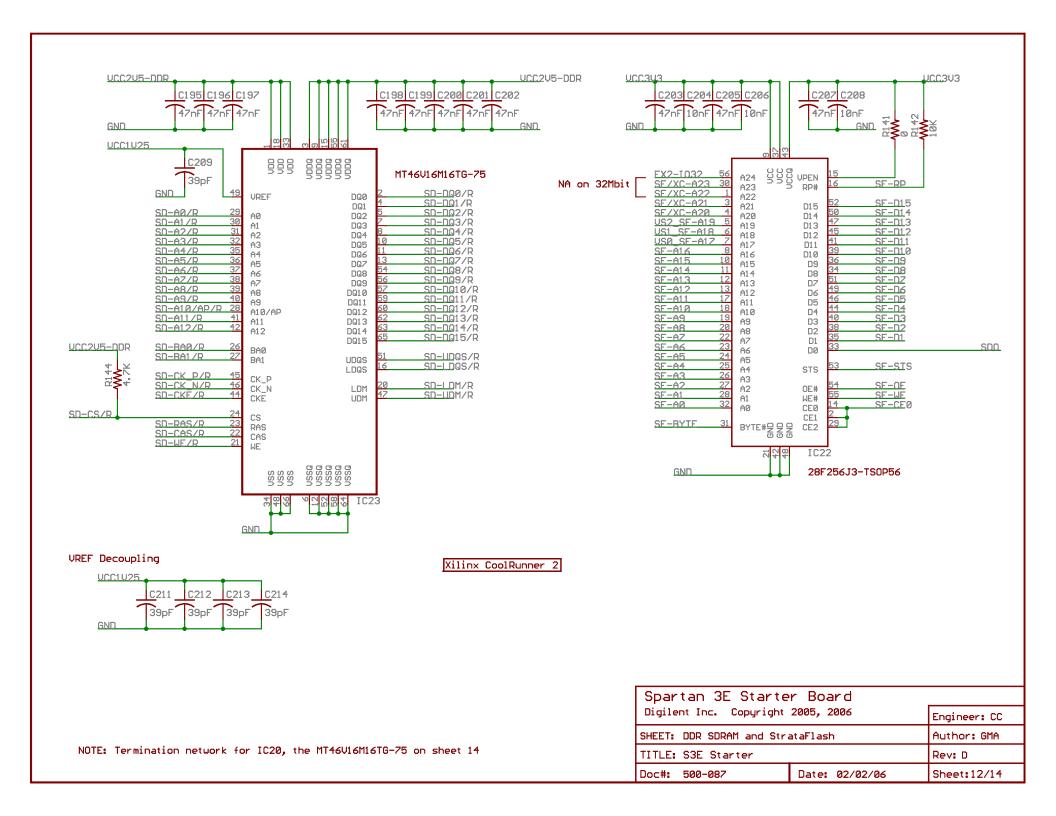


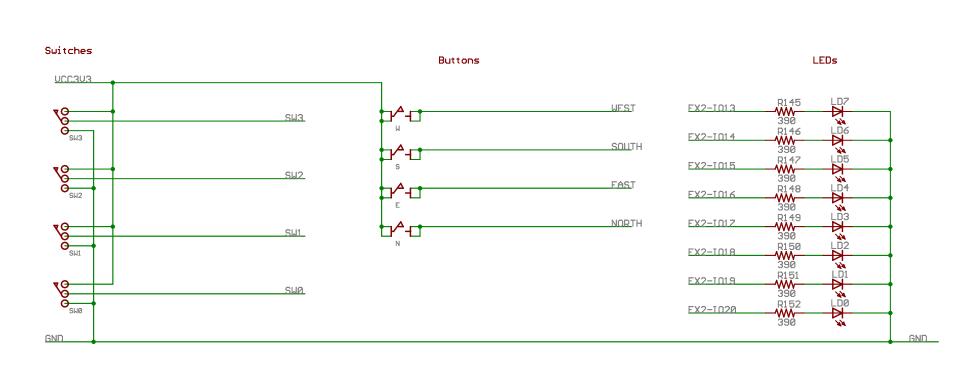
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SHEET: XC3SE Power Decoupling		Author: GMA	
TITLE: S3E Starter		Rev: D	
Doc#: 500-087	Date: 02/02/06	Sheet: 9/14	

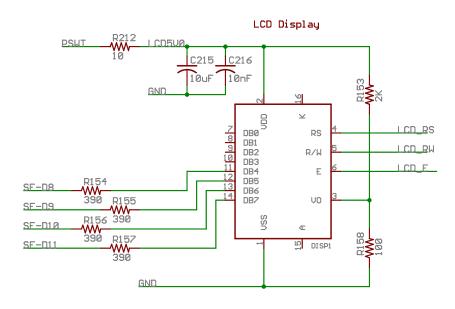


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SHEET: XC2C64 CPLD	Author: GMA	
TITLE: S3E Starter		Rev: D
Doc#: 500-087	Date: 02/02/06	Sheet:10/14

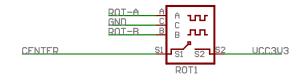








Pushbutton/Rotary Encoder



Spartan 3E Starter Board			
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SHEET: LCD and General IC	Author: GMA		
TITLE: S3E Starter	Rev: D		
Doc#: 500-087	Date: 02/02/06	Sheet:13/14	



FX2 Differential Termination Not Loaded

EX2-I017 R202 FX2-I018 100 100 FX2-I020 EX2-I019 WM FX2-I020 100 FX2-I021 FX2-I022 100 FX2-I023 FX2-I024 100 FX2-I025 FX2-I024 100 FX2-I026 FX2-I026 100 FX2-I028 FX2-I028 100 FX2-I036 FX2-I036 100 FX2-I037 FX2-I038 100 FX2-I038 FX2-I038 FX2-I037 FX2-I038 FX2-I038 100 FX2-I038 FX2-I038		HOT LOGGEG	
EX2-I019 R203 FX2-I020 100 R204 FX2-I022 100 FX2-I023 FX2-I024 100 FX2-I024 R206 FX2-I025 FX2-I026 FX2-I026 100 FX2-I027 FX2-I028 100 FX2-I028 FX2-I028 100 FX2-I035 FX2-I036 100 FX2-I038 FX2-I038 FX2-I037 FX2-I038 FX2-I038 FX2-I038 I00 FX2-I038 FX2-I039 FX2-I038 FX2-I038	EX2-I017		FX2-I018
EX2-I021 R204 FX2-I022 100 EX2-I023 R205 FX2-I024 100 EX2-I025 R206 FX2-I026 100 EX2-I027 R207 FX2-I028 100 EX2-I035 R208 FX2-I036 100 EX2-I037 R209 FX2-I038 100 EX2-I037 R209 FX2-I038 100 EX2-I037 R209 FX2-I038 100 EX2-I037 R209 FX2-I038	FX2-I019	R203	FX2-I020
EX2-I023 R205 FX2-I024 100 EX2-I025 R206 FX2-I026 100 EX2-I027 FX2-I028 100 EX2-I035 R208 FX2-I036 100 EX2-I037 R209 FX2-I038 100 EX2-I037 R209 EX2-I038 FX2-I038 100 EX2-I037 R209 EX2-I038 FX2-I038	FX2-I021	R204	FX2-I022
EX2-I025 R206 FX2-I026 100 EX2-I027 FX2-I028 100 EX2-I035 R208 FX2-I036 100 EX2-I037 R209 EX2-I037 R209 EX2-I037 R209 EX2-I037 R209 EX2-I037 R209 EX2-I037 R209 EX2-I038 I000 EX2-I037 R209 EX2-I038 FX2-I038	EX2-I023	R205	FX2-I024
EX2-I027 R207 FX2-I028 100 100 EX2-I035 WW FX2-I036 100 FX2-I038 EX2-I037 WW FX2-I038 100 FX2-CLKDUT	FX2-I025	R206	FX2-I026
EX2-1035 R208 FX2-1036 100 EX2-1037 R209 FX2-1038 100 EX2-CLKIN R210 FX2-CLKOUT	EX2-I027	R207	FX2-I028
EX2-0137 R209 FX2-1038 100 EX2-01KIN R210 FX2-01K0UT	EX2-I035	R208	FX2-I036
EX2-CLKIN R210 FX2-CLKOUT	EX2-I037	R209	FX2-I038
	EX2-CLKIN	R210	FX2-CLKOUT

Spartan 3E Starter Board			
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SHEET: DDR Memory Signals	Author: GMA		
TITLE: S3E Starter	Rev: D		
Doc#: 500-087	Date: 02/02/06	Sheet:14/14	