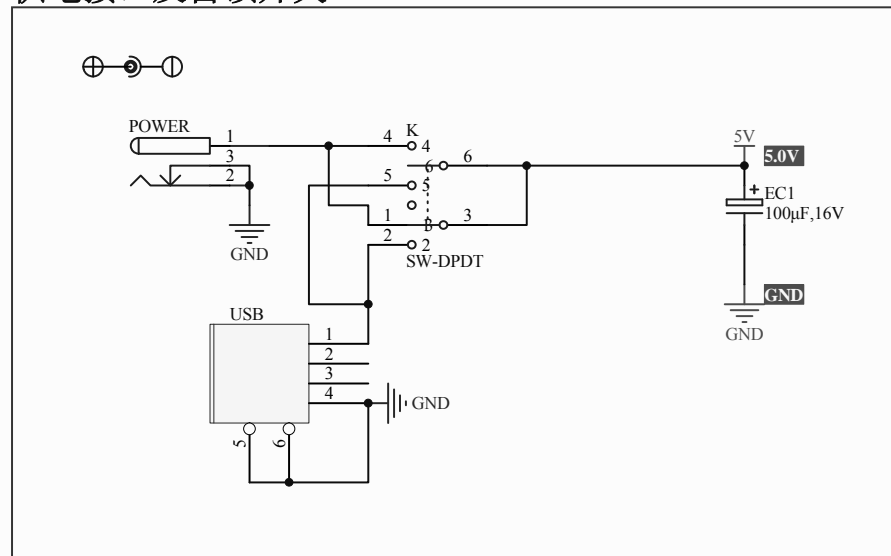
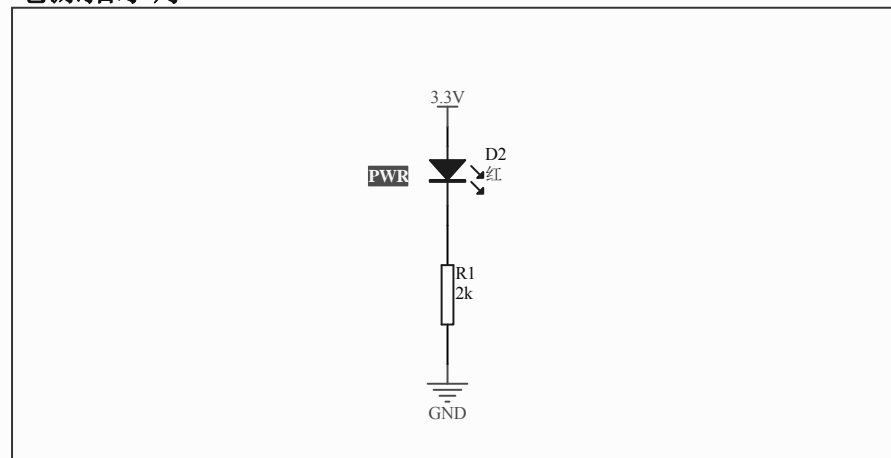


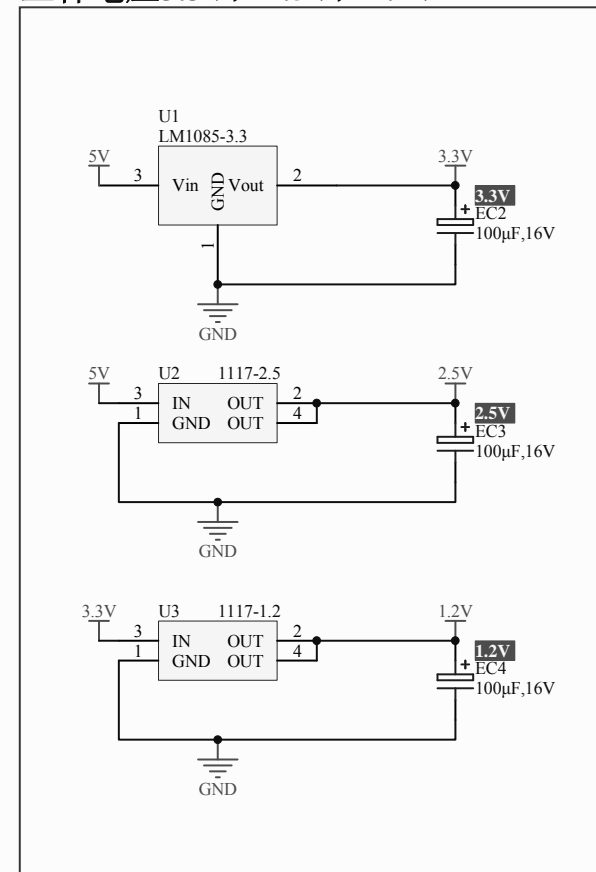
## 供电接口及自锁开关



## 电源指示灯

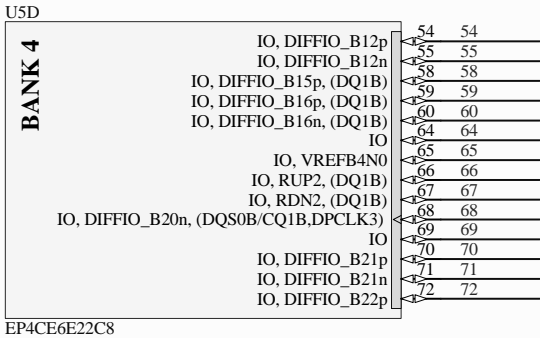
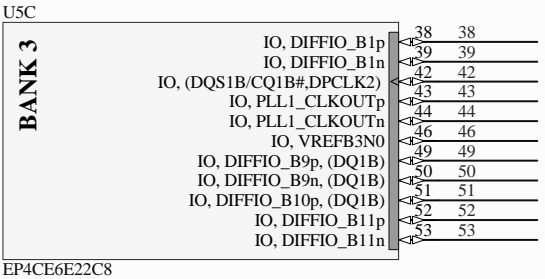
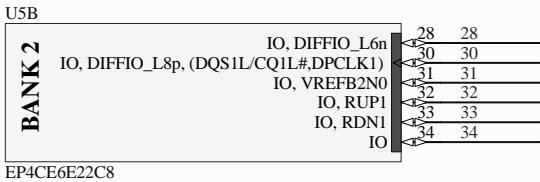
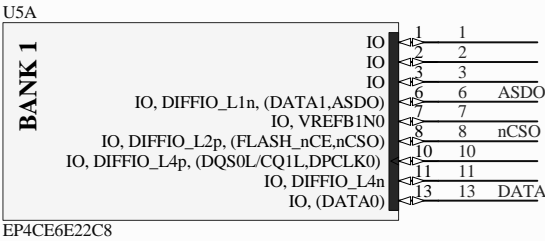


## 三种电压3.3V, 2.5V, 1.2V



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Date:	2014/9/20	Sheet of
File:	G:\ok\1\ POWER.SchDoc	Drawn By:

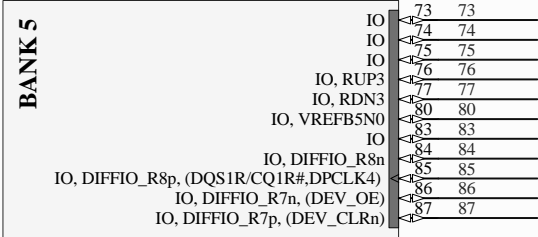
FPGA主芯片 PARTA、B、C、D



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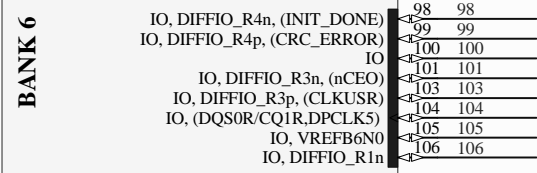
FPGA主芯片 PartE--PartH

U5E



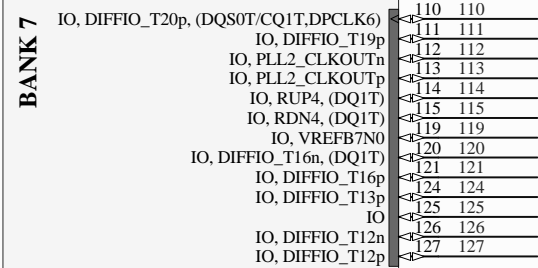
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U5F



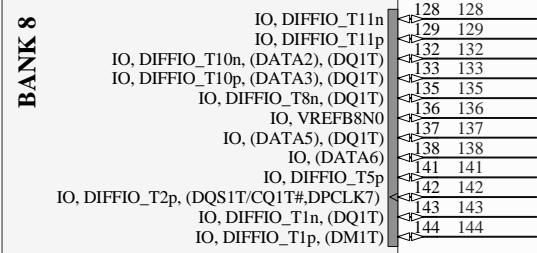
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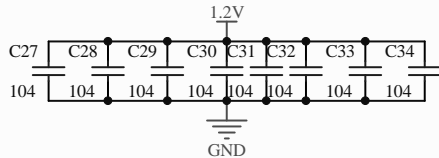
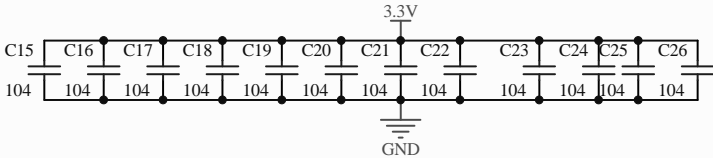
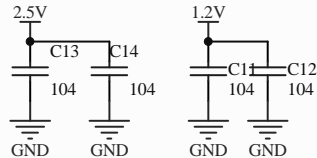
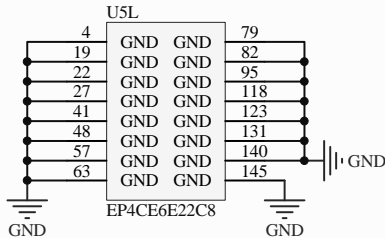
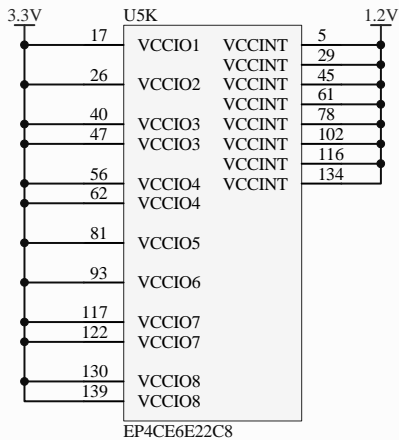
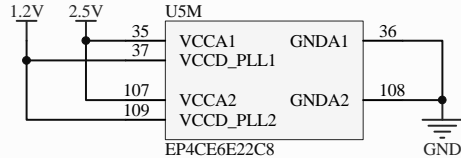
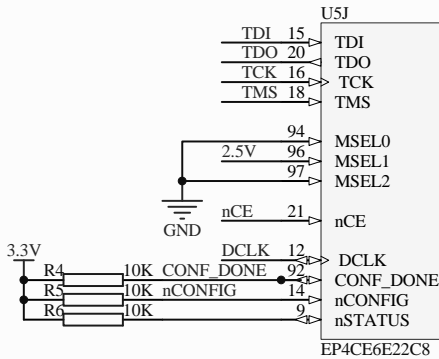
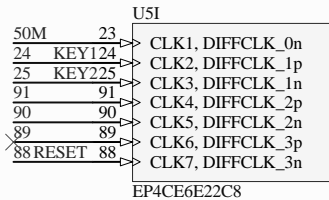
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U5H



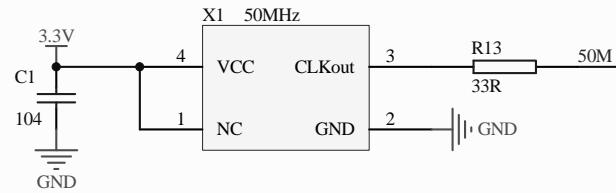
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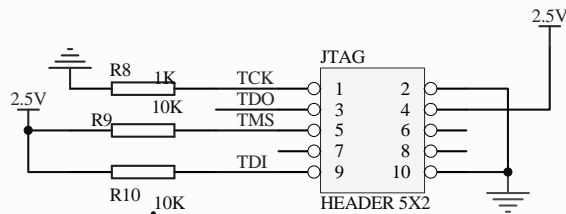


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FPGA开发板 酷睿掌中宝1A www.OurFPGA.com			
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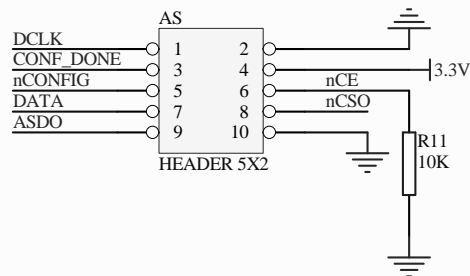
## 50M晶振



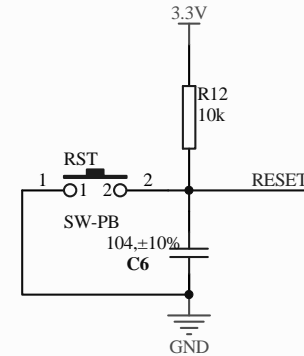
## JTAG下载口



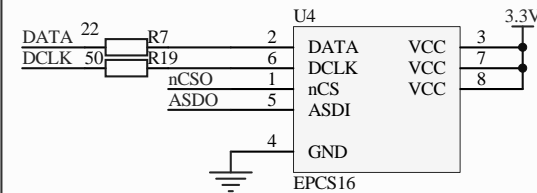
## AS固化下载口



## 复位电路

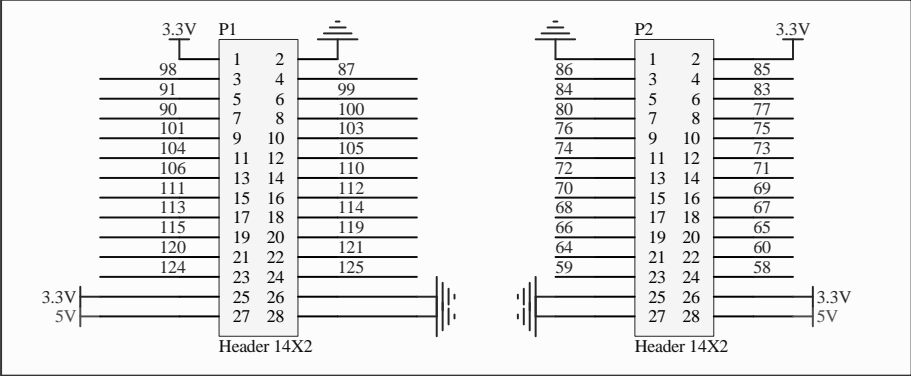


## 配置芯片16Mb串行FLASH

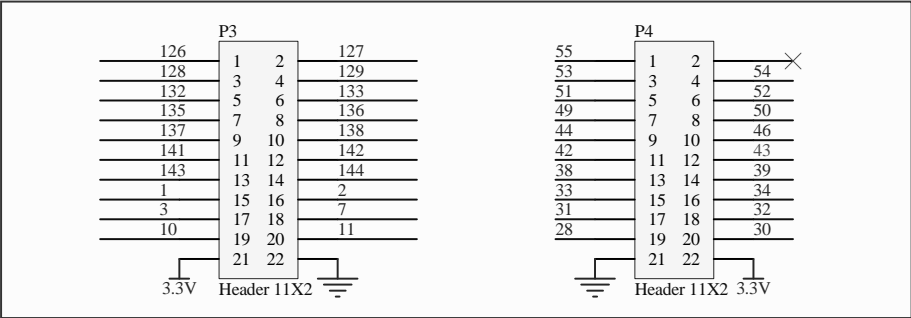


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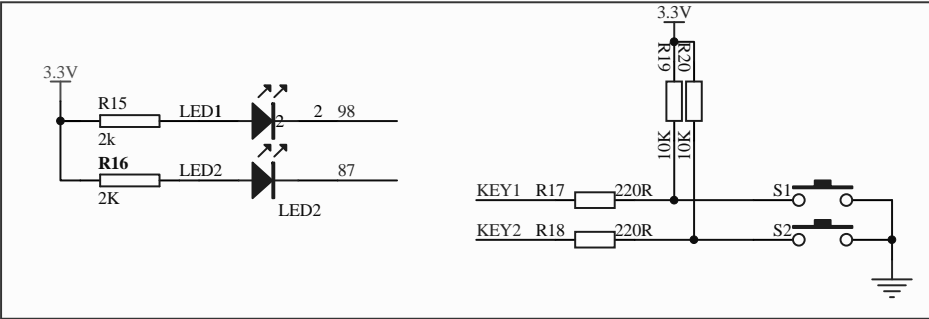
扩展IO P1, P2



扩展IO P3, P4 (也可插上SDRAM模块)



2个LED指示灯 2个独立按键



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Orcad A			1.0
Date:	2014/9/20		Sheet of
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