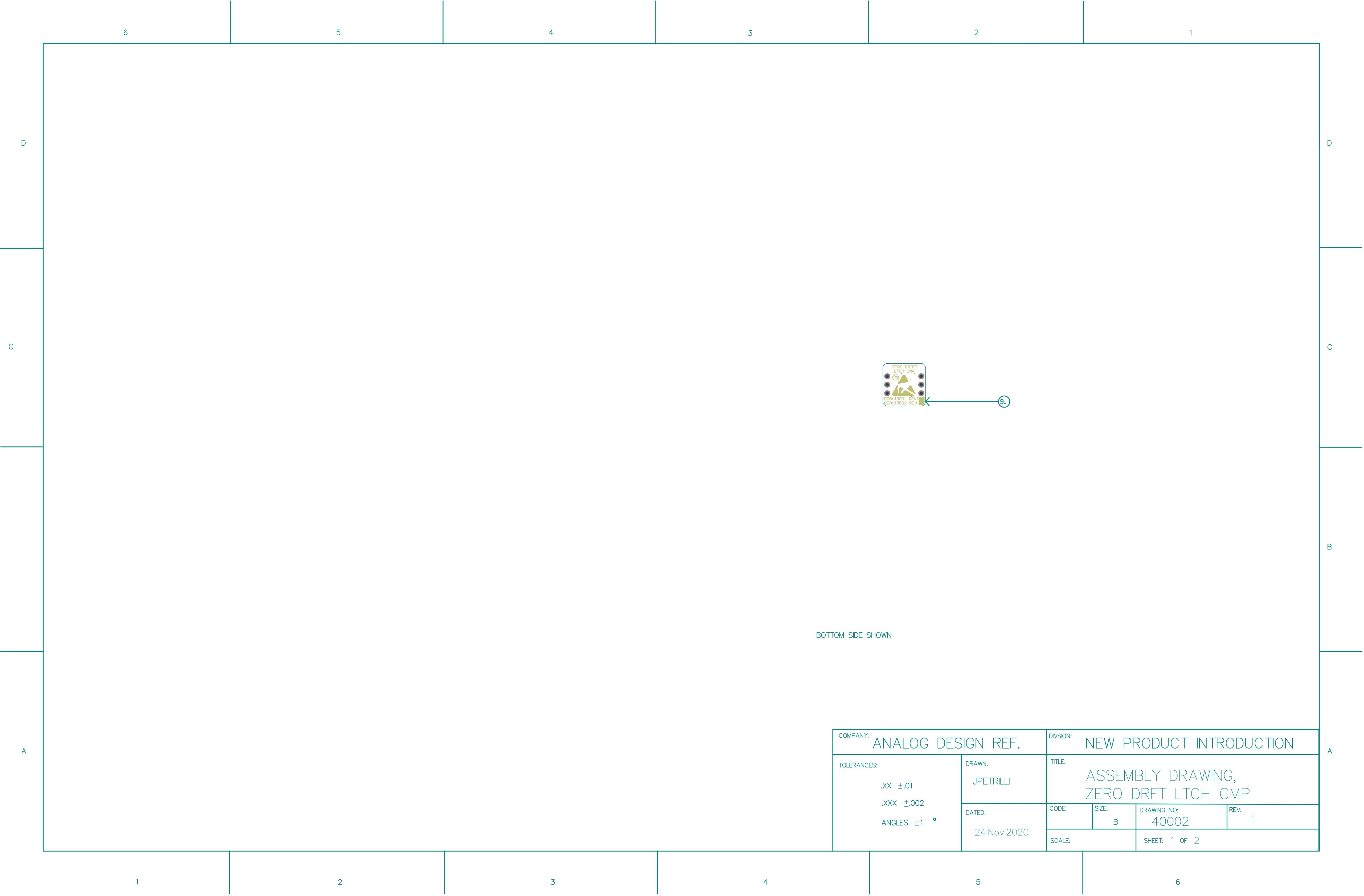


TOP SIDE SHOWN

- ASSEMBLY NOTES:
1. THIS BOARD MAY HAVE ALTERNATE ASSEMBLY VARIATIONS ALWAYS CHECK FOR CORRECT AND LATEST BILL OF MATERIALS.
  2. WORKMANSHIP STANDARD TO BE IN COMPLIANCE WITH IPC-A-610 LATEST REVISION.
  3. SQUARE PADS AND OR SILKSCREEN DENOTE THE FOLLOWING:  
PIN 1 OF IC'S, CONNECTORS, DIODE CATHODE, & + OF CAPACITORS.
  4. MARK APPLICABLE REVISION LEVEL IN LOCATION PROVIDED USING BLACK PERMANENT INK OR NONSMEAR NAMEPLATE.
  5. VENDOR WILL INDICATE VENDOR CODE AND DATE CODE ON BOTTOM SIDE IN APPROX LOCATION DENOTED (BOTTOM SIDE).
  6. ASSEMBLED BOARD SHALL BE SHIPPED IN ESD BAGS THAT PROVIDE ESD DRAIN.
  7. RoHS/WEEE COMPLIANT ASSEMBLY
  8. NOTES 5 AND 6 MAY BE REPLACED WITH NONSMEAR NAMEPLATE NAMEPLATE TO INCLUDE ASSY REV LEVEL, & DATE CODE ALL BARCODES MUST BE ACCOMPANIED BY READABLE CHARACTERS.
  9. MARK THE ASSY REVISION (1) IN THE LOCATION DENOTED
  10. PART/ASSEMBLY SHALL BE IN COMPLIANCE WITH DIRECTIVE 2011/65/EU OF THE EUROPEAN PARLIAMENT CONCERNING THE RESTRICTION OF CERTAIN HAZARDOUS SUBSTANCES IN ELECTRICAL AND ELECTRONIC EQUIPMENT WITHOUT EXEMPTION

COMPANY: ANALOG DESIGN REF.		DIVISION: NEW PRODUCT INTRODUCTION			
TOLERANCES:  .XX ±.01  .XXX ±.002  ANGLES ±1 °	DRAWN:  JPETRILLI	TITLE:  ASSEMBLY DRAWING, ZERO DRFT LTCH CMP			
	DATED:  24.Nov.2020	CODE:	SIZE:  B	DRAWING NO:  40002	REV:  1
		SCALE:		SHEET: 1 OF 2	



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