**Top Level Project Description**

# Project Information

## Project Name:

## Requestor(s):

## Resources(s):

## Target Start Date:

## Target End Date:

## End Use:

## Directory Location:

## Brief Description:

# Electrical & Electronics Design

## Git Repo:

## Directory Location:

## E-CAD & Version:

## PCB Design Required?

## PCBA Part Number(s):

## Brief Description:

# Cable Design

## Directory Location:

## Connector Part Number(s):

## Crimp Part Number(s):

## Crimp Tool(s):

## Extraction Tool(s):

## Cable Part Number(s):

## Brief Description:

# Firmware Design

## Git Repo:

## Directory Location:

## Language:

## IDE Used:

## Compiler Version:

## Dependencies:

## Brief Description:

# Software Design

## Git Repo:

## Directory Location:

## Language:

## IDE Used:

## Compiler Version:

## Dependencies:

## Brief Description:

# Other Design Info

## Plumbing Required?

## Enclosure Needed?

## Brief Description of Needs:

# Third Party Software & Installers

## Third Party Software Applications:

## Installer Directory Location:

**REVISION HISTORY**

|  |  |  |  |
| --- | --- | --- | --- |
| **Revision** | **Description** | **Author** | **Date** |
| 01 | Initial Release | J. Petrilli | 09/28/2024 |
|  |  |  |  |