**Hardware Test Plan for XXXXX Rev XXX**

1. **Visual and Pre-Power Up Check**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
2. **Power-Up Check**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
3. **Test Section 3**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
4. **Test Section 4**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
5. **Test Section 5**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
6. **Test Section 6**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
7. **Test Section 7**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
8. **Test Section 8**
   1. Test 1
   2. Test 2
      1. Sub test 1
      2. Sub test 2
   3. Test N-1
      1. Sub test 1
      2. Sub test 2
   4. Test N
9. **Revision History**

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| --- | --- | --- | --- |
| **Revision** | **Description** | **Author** | **Date** |
| 01 | Initial Release | J. Petrilli | 10/30/2022 |
|  |  |  |  |