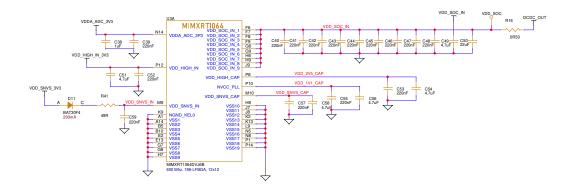
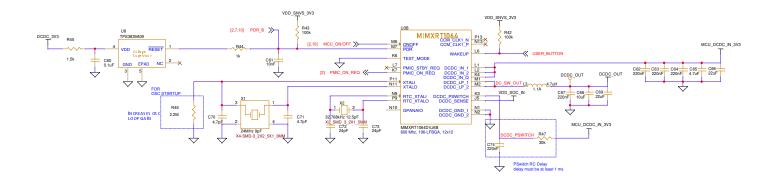


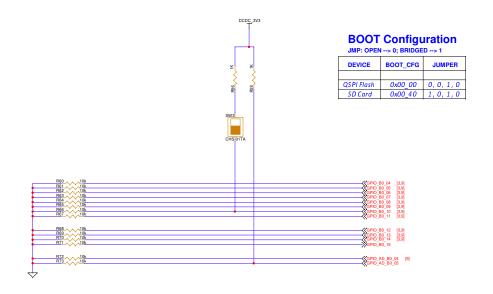
IMXRT1064 POWER





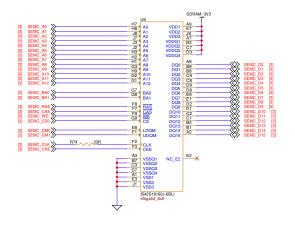
IMXRT1064 PINOUT GPIO EMC 40 C7 C7

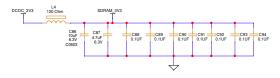
IMXRT1064 BOOT



FUSE MAP	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1
TYPE	BOOT_CFG[11]	BOOT_CFG[10]	BOOT_CFG[9]	BOOT_CFG[8]	BOOT_CFG[7]	BOOT_CFG[6]	BOOT_CFG[5]	BOOT_CFG[4]	BOOT_CFG[3]	BOOT_CFG[2]	BOOT_CFG[1]	BOOT_CFG[0]
FlexSPI1 - Serial NOR	Infinit-Loop: (Debug USE only) 0 - Disable 1- Enable	FLASH_TYPE 000-Device supports 3B read by default 001-Device supports 4B read by default 010-HyperFlash 1V8 011-HyperFlash 3V3 100-MXIC Octal DDR			0	0	0	0	HOLD TIME: 00 - 500us 01 - 1ms 10 - 3ms 11 - 10ms		EncryptedXIP 0 - Disabled 1- Enabled	Reserved
SD	Infinit-Loop: (Debug USE only) 0 - Disable 1- Enable	Reserved	Bus Width: 0 - 1-bit 1 - 4-bit	SD1 VOLTAGE SELECTION: 0 - 3.3 V 1 - 1.8 V	0	1	SD/SDXC Speed: 00 - Normal/SDR12 01 - High/SDR25 10 - SDR50		Cycle Enable: '0' - No power cycle		Port Select: 0 - eSDHC1 1 - eSDHC2	Fast Boot: 0 - Regular 1 - Fast Boot

SDRAM 32MB

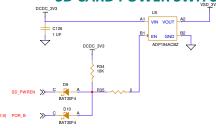


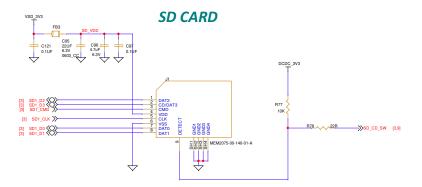




SD CARD

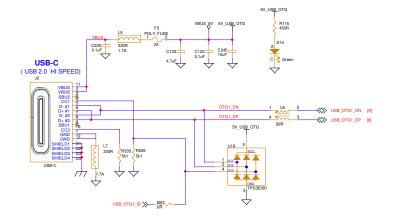
SD CARD POWER SWITCH



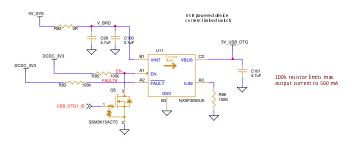


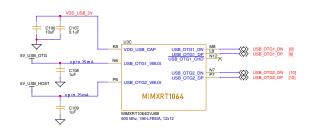
USB

USB OTG



USB POWER

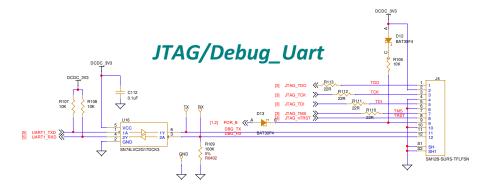




Fitle USB
Size Document Number Rev - Obc.
Date: Monday, August 09, 2021 Sheet 7 of 10

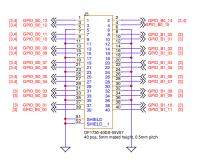
FLASH

CONNECTORS



LCD_Interface





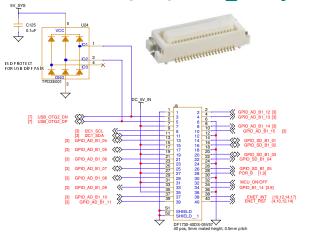
Mating Connectors:

HIROSE DF17(2.0)-40DP-0.5V(57) Digikey PN: H11130TR-ND Board -> Board Stacking Height == 5mm

HIROSE DF17(4.0)-40DP-0.5V(57) Digikey PN: H11144TR-ND Board -> Board Stacking Height == 7mm



USB/CSI/ENET_Interface







enable FW update over wifi add led for wifi on add led for tm/rm **WIFI** | VDD C252 C259 C260 0.1uF 0.1uF 0.1uF C254 C255 C3113 0.1uF 4.7uF 0.1uF L16 4.7 nH WL_EN_RST 9 EN/RST SD2 28 SD3 29 SD3 31 CLK 30 --≪ SD1_D2 ≪ SD1_CLK WL_EN_RST

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