fbrca_demo

fbrca m0

Notice SW[8] is on the far
right not following the usual order.

SW[7:4]   SW[3:0]   SW[8]

/4   /4

A[3:0]   B[3:0]   cin

A[3]   A[2]   A[1]   A[0]

B[3]   B[2]   B[1]   B[0]

C1

a b cin   a b cin   a b cin   a b cin

full_adder   full_adder   full_adder   full_adder
U3   U2   U1   U0

cout sum   cout sum   cout sum   cout sum

C3   C2   C1

Cout   S[3]   S[2]   S[1]   S[0]

LEDR[4]   LEDR[3:0]
↳ S[3:0] matching LEDR[3:0] bitwise