



Exercise 1:

State consists of registers X, A (HI), LOW plus C and Z bits, plus two inputs F1 and F2. A reasonable representation for State the following tuple:

```
type State = ([8] // F1
              , [8] // F2
              , [8] // A
              , [8] // X
              , [8] // LOW
              , Bit // C (Carry)
              , Bit // Z (Zero)
              )
```

Exercise 2: