

The USART ISP interface is implemented on the following pins:

- PIO0\_0 for receive
- PIO0\_1 for transmit

The USB interface is implemented on the following pins:

- PIO1\_6 for VBUS
- USB0\_DP for USB D+
- USB0\_DM for USB D-

(ISP0/PIO0\_31) (ISP1/PIO0\_4) (VBUS/PIO1\_6)

1 x x (FLASH) ISP is bypassed. The device boots from flash if valid user code is detected.  
0 0 x (I2C/SPI) The first valid probe message on I2C of Flexcomm Interface 1 or SPI of Flexcomm Interface 3.  
0 1 0 (USART) Part enters ISP via the USART of Flexcomm Interface 0.  
0 1 1 (USB) Allow programming flash as USB mass storage device class (MSC).

