

Senior Design Contract

Team Members & Contact Information

- John Parks – Email: parksjh@mail.uc.edu | Phone: 513-720-0058

Meeting Schedule

Weekly team meetings will occur weekly with my project advisor to discuss project work.

Project Description

This project will focus on developing new static methods to detect hardware vulnerabilities in RTL designs written in Verilog or VHDL. The goal of this project aims to enhance the security of hardware design early during the design stage.

Responsibilities

My responsibilities on the project will include but not limited to

- Implement detection algorithms
- Collect datasets for research project
- Summarize research findings, write reports and research papers

Approval

Approval of this contract signifies an initial understanding of the team structure, project description, and team member expectations and responsibilities.

X *John Parks*

John Parks

X *Boyang Wang*

Dr. Boyang Wang