Released Notes

Rev. Code	Date	Released Notes
1.0	2018-03-29	Author: Joe.Zhong Description: Draft
1.1	2018-06-29	Author: Joe.Zhong Description: 1. Rearrange the Pin mapping between Master and Mother board The mapping formular is 41 - N 2. Change some signals from IO signals to 5V_DC (GPIO5_IO: 09, 10, 11, 12, 13) 3. Add an alternative 5V Power Supply for Bell (Add R904 and R905)

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Signals Routing

Signals from master board

CON 1

1	VDD_5V	2	GEN_3V3
3	CAN1_TX	4	CAN2_TX
5	CAN1_RX	6	CAN2_RX
7	GND	8	GND
9	UART1_TXD	10	UART2_TXD
11	UART1_RXD	12	UART2_RXD
13	GND	14	GND
15	UART3_TXD	16	UART4_TXD
17	UART3_RXD	18	UART4_RXD
19	GND	20	GND
21	UART5_TXD	22	GPIO2_IO25
23	UART5_RXD	24	GPIO2_IO23
25	GND	26	GND
27	GPIO2_IO24	28	GPIO3_IO16
29	GPIO5_IO02	30	GPIO3_IO17
31	GND	32	GND
33	GPIO3_IO18	34	GPIO3_IO20
35	GPIO3_IO19	36	GPIO3_IO23
37	GND	38	GND
39	GPIO3_IO28	40	GPIO3_IO31

CON 2

1	GEN_3V3	2	GPIO4_IO19
3	GPIO4_IO16	4	GPIO4_IO20
5	GPIO4_IO18	6	GPIO4_IO17
7	GND	8	GND
9	GPIO4_IO21	10	GPIO4_IO26
11	GPIO4_IO22	12	GPIO4_IO27
13	GPIO4_IO23	14	GPIO4_IO28
15	GPIO4_IO24	16	GPIO4_IO29
17	GPIO4_IO25	18	GPIO4_IO30
19	GND	20	GND
21	GPIO4_IO31	22	5V_DC
23	GPIO5_IO05	24	5V_DC
25	GPIO5_IO06	26	5V_DC
27	GPIO5_IO07	28	5V_DC
29	GPIO5_IO08	30	5V_DC
31	GND	32	GND
33	GPIO5_IO14	34	GPIO1_IO30
35	GPIO5_IO15	36	GEN_3V3
37	GPIO5_IO16	38	GEN_3V3
39	GPIO5_IO17	40	GEN_3V3

Signals to mother board

CON 1

1	OUT_SPARE2	2	OUT_SPARE1
3	GND	4	GND
5	OUT_JIG_POWER_ON	6	OUT_PICK_UP
7	OUT_CELL_RESPONS	8	OUT_DROP_OFF
9	GND	10	GND
11	OUT_LED_READY	12	OUT_LED_ERROR
13	OUT_LED_RUN	14	OUT_MC_ON_SSR
15	GND	16	GND
17	OUT_JIG_FAN	18	UART5_RXD
19	OUT_POWER_FAN	20	UART5_TXD
21	GND	22	GND
23	UART4_RXD	24	UART3_RXD
25	UART4_TXD	26	UART3_TXD
27	GND	28	GND
29	UART2_RXD	30	UART1_RXD
31	UART2_TXD	32	UART1_TXD
33	GND	34	GND
35	CAN2_RX	36	CAN1_RX
37	CAN2_TX	38	CAN1_TX
39	GEN_3V3	40	VDD_5V

CON 2

1	GEN_3V3	2	GPIO5_IO17
3	GEN_3V3	4	GPIO5_IO16
5	GEN_3V3	6	BELL
7	GPIO1_IO30	8	IN_STACKER_IN
9	GND	10	GND
11	VCC_5V	12	IN_FIRE_CLOSE
13	VCC_5V	14	OUT_SPARE3
15	VCC_5V	16	OUT_SMOKE
17	VCC_5V	18	OUT_TEMPFUSE
19	VCC_5V	20	OUT_ALLBOX_OPEN
21	GND	22	GND
23	GPIO4_IO30	24	IN_ALLBOX_OPEN
25	GPIO4_IO29	26	IN_TEMPFUSE
27	GPIO4_IO28	28	IN_SPARE2
29	IN_SPARE3	30	IN_SPARE1
31	GPIO4_IO26	32	IN_CELL_EXIST
33	GND	34	GND
35	IN_FINISH	36	IN_EMG
37	GPIO4_IO20	38	IN_AC_LOSS
39	IN_POWER_SW	40	GEN_3V3

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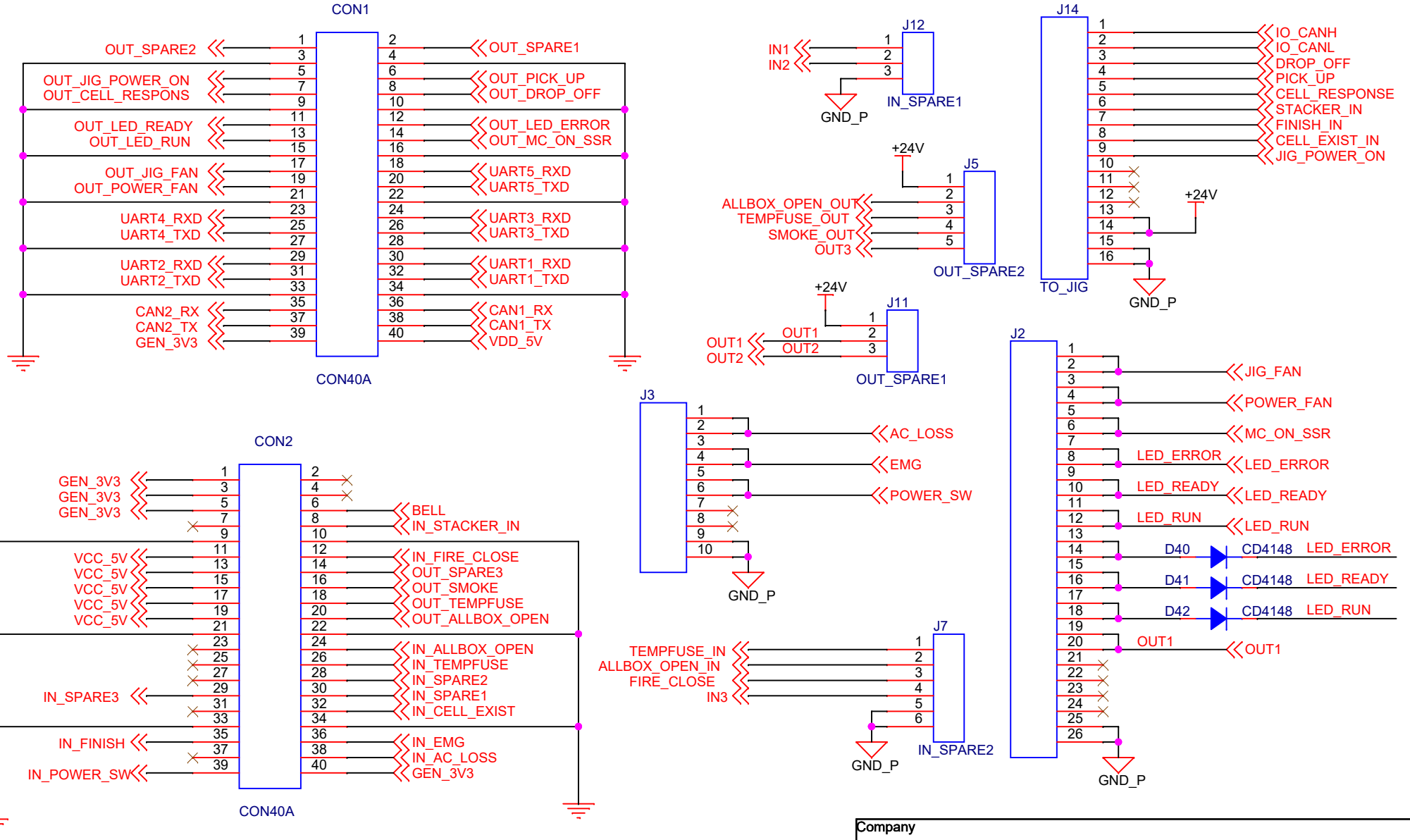
Connectors



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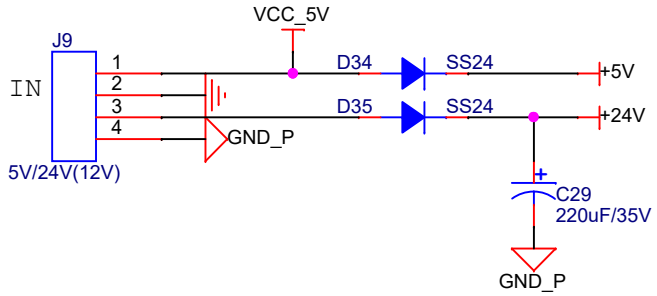
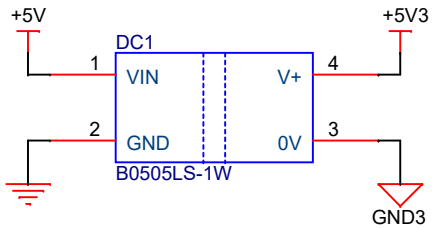
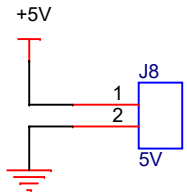
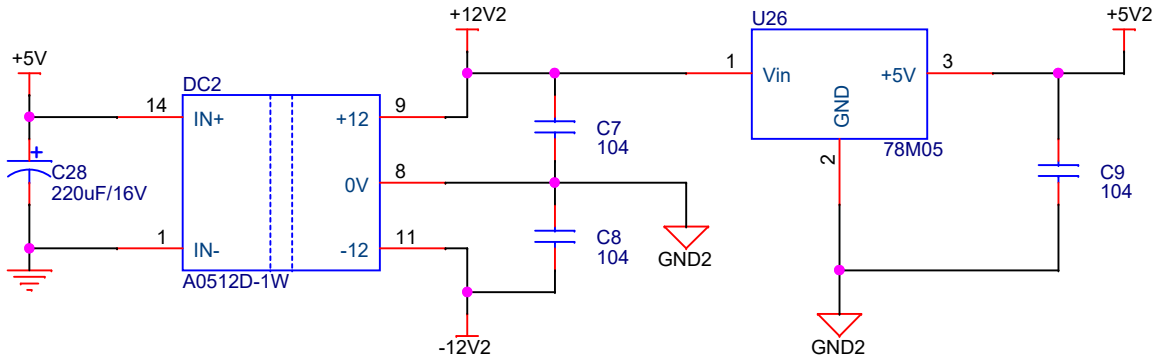
Power Supplies









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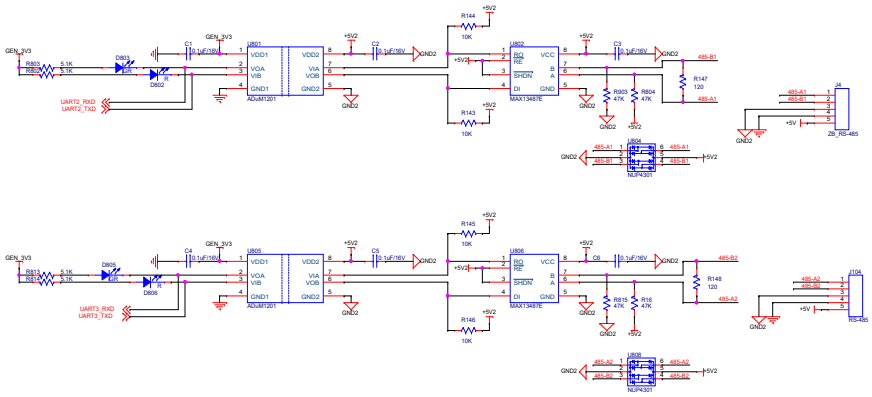
RS-485 & RS-232 & CAN

Isolated RS-485



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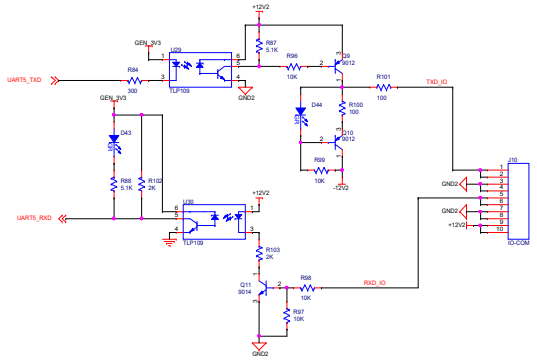
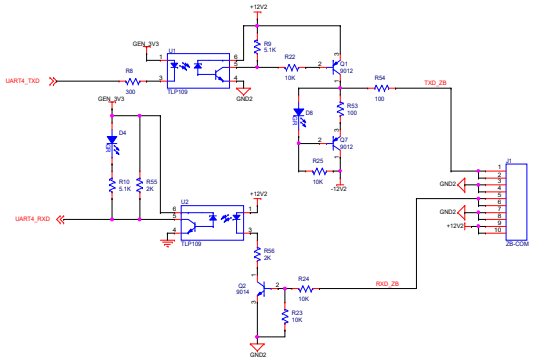
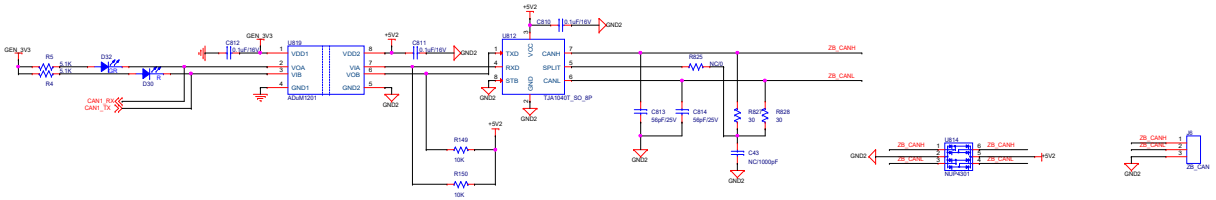


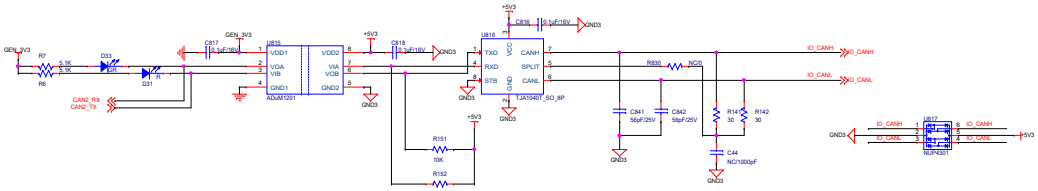
Isolated RS-232



Isolated CAN (ZB)



Isolated CAN (IO)



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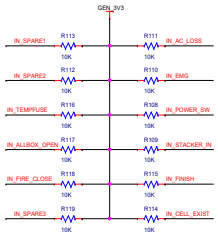
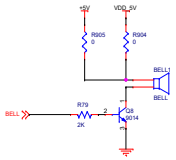
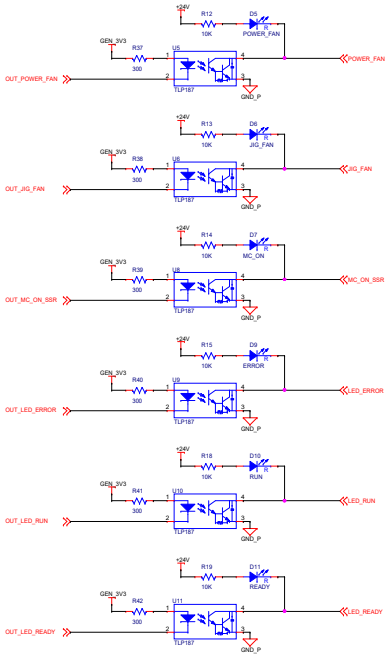
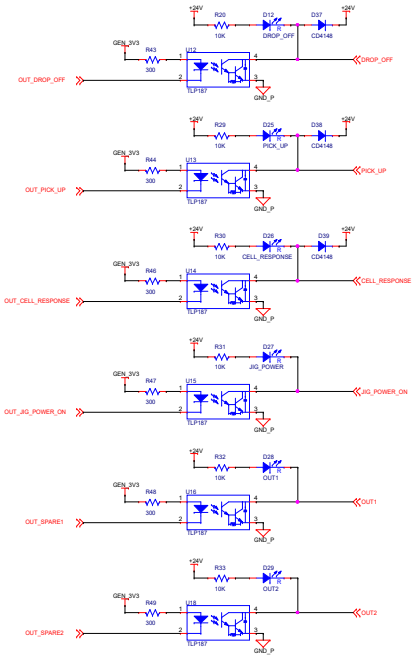
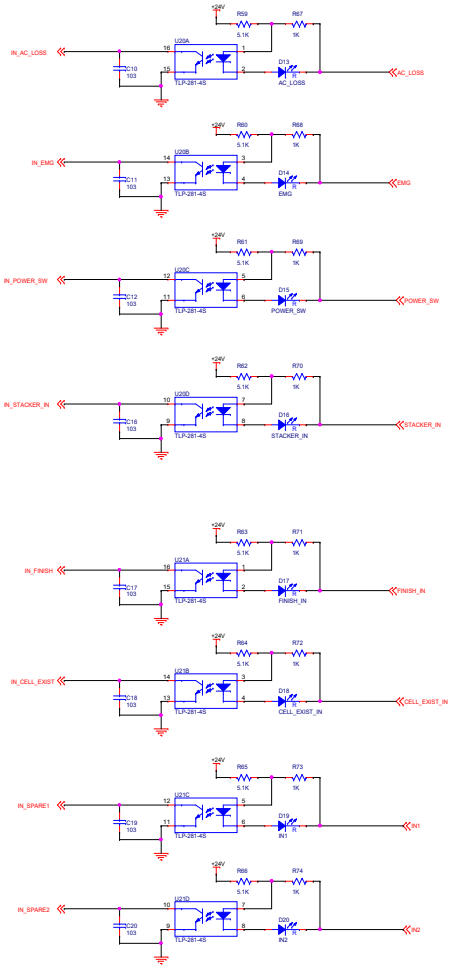
Input and Output Signals

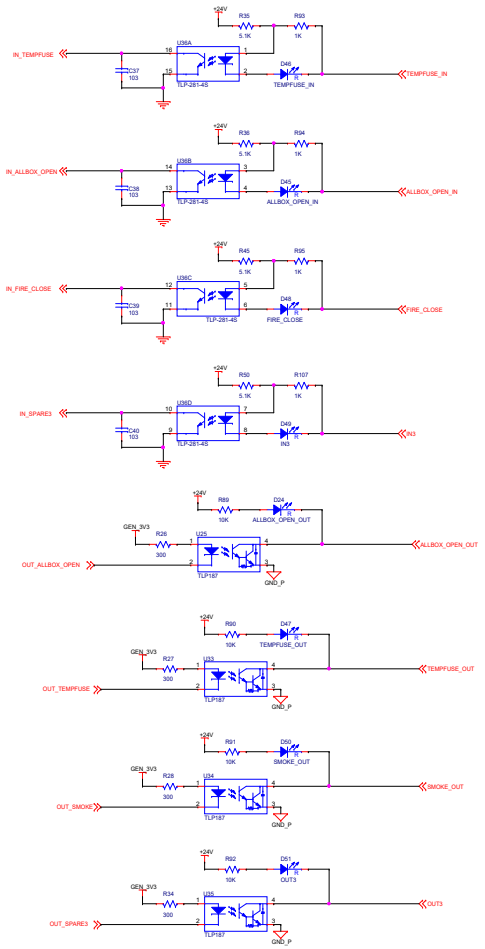












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