National Institute of Electronics & Information Technology

Ministry of Electronics and Information Technology,

GOVERNMENT OF INDIA

Certificate of Participation

This is to certify that Dr./Mr./ Ms./ **JOJI JOHN VARGHESE** has participated in 'Online Training on Embedded System Design using Swadeshi Processor on FPGA' organized by NIELIT CALICUT during 07th April 2021 to 12th April 2021, supported through C2SD (SMDP-III) Project.

IEP Coordinator

Executive Director

https://www.nielit.gov.in/calicut

Certificate No. **IEP-OLC 3486**

Date: 26thApril 2021