Project 1: "Logic Analyzer + Minimal Oscilloscope + Signal Generator" - Task Description

Task Description:

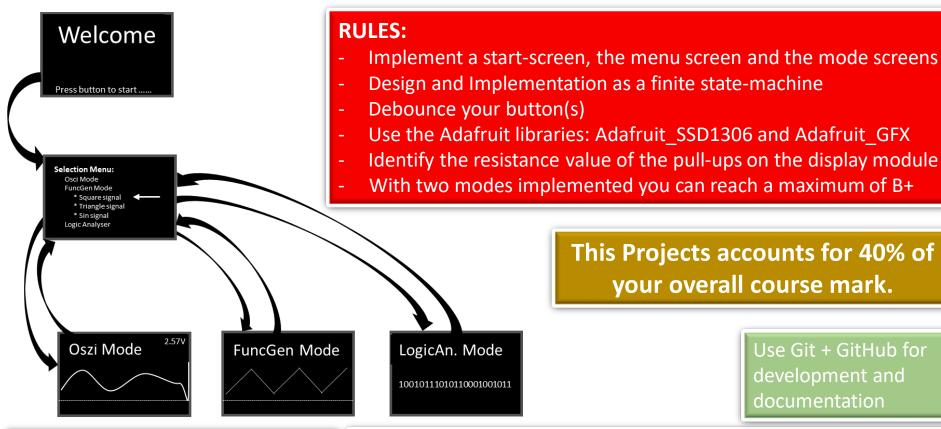
You are asked to implement an uC –based system that provides the following functionality:

- One channel oscilloscope (0V 3.3V) using the uC ADC peripheral
- One channel function generator using the uC DAC with the option to select between at least a square, triangle and sin signal)
- One channel logic analyzer, that allows for decoding of a 9600 baud serial frame.
- When acting in one of the above-mentioned modes, the relevant signals should be streamed out in "real-time" via UART(-> USB) to a connected PC and visualized using a serial plotter and in parallel displayed on the provided OLED display
- It should be possible to control the different functions(modes) via connected push-buttons and in parallel via commands send from the PC to the uC via USB(->UART)
- The system should be structured and implemented as a finite state machine

Breadboard + Jumper Wines

Buttons as needed

MG7013 Trimester 1 2020 Corona Version 128x64 OLED Display



This Projects accounts for 40% of your overall course mark.

> Use Git + GitHub for development and documentation

Questions: Slack or

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REQUIRED PROJECT OUTPUT:

- Working hardware prototype
- Video of yourself explaining the structure (code, hardware) and the correct operation of your system (incl. serial plot visualization)
- Project report, including
 - General project description (purpose, ...)
 - Describe your design process (incl. state transition diagram)
 - **Schematics**
 - Source Code (well commented)
 - Limitations of your project + possible future improvements
 - Appendix with relevant datasheets(if available)
 - Use of proper APA referencing