# 1. Description

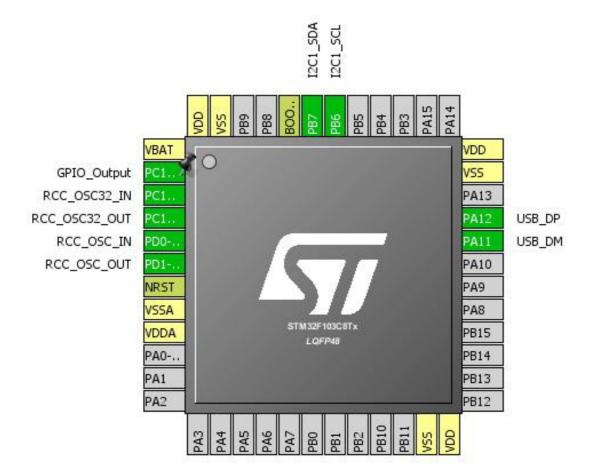
### 1.1. Project

| Project Name    | stm_cdc            |
|-----------------|--------------------|
| Board Name      | stm_cdc            |
| Generated with: | STM32CubeMX 4.24.0 |
| Date            | 03/10/2018         |

### 1.2. MCU

| MCU Series     | STM32F1       |
|----------------|---------------|
| MCU Line       | STM32F103     |
| MCU name       | STM32F103C8Tx |
| MCU Package    | LQFP48        |
| MCU Pin number | 48            |

## 2. Pinout Configuration

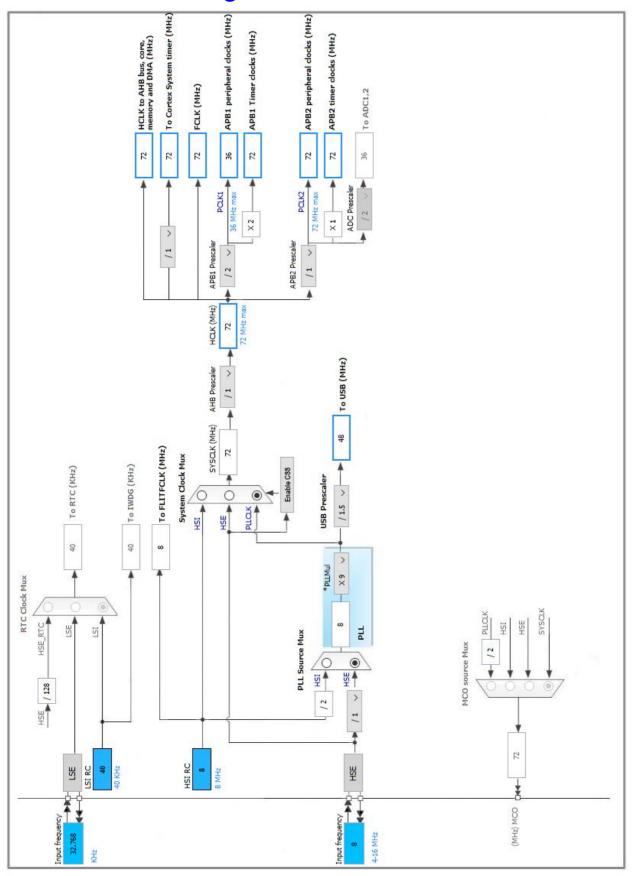


# 3. Pins Configuration

| Pin Number<br>LQFP48 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|-------|
| 1                    | VBAT                                  | Power    |                          |       |
| 2                    | PC13-TAMPER-RTC *                     | I/O      | GPIO_Output              |       |
| 3                    | PC14-OSC32_IN                         | I/O      | RCC_OSC32_IN             |       |
| 4                    | PC15-OSC32_OUT                        | I/O      | RCC_OSC32_OUT            |       |
| 5                    | PD0-OSC_IN                            | I/O      | RCC_OSC_IN               |       |
| 6                    | PD1-OSC_OUT                           | I/O      | RCC_OSC_OUT              |       |
| 7                    | NRST                                  | Reset    |                          |       |
| 8                    | VSSA                                  | Power    |                          |       |
| 9                    | VDDA                                  | Power    |                          |       |
| 23                   | VSS                                   | Power    |                          |       |
| 24                   | VDD                                   | Power    |                          |       |
| 32                   | PA11                                  | I/O      | USB_DM                   |       |
| 33                   | PA12                                  | I/O      | USB_DP                   |       |
| 35                   | VSS                                   | Power    |                          |       |
| 36                   | VDD                                   | Power    |                          |       |
| 42                   | PB6                                   | I/O      | I2C1_SCL                 |       |
| 43                   | PB7                                   | I/O      | I2C1_SDA                 |       |
| 44                   | воото                                 | Boot     |                          |       |
| 47                   | VSS                                   | Power    |                          |       |
| 48                   | VDD                                   | Power    |                          |       |

<sup>\*</sup> The pin is affected with an I/O function

## 4. Clock Tree Configuration



### 5. IPs and Middleware Configuration

#### 5.1. I2C1

**I2C: I2C** 

#### 5.1.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled
Primary Address Length selection 7-bit
Dual Address Acknowledged Disabled
Primary slave address 0
General Call address detection Disabled

#### 5.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

#### 5.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

#### 5.3. SYS

**Debug: No Debug** 

Timebase Source: SysTick

#### 5.4. USB

mode: Device (FS)

#### 5.4.1. Parameter Settings:

#### **Basic Parameters:**

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 8 Bytes

**Power Parameters:** 

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

#### 5.5. USB DEVICE

#### Class For FS IP: Communication Device Class (Virtual Port Com)

#### 5.5.1. Parameter Settings:

#### **Basic Parameters:**

USBD\_MAX\_NUM\_INTERFACES (Maximum number of supported interfaces)

USBD\_MAX\_NUM\_CONFIGURATION (Maximum number of supported configuration)

1

USBD\_MAX\_STR\_DESC\_SIZ (Maximum size for the string descriptors)

512

USBD\_SUPPORT\_USER\_STRING (Enable user string descriptor)

Disabled

USBD\_SELF\_POWERED (Enabled self power)

Enabled

USBD\_DEBUG\_LEVEL (USBD Debug Level) 0: No debug message

#### **Class Parameters:**

USB CDC Rx Buffer Size 1000
USB CDC Tx Buffer Size 1000

#### 5.5.2. Device Descriptor:

#### **Device Descriptor:**

VID (Vendor IDentifier) 1155

LANGID\_STRING (Language Identifier)

English(United States)

MANUFACTURER\_STRING (Manufacturer Identifier) STMicroelectronics

**Device Descriptor FS:** 

PID (Product IDentifier) 22336

PRODUCT\_STRING (Product Identifier) STM32 Virtual ComPort

SERIALNUMBER\_STRING (Serial number) 0000000001A
CONFIGURATION\_STRING (Configuration Identifier) CDC Config
INTERFACE\_STRING (Interface Identifier) CDC Interface

<sup>\*</sup> User modified value

## 6. System Configuration

### 6.1. GPIO configuration

| IP   | Pin                     | Signal            | GPIO mode                     | GPIO pull/up pull<br>down | Max<br>Speed | User Label |
|------|-------------------------|-------------------|-------------------------------|---------------------------|--------------|------------|
| I2C1 | PB6                     | I2C1_SCL          | Alternate Function Open Drain | n/a                       | High *       |            |
|      | PB7                     | I2C1_SDA          | Alternate Function Open Drain | n/a                       | High *       |            |
| RCC  | PC14-<br>OSC32_IN       | RCC_OSC32_IN      | n/a                           | n/a                       | n/a          |            |
|      | PC15-<br>OSC32_OU<br>T  | RCC_OSC32_O<br>UT | n/a                           | n/a                       | n/a          |            |
|      | PD0-<br>OSC_IN          | RCC_OSC_IN        | n/a                           | n/a                       | n/a          |            |
|      | PD1-<br>OSC_OUT         | RCC_OSC_OUT       | n/a                           | n/a                       | n/a          |            |
| USB  | PA11                    | USB_DM            | n/a                           | n/a                       | n/a          |            |
|      | PA12                    | USB_DP            | n/a                           | n/a                       | n/a          |            |
| GPIO | PC13-<br>TAMPER-<br>RTC | GPIO_Output       | Output Push Pull              | n/a                       | Low          |            |

### 6.2. DMA configuration

nothing configured in DMA service

### 6.3. NVIC configuration

| Interrupt Table                         | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt                  | true   | 0                    | 0           |
| Hard fault interrupt                    | true   | 0                    | 0           |
| Memory management fault                 | true   | 0                    | 0           |
| Prefetch fault, memory access fault     | true   | 0                    | 0           |
| Undefined instruction or illegal state  | true   | 0                    | 0           |
| System service call via SWI instruction | true   | 0                    | 0           |
| Debug monitor                           | true   | 0                    | 0           |
| Pendable request for system service     | true   | 0                    | 0           |
| System tick timer                       | true   | 0                    | 0           |
| USB low priority or CAN RX0 interrupts  | true   | 0                    | 0           |
| PVD interrupt through EXTI line 16      | unused |                      |             |
| Flash global interrupt                  | unused |                      |             |
| RCC global interrupt                    | unused |                      |             |
| USB high priority or CAN TX interrupts  | unused |                      |             |
| I2C1 event interrupt                    | unused |                      |             |
| I2C1 error interrupt                    | unused |                      |             |

<sup>\*</sup> User modified value

# 7. Power Consumption Calculator report

#### 7.1. Microcontroller Selection

| Series    | STM32F1       |
|-----------|---------------|
| Line      | STM32F103     |
| MCU       | STM32F103C8Tx |
| Datasheet | 13587 Rev17   |

#### 7.2. Parameter Selection

| Temperature | 25  |
|-------------|-----|
| Vdd         | 3.3 |

## 8. Software Project

### 8.1. Project Settings

| Name                              | Value                                     |
|-----------------------------------|---|
| Project Name                      | stm_cdc                                   |
| Project Folder                    | C:\Users\Jomon Thomas\Desktop\DIY\stm_cdc |
| Toolchain / IDE                   | MDK-ARM V5                                |
| Firmware Package Name and Version | STM32Cube FW_F1 V1.6.0                    |

### 8.2. Code Generation Settings

| Name  | Value   |
|---|---|
| STM32Cube Firmware Library Package                            | Copy all used libraries into the project folder |
| Generate peripheral initialization as a pair of '.c/.h' files | No  |
| Backup previously generated files when re-generating          | No  |
| Delete previously generated files when not re-generated       | Yes   |
| Set all free pins as analog (to optimize the power            | No  |
| consumption)  |   |

## 9. Software Pack Report