

led[3:0]

clk

clr

rom_inst[7:0]

btn[1:0]

PS[3:0]

R0

R1

R2

R3

100 ns

200 ns

300 ns

400 ns

500 ns

600 ns

X

0

00000101

00010100

00100011

00110010

11011..

00

10

00

0

7

8

9

10

8

9

10

8

9

10

8

9

10

8

X

0

5

X

0

4

X

0

3

X

0

2